



Chapter 18.

NONIDEAL MOS

Sung June Kim

kimsj@snu.ac.kr

<http://helios.snu.ac.kr>



CONTENTS

- Metal-Semiconductor Work function Difference
- Oxide Charges
- MOSFET Threshold Considerations



Physics of Non-ideal MOS-C

//
“ real ”

① $\phi_{ms} \neq 0$

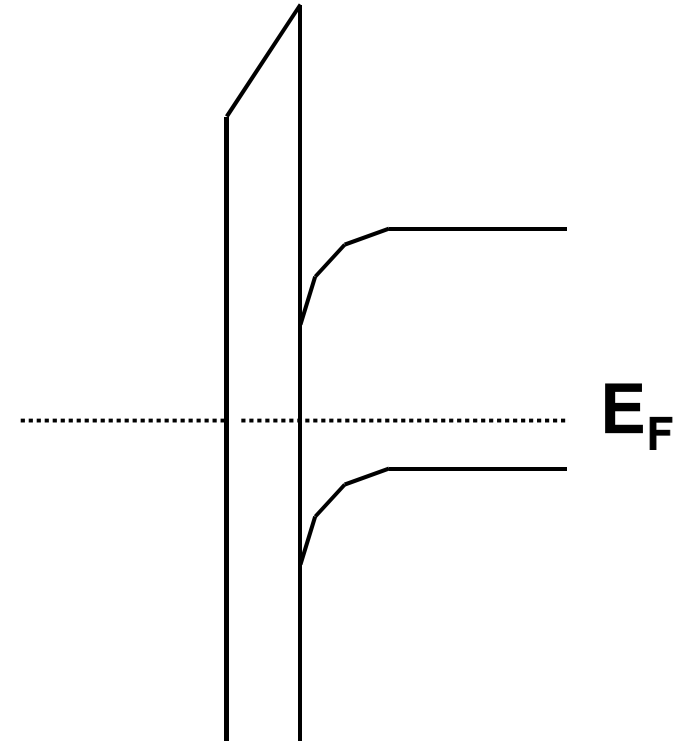
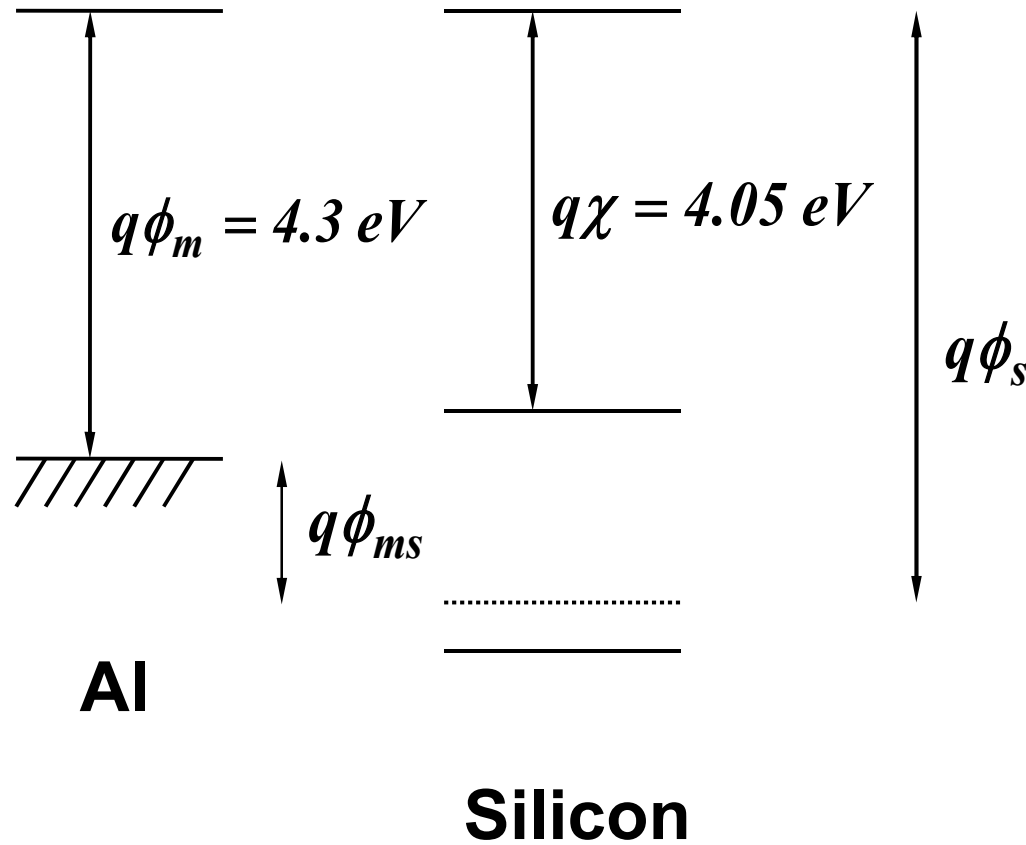
② charges exist in SiO_2



Workfunction Difference

- Workfunction :
 - the minimum energy required to bring an electron from the Fermi level to the vacuum level.
 - for Al , $q\phi_m = 4.3 eV$.
- Electron affinity :
 - the energy difference between conduction band edges of the semiconductor and the vacuum level.
 - for silicon, $q\chi = 4.05 eV$.





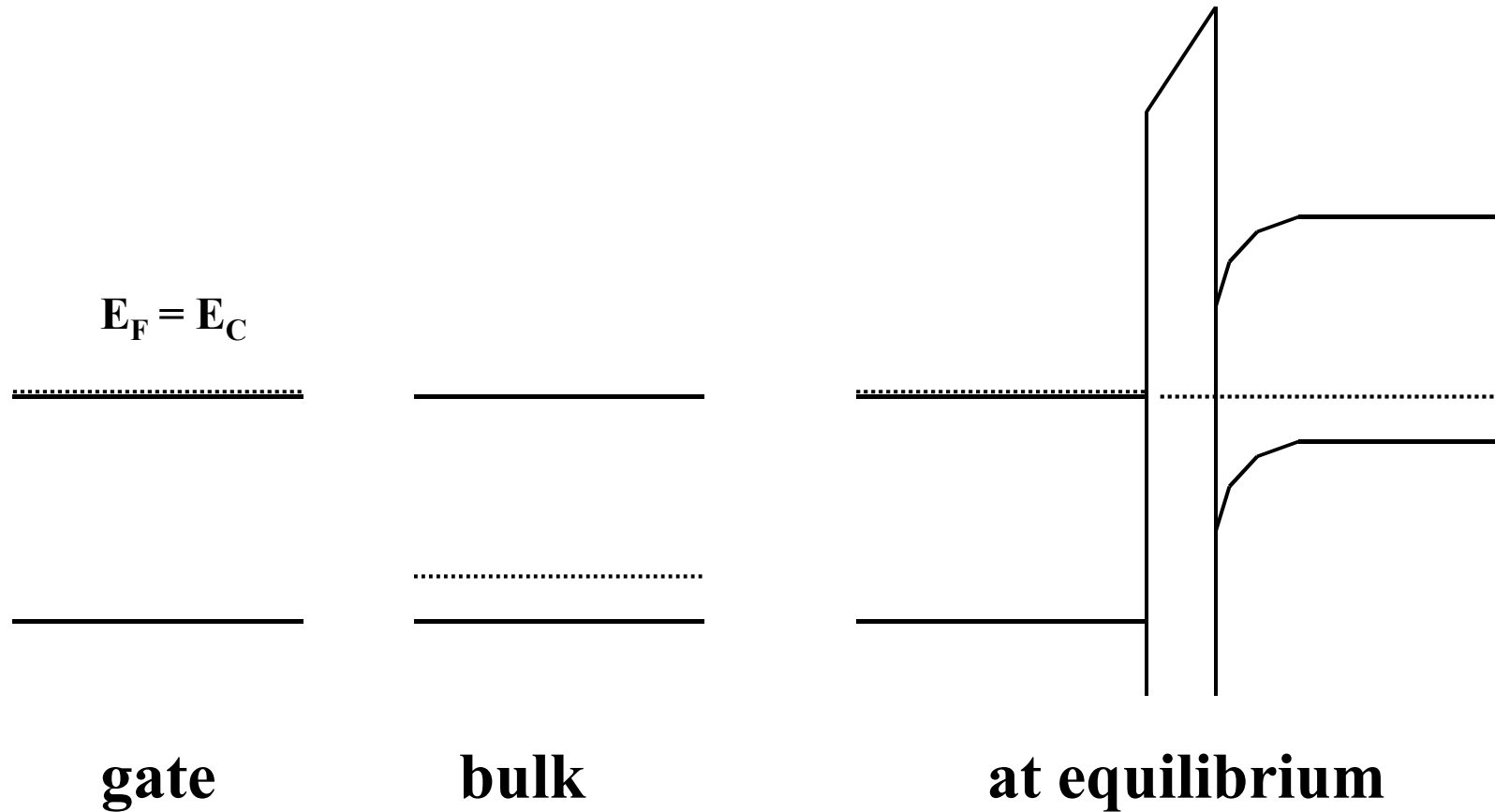
Equilibrium

$$\phi_m \neq \phi_s$$

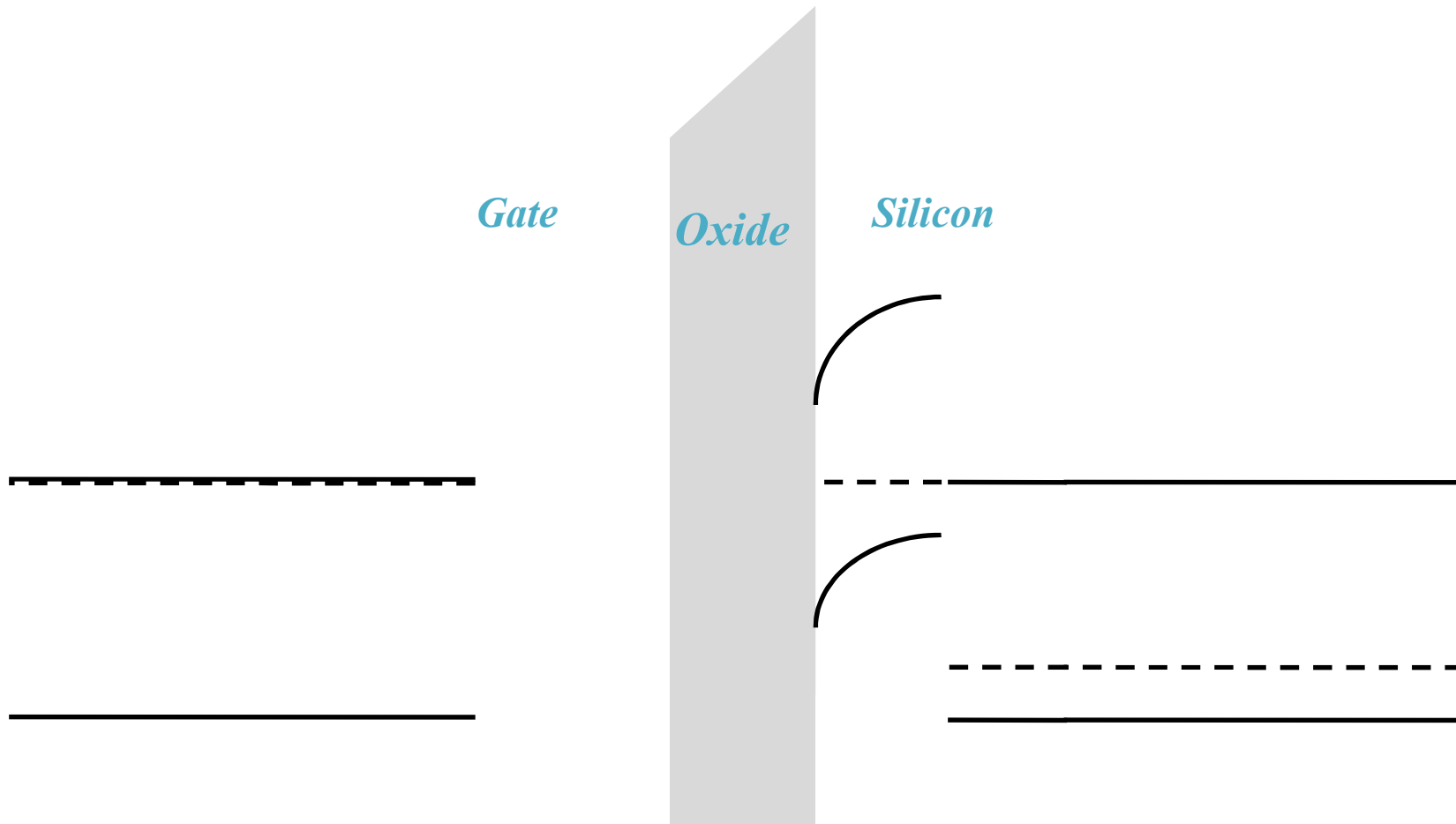
$$V_{FB} = \phi_{ms}$$



Heavily-doped polysilicon gate



Heavily-doped polysilicon gate



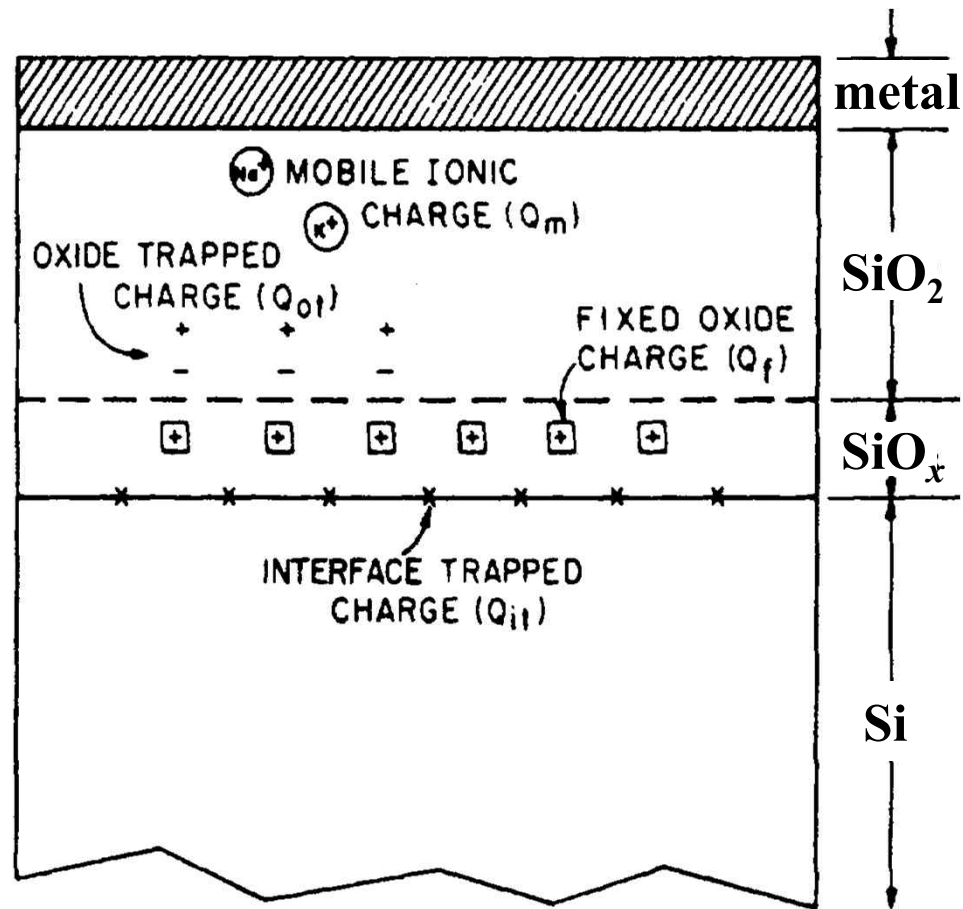
$$\begin{aligned}\phi_{ms} &= -E_g / 2 - \phi_F \\ &= -E_g / 2 - \frac{kT}{q} \ln\left(\frac{N_A}{n_i}\right) \quad \text{약 } -900 \text{ mV}\end{aligned}$$

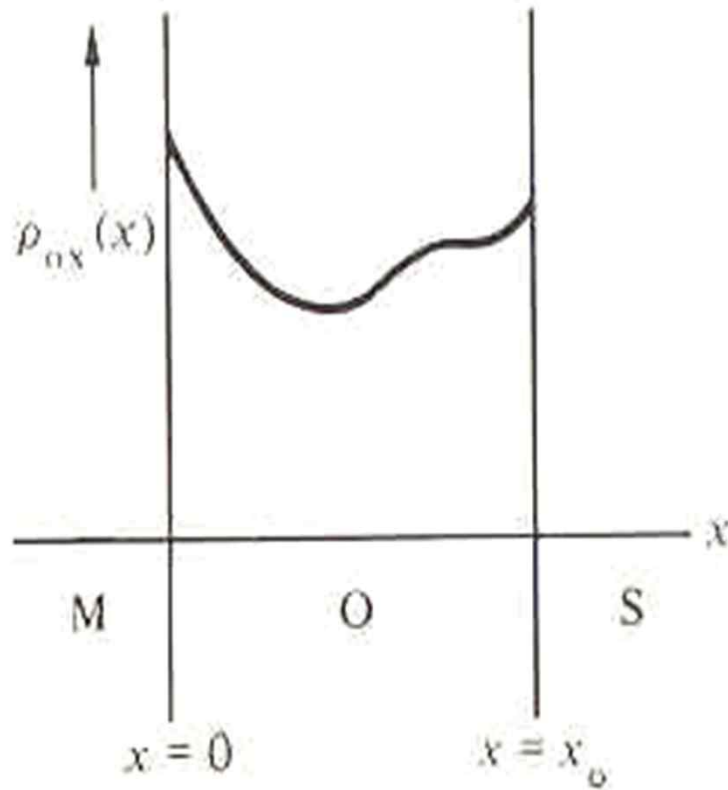
\uparrow $\sim 560\text{mV}$ \uparrow $\sim 350\text{mV}$



Oxide Charges and Traps

- Q_{tot} : total oxide charges (per unit area)





$$\Delta V_T = -\frac{1}{K_o \epsilon_o} \int_0^{x_o} x \rho_{ox}(x) dx$$

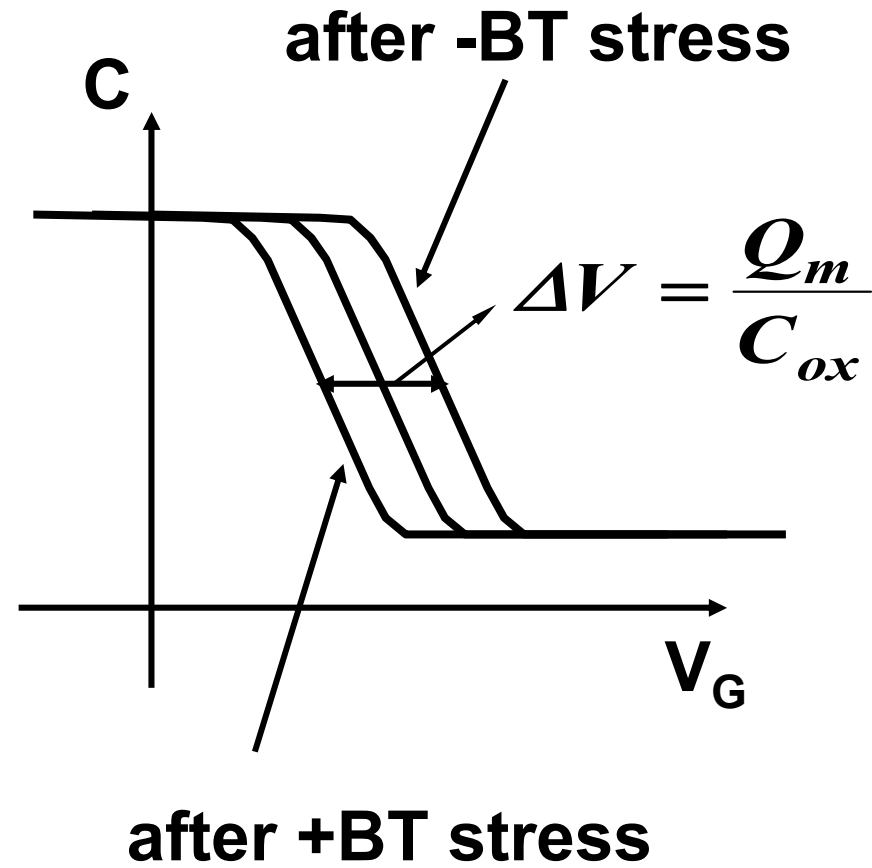
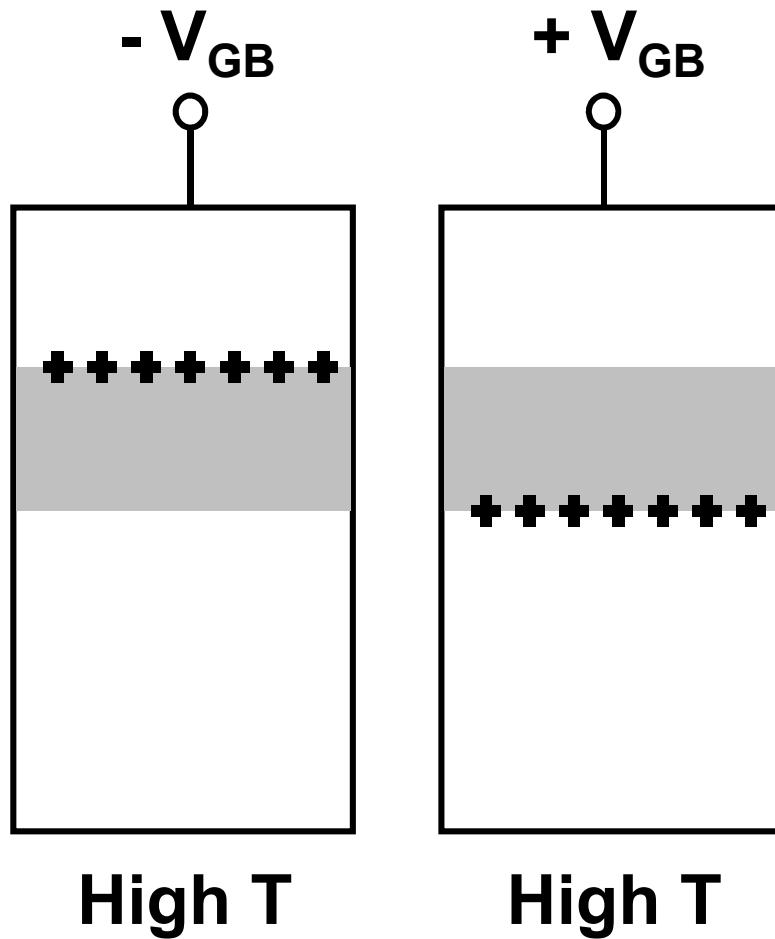


Mobile Ionic Charges

- Na^+ , K^+
- Very high diffusivities in the oxide even below $200\text{ }^\circ\text{C}$
 - V_T instability



- Test : MOS-C CV



Fixed Oxide Charge

1. Located very close to the interface
2. Associated with the structure of the interfacial between Si and SiO_2
3. Function of substrate orientation, oxidation temperature (anneal condition)



$$(111) > (110) > (100)$$

$$\uparrow$$

$$1 \times 10^{10} \text{ cm}^{-2}$$

\therefore (100) wafers are used for MOS IC

$$\Delta V_T = \frac{-Q_f}{C_{ox}} = \frac{-qN_f}{C_{ox}}$$

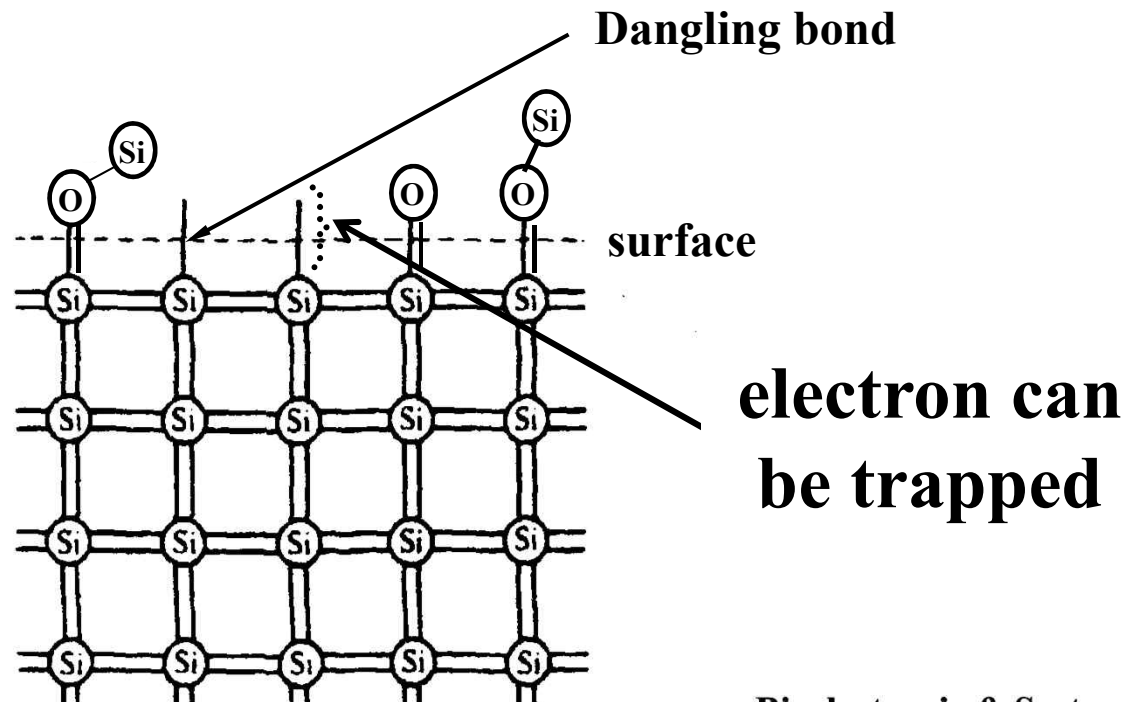
$$(100) \quad t_{ox} = 50 \text{ \AA} \quad C_{ox} = \frac{K_{ox}\epsilon_0}{t_{ox}} = \frac{3.9 \times 8.854 \times 10^{-14} \text{ F/cm}}{50 \times 10^{-8} \text{ cm}} = 690 \text{ nF/cm}^2$$

$$\Delta V_T = - \frac{(1.6 \times 10^{-19} \text{ C})(1 \times 10^{10} \text{ cm}^{-2})}{690 \times 10^{-9} \text{ F/cm}^2} = 2.5 \text{ mV}$$

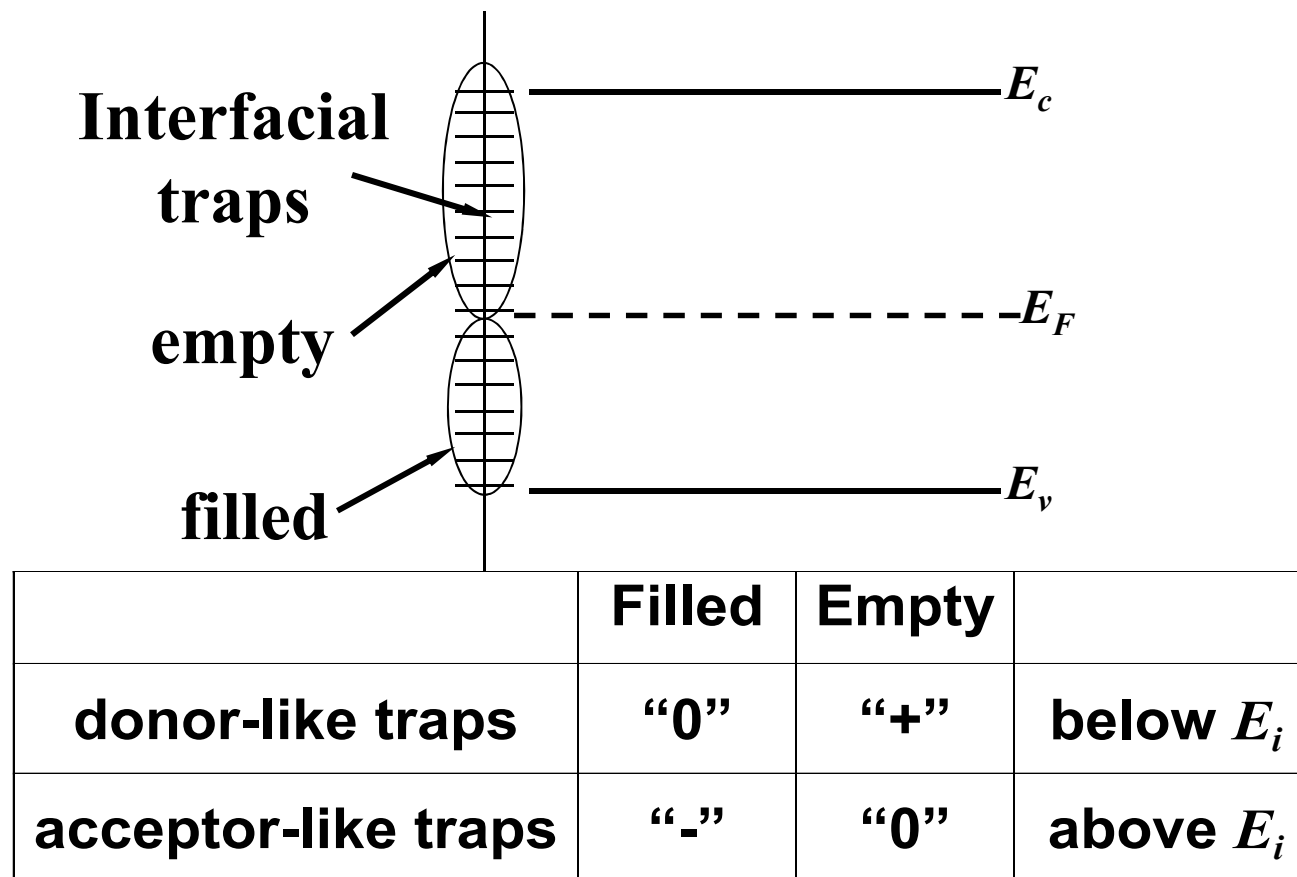


Interface Trapped Charge

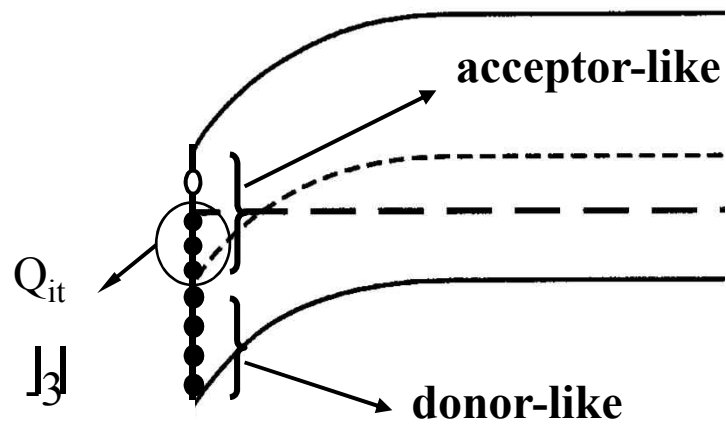
1. Arises from allowed energy states in the forbidden gap of the Si, very close to Si-SiO₂ interface.
2. Physical origin unknown yet.
→ “dangling bond” (?)



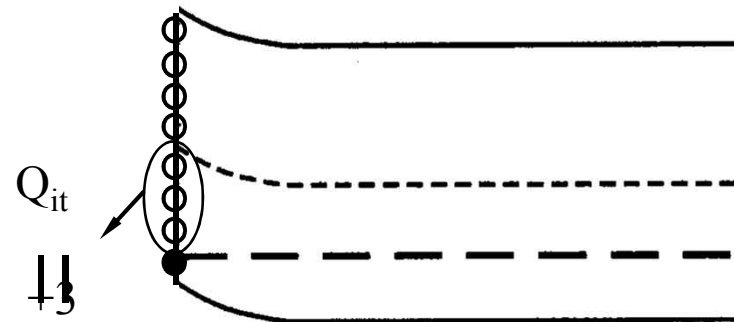
3. The surface state acts like a recombination center.
4. Function of $V_{GB} \rightarrow$ 가장 나쁜 charge.
5. Distributed throughout the entire band gap



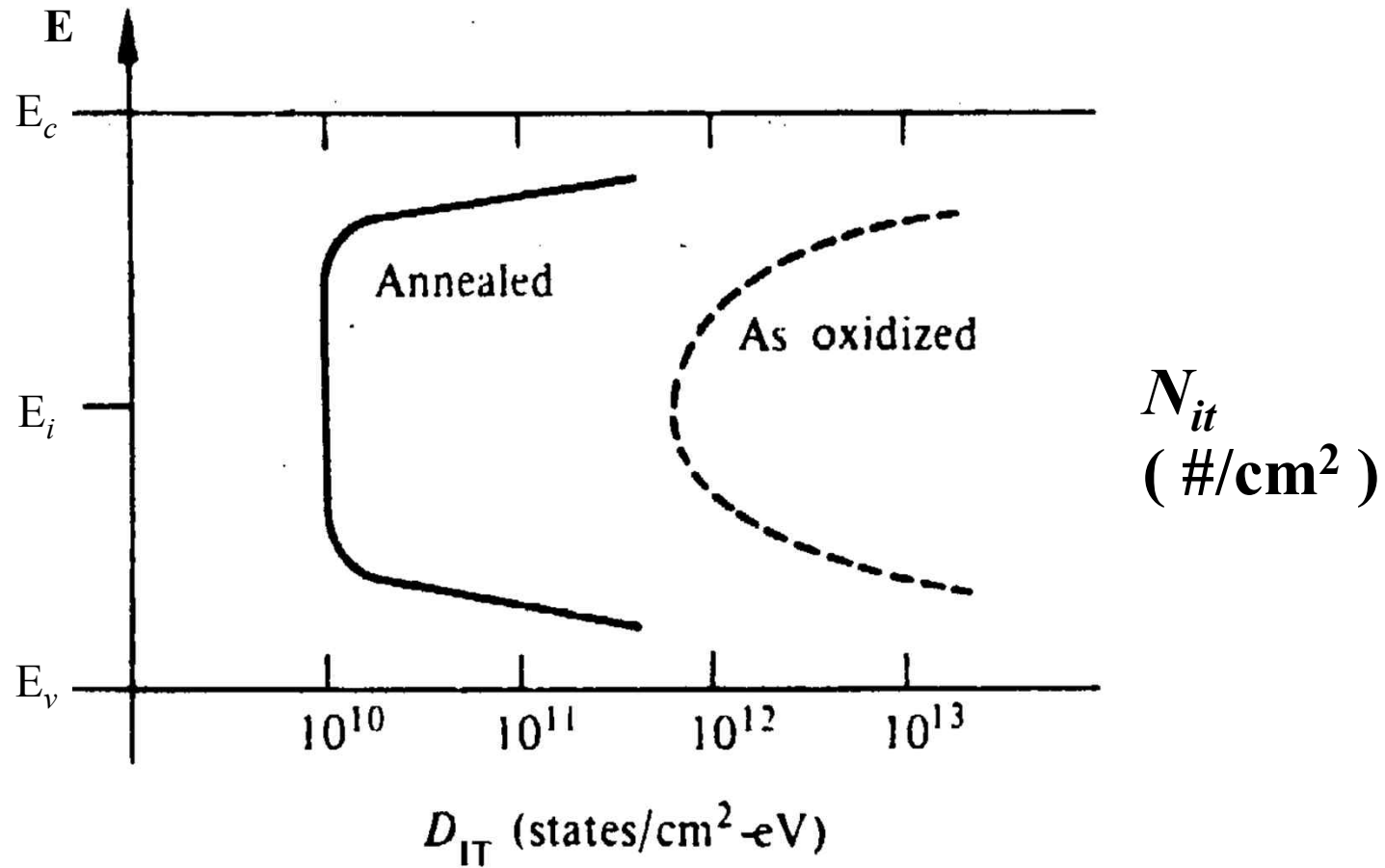
- inversion :
mostly filled



- accumulation :
mostly empty



6. Distribution of trap level vs. energy

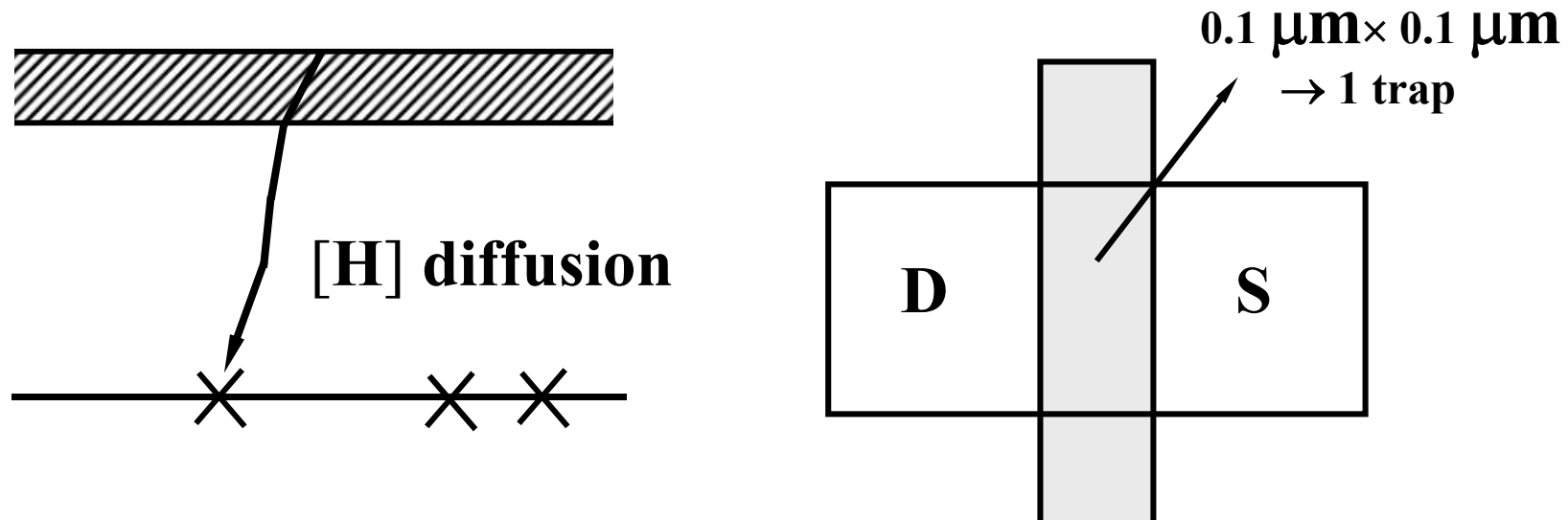


7. (111)



8. Annealing with hydrogen at relatively low temperature ($\leq 500\text{ }^{\circ}\text{C}$) minimize D_{it} .

$$10^{10} \text{ traps/cm}^2 \rightarrow 100 \text{ traps}/\mu\text{m}^2$$



Oxide Trapped Charge Q_{ot}

- Due to holes and electrons trapped in the bulk of oxide.



Effect of Oxide Charge on V_T

$$\Delta V_T = -\frac{Q_{tot}}{C_{ox}} \quad (\text{If } Q_{tot} \text{ at the interface})$$



Threshold Voltage Equation

- Flat band voltage $\equiv V_{FB} \equiv \phi_{ms} - \frac{Q_{tot}}{C_{ox}}$

$$V_T = V_{FB} + 2\phi_F + \frac{\sqrt{4K_s \epsilon_0 q N_A \phi_F}}{C_{ox}} \sim 0.6 \text{ V}$$

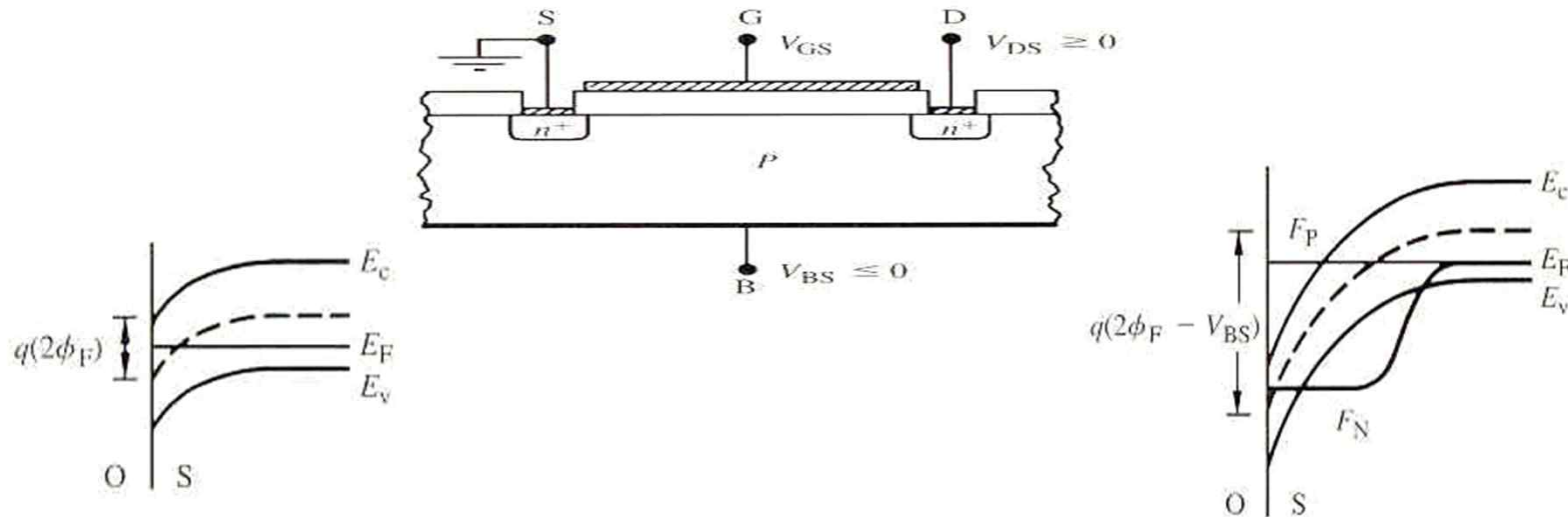
↑
potential drop in Si

↙
potential drop in oxide



Back Biasing

- Body effect: Reverse biasing the back contact relative to the source $\Rightarrow V_T$ change



If $V_{BS} = 0$, inversion occurs
when $\phi_s = 2\phi_F$

If $V_{BS} < 0$, inversion layer carriers
migrate laterally into the S/D. The
inversion occurs when $\phi_s = 2\phi_F + |V_{BS}|$.
The maximum depletion width

$$W_T = \sqrt{\frac{2K_s \epsilon_o (2\phi_F + |V_{BS}|)}{qN_A}}$$



- At threshold,

$$V_{GB} = V_{FB} + 2\phi_F + |V_{BS}| + \frac{\sqrt{2qK_s\epsilon_0N_A(2\phi_F + |V_{BS}|)}}{C_{ox}}$$

$$\begin{aligned} \therefore V_{GS} &= V_{GB} + V_{BS} = V_{FB} + 2\phi_F + \frac{\sqrt{2qK_s\epsilon_0N_A(2\phi_F + |V_{BS}|)}}{C_{ox}} \\ &\parallel \\ &V_T \\ &= V_T(V_{BS} = 0) + \underbrace{\frac{\sqrt{2qK_s\epsilon_0N_A}}{C_{ox}} \left(\sqrt{2\phi_F + |V_{BS}|} - \sqrt{2\phi_F} \right)}_{\text{Body Factor}} \end{aligned}$$

11/25/2010

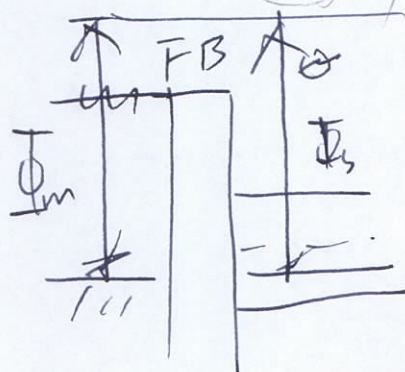
Ch10
Non-Ideal MOS

1. ϕ_{ms} : workfunction difference.

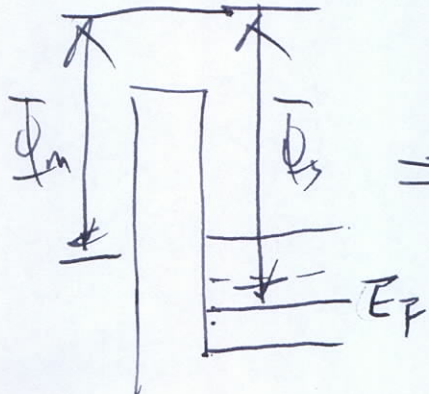
energy

$$\phi_{ms} = \frac{1}{q} (\Phi_M - \Phi_S) =$$

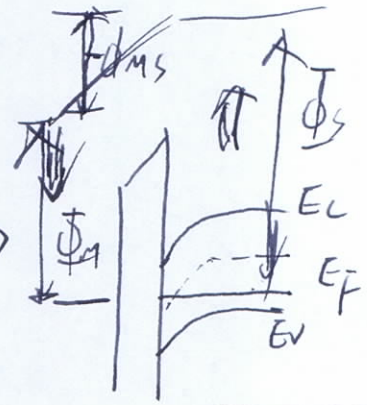
VTS.



(1) when $\phi_{ms} = 0$

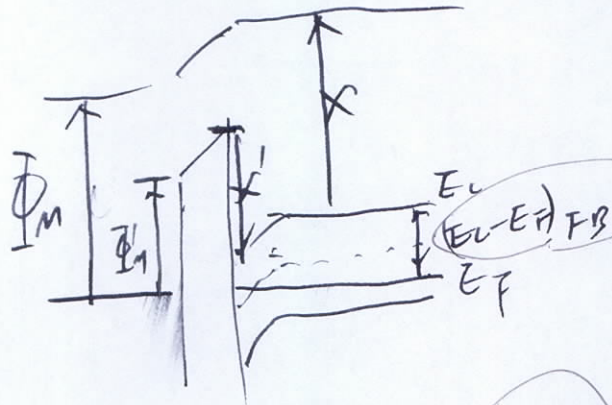


$\phi_m < \phi_s$



$$\Delta V_{FB} = +\phi_{ms} \left(\frac{q}{kT} \right)$$

Required for FB.

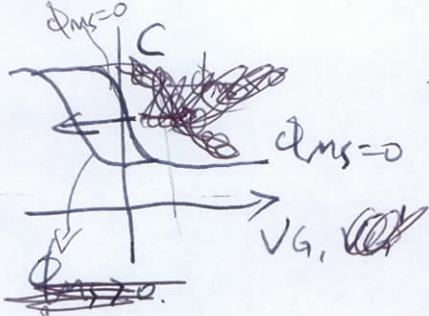


$$\Phi'_M + q\Delta\phi_{ox} = (E_C - E_F)_{FB} - q\psi_s + X'$$

$$V_{bi} = -(\phi_s + \Delta\phi_{ox}) = \phi_{ms}$$

(vacuum level of SiO2)

$$\phi_{ms} = \frac{1}{q} (\Phi_M - \Phi_S) = \frac{1}{q} (\Phi'_M - X' - (E_C - E_F)_{FB})$$



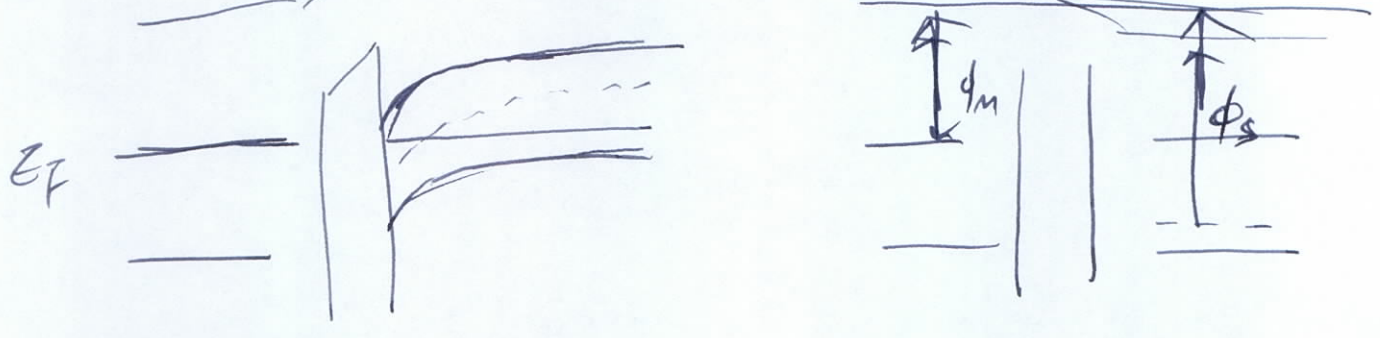
ΔV_{G1} to the same C or ϕ_s

$$\Delta V_{G1} = \phi_{ms}$$

$\phi_{ms} < 0$ (if $V_G = 0$ only or if inversion of ϕ_s is $\frac{q}{kT}$)

Refer Table 12.1
or 12.3. Mostly $\phi_{ms} < 0$

If Metal = heavily doped polysilicon



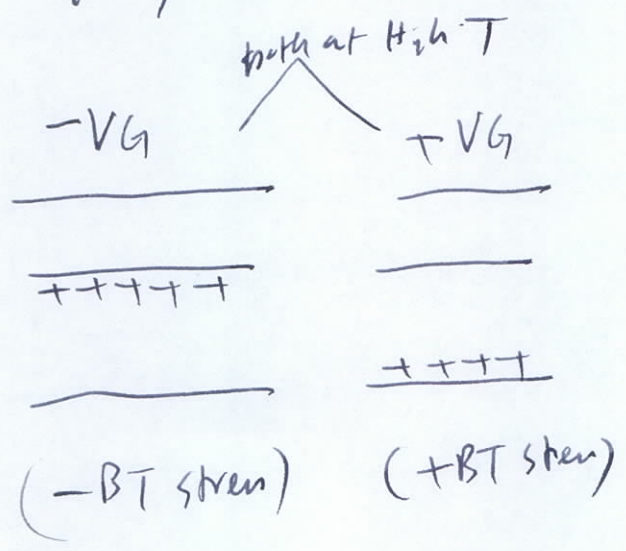
$$\phi_{MS} = -E_g/2 - \phi_F$$

$$= -\frac{E_g}{2} - \frac{kT}{q} \ln\left(\frac{N_A}{n_i}\right) \approx \underline{-900\text{ mV}}$$

$\sim 50\text{ mV} \quad \sim 300\text{ mV}$

2. Mobile charges in oxide

K⁺, Na⁺ ions - moving around



$$\Delta V_G = - \frac{1}{K_0 \epsilon_0} \int_0^{x_0} x \rho_{ox}(x) dx$$

↑ ↑ ↑ ↑ ↑

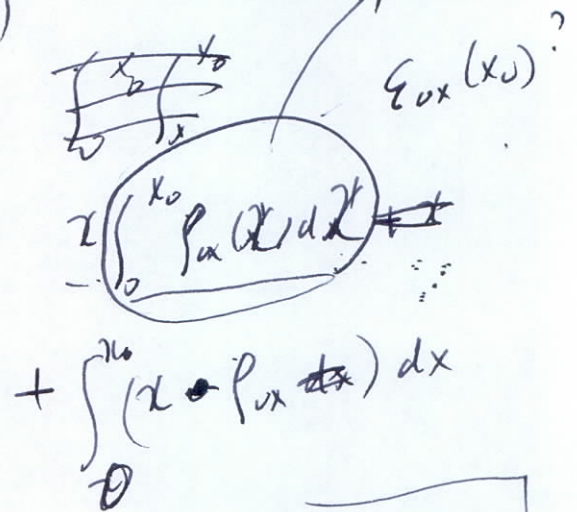
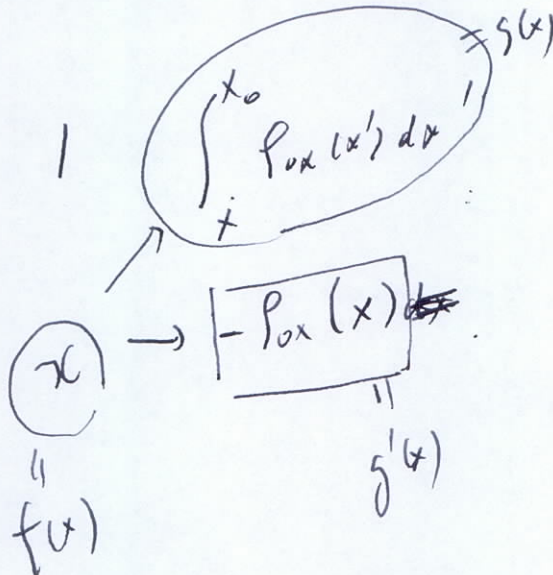
$$\int f'(x) g(x) dx = \int (f(x) g(x))' dx - \int f(x) g'(x) dx$$

$$= f(x) g(x) - \int f(x) g'(x) dx$$

$$\int_0^{x_0} \int_x^{x_0} \rho_{ox}(x') dx' dx \neq \int_0^{x_0} x \rho_{ox}(x) dx$$

~~$\int_0^{x_0} \rho_{ox}(x) dx$~~

~~$\int_0^{x_0} x \rho_{ox}(x) dx$~~
 (if $b = 2 \text{ yin}$) $(x=0 \text{ yin})$
 $(\epsilon_{ox}(0) - \epsilon_{ox}(x_0)) k_0 \epsilon_0$
 $k_0 \epsilon_0 \epsilon_{ox}$



$$= \cancel{x \epsilon_{ox}(x_0)} + \int_0^{x_0} x \rho dx$$

$$x (\epsilon_{ox}(0) - \epsilon_{ox}(x_0)) k_0 \epsilon_0 + \int_0^{x_0} x \rho dx$$

$$\therefore \Delta \phi_{ox} = x_0 \epsilon_{ox}(x_0) - \frac{1}{k_0 \epsilon_0} \left[x (\epsilon_{ox}(0) - \epsilon_{ox}(x_0)) k_0 \epsilon_0 + \int_0^{x_0} x \rho dx \right]$$

$$= \int_0^{x_0} x \rho dx //$$

$\frac{1}{2} \left(\frac{2}{3} \frac{2}{3} \frac{2}{3} \frac{2}{3} \frac{2}{3} \frac{2}{3} \frac{2}{3} \frac{2}{3} \frac{2}{3} \frac{2}{3} \right)$ (charge at ~~Top~~ Top or Bottom of oxide) }
 or using Delta Function in oxide

$$\Delta V_G (\text{Mobile ions after } +BT) = -\frac{1}{K_0 \epsilon_0} \int_0^{x_0} x Q_M \delta(x_0) dx$$

$$= -\frac{x_0}{K_0 \epsilon_0} Q_M = -\frac{Q_M}{C_0}$$

$\frac{2}{3} \frac{2}{3} \frac{2}{3}$
 $\sim -10V$
 to set $Q_M \sim 2 \times 10^{12} \text{ cm}^{-2} \times 6$
 1.6×10^{-19}
 $\frac{3.9 \times 60}{10^{-5}}$
 $C_0 = \frac{6.9 \times 10^{-19}}{\text{cm}^2}$
 for ~~oxide~~ oxide
 0.1 μm^2
 $2 \times 10^{12} \times 1.6 \times 10^{-19}$
 6.9×10^{-8}
 $\frac{3.9 \times 10^{-19}}{6.9}$
 $=$

$$\Delta V_G (-BT) = -\frac{1}{K_0 \epsilon_0} \int_0^{x_0} x Q_M \delta(x_0) dx = 0$$

Solution: phosphorus Gettering.

Use PSG \rightarrow this binds Mobile ions
 NOT to move around
Under control!
 thin layer of at top.

3. Fixed oxide charge

Answer:

3 Fixed oxide charge

associated structure ^{at} Si-SiO₂ interface

111 > 110 > 110
 $\sim 10^{10} \text{ cm}^{-2}$
 $\Delta V_T = -\frac{Q_f}{C_{ox}} = -\frac{q N_f}{C_{ox}}$

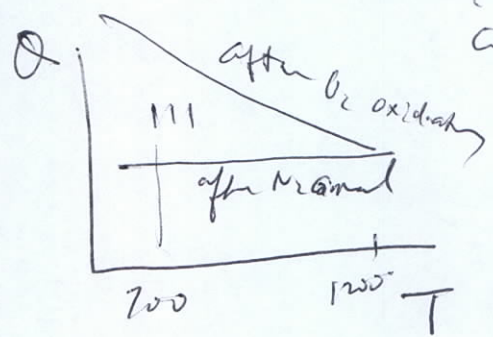
Due to major oxidizing reaction at the Si-SiO₂ interface

50 Å, C_{ox} = 690 nF/cm²

$\Delta V_T = 2.5 \text{ mV}$

Due to excess ionic silicon broken away from the silicon surface, and \rightarrow ready to react in the vicinity of Si-SiO₂ interface when the oxidation process is stopped. removes excess reaction components, SiO_x, Si.

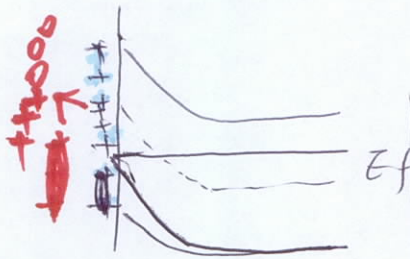
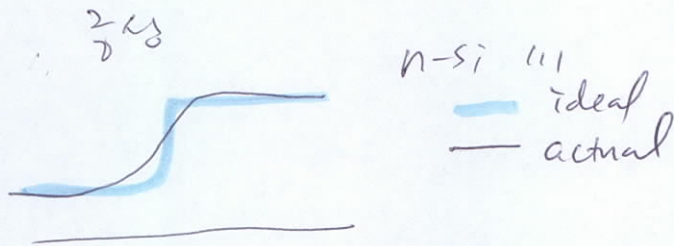
Solution. N₂ anneal at High Temp. \rightarrow removes



4. Interface Trapped charge

Origin ~ dangling bond at Si-SiO₂ surface

act like recomb centers (distributed throughout bandgap)
 fn vs Vg



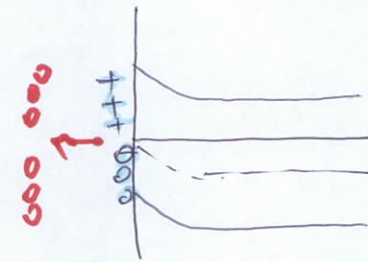
Inversion

$V_G < 0$

① Donor-like state ξ_i

\Rightarrow P_{0i} vs V_G min

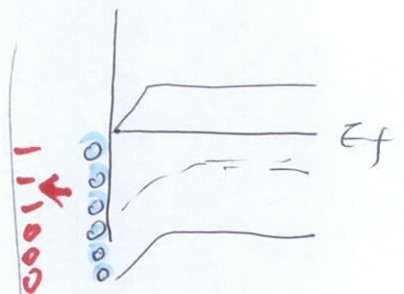
\rightarrow -1/2 slope Data at $V_G = 0$



depletion

(filled 0 charge, Empty + charge)

이영역의 특징



Accumulation

$V_G > 0$

이영역의 특징

② Modify Model

E_i above Donor-like
 E_i below Acceptor-like

(filled - charge, Empty 0 charge)

이영역의 특징! Model-1의 Data의 $\alpha = 1/2$ 인 것

\Rightarrow 이영역의 V_G 특징 (-) 특징

이영역의 특징

이영역의 (+) 특징 특징

이 Data의 특징

Solution is H₂ anneal (passivation)
 ϵ_c TCC ϵ_c 10¹⁰ 10¹² \rightarrow Dit

overall

~~V_T~~

$$V_T = V_{FB} + \cancel{2\phi_F} + \phi_s$$

where $V_T' = 2\phi_F + \Delta\phi_{ox}$

$$\phi_{ms} - \frac{Q_{tot}}{C_{ox}}$$

all oxide charges

$$\Delta\phi_{ox} = \frac{k_s x_o}{k_o} \sqrt{\frac{q N_D}{k_s \epsilon_0}} \phi_F$$

to ~~n-ch~~
n-ch

$$- \frac{k_s x_o}{k_o} \sqrt{\frac{q N_D}{k_s \epsilon_0}} (-\phi_F)$$

to p-ch

Back Bias

$$V_{BS} < 0$$

This makes it harder to ~~inv~~ reach inversion by $(-V_{BS})$

So now $2\phi_F$ is replaced by $(2\phi_F - V_{BS})$

V_T adjust by
Implantation

Boron-implantation

Charge introduced Q_1

then $\Delta V_G = - \frac{Q_1}{C_o}$

(Donor $\frac{1}{2}$ Q_{ox})

12/7

ch 18 Non ideal MOS devices

+ ch 19 Modern FET

~~12/11~~

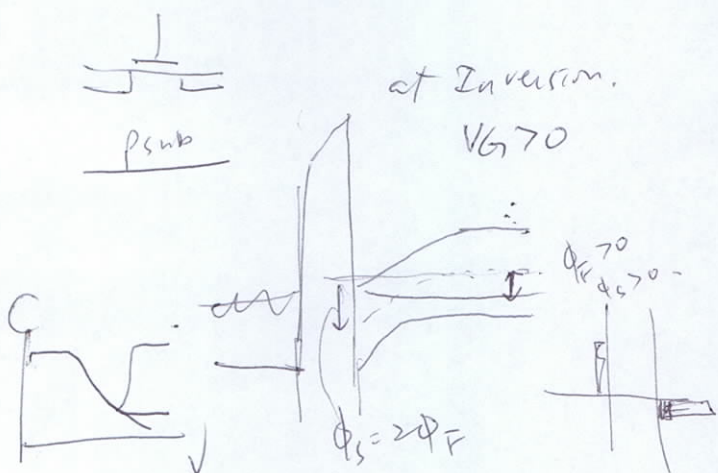
12/9 n/2M3 Lecture + Q/A.

12/14 W3321 Fab Tour

12/16 1) 2) 12.

Review

NMOS (on p-Si sub)



$$V_T = V_{FB} + V_T'$$

$$V_{FB} = \phi_{ms} - \frac{Q_{tot}}{C_{ox}}$$

Fixed charge
mobile charge
interface trap.
charge

$$V_T' = 2\phi_F + \Delta\phi_x$$

$$+ \frac{k_s x_o}{k_o} \sqrt{\frac{48 N_A}{k_s \epsilon_o} \phi_F}$$

$$\phi_F = \frac{KT}{q} \ln(N_A/n_i)$$

PMOS (on p-Si sub)



(P569)

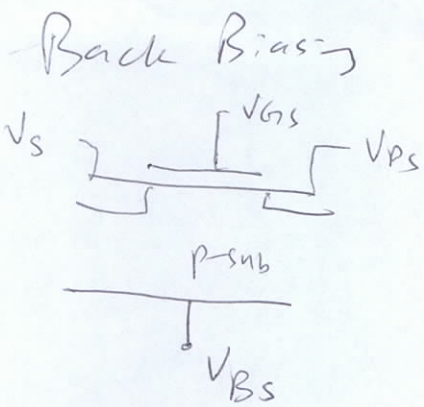
$$V_T = V_{FB} + V_T'$$

$$V_{FB} = \phi_{ms} - \frac{Q_{tot}}{C_{ox}}$$

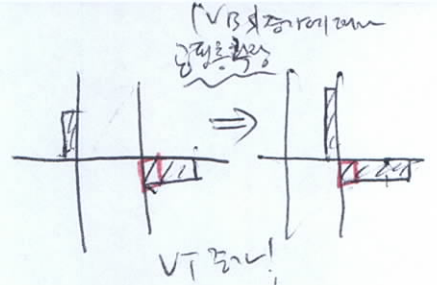
negative, but less negative than in p-sub. P049

$$V_T' = 2\phi_F - \frac{k_s x_o}{k_o} \sqrt{\frac{48 N_A}{k_s \epsilon_o} (-\phi_F)}$$

$$\phi_F = -\frac{KT}{q} \ln\left(\frac{N_D}{n_i}\right)$$



nMOS
 $V_{BS} < 0$



This makes it harder to reach inversion by $(-V_{BS})$ also depletion region widens

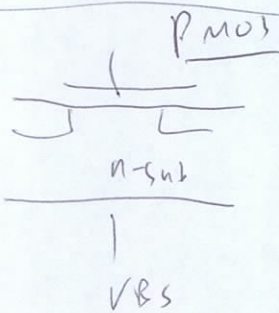
So now $2\phi_F$ is replaced by $(2\phi_F - V_{BS})$

So $V_T = V_{FB} + V_T'$

$V_{FB} = \phi_{ms} - \frac{Q_{ox}}{C_{ox}}$

$V_{T'(nMOS)} = (2\phi_F - V_{BS}) + \frac{k_s \epsilon_0 \sqrt{2q N_A}}{k_0 \sqrt{k_s \epsilon_0}} (2\phi_F - V_{BS})$

Becomes more positive.



$V_{BS} > 0$ replaced by $(2\phi_F - V_{BS})$, becomes more negative

$V_{FB} = \phi_{ms} - \frac{Q_{ox}}{C_{ox}}$

$V_{T'(pMOS)} = (2\phi_F - V_{BS})$

$(pMOS) - \frac{k_s \epsilon_0}{k_0} \sqrt{\frac{2q N_D}{k_s \epsilon_0}} (2\phi_F + V_{BS})$

V_T adjustment by Γ^2

Barrier into nMOS holes
 positive charge induced Q_i

$\Delta V_T = - \frac{Q_i}{C_0}$