Shared Counters and Parallelism

Companion slides for The Art of Multiprocessor Programming by Maurice Herlihy & Nir Shavit

A Shared Pool

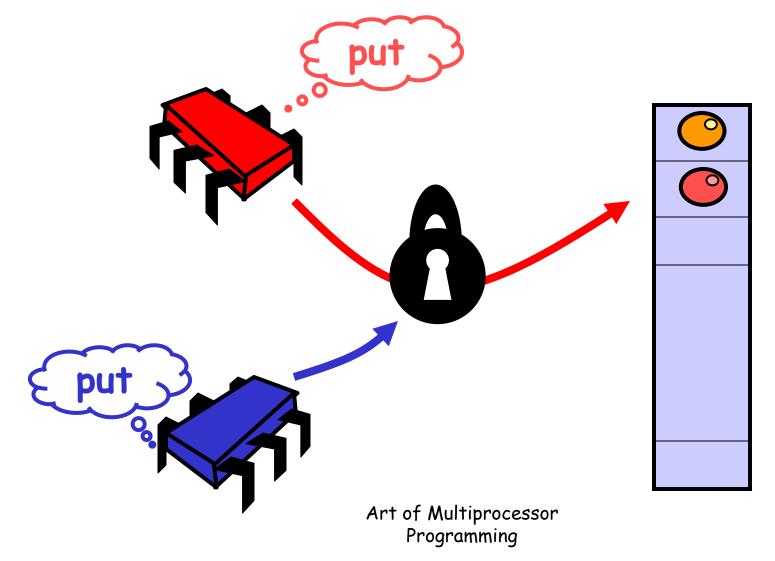
```
public interface Pool {
   public void put(Object x);
   public Object remove();
}
```

Unordered set of objects

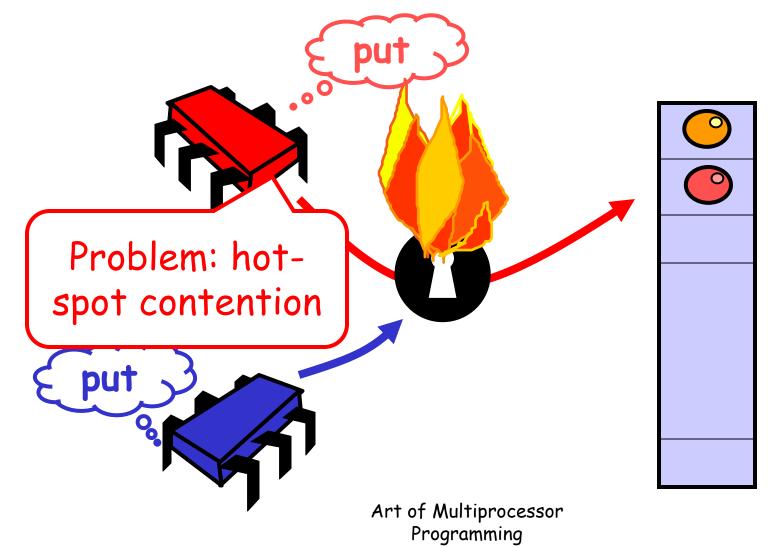
- Put
 - Inserts object
 - blocks if full

- Remove
 - Removes & returns an object
 - blocks if empty

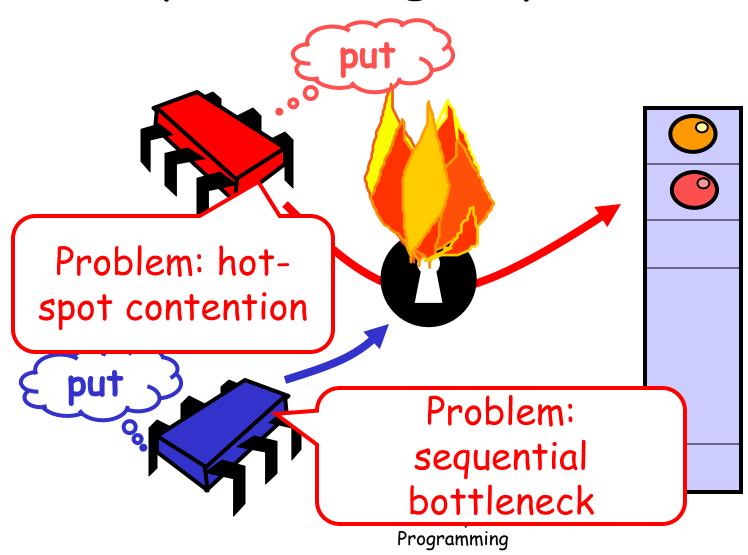
Simple Locking Implementation

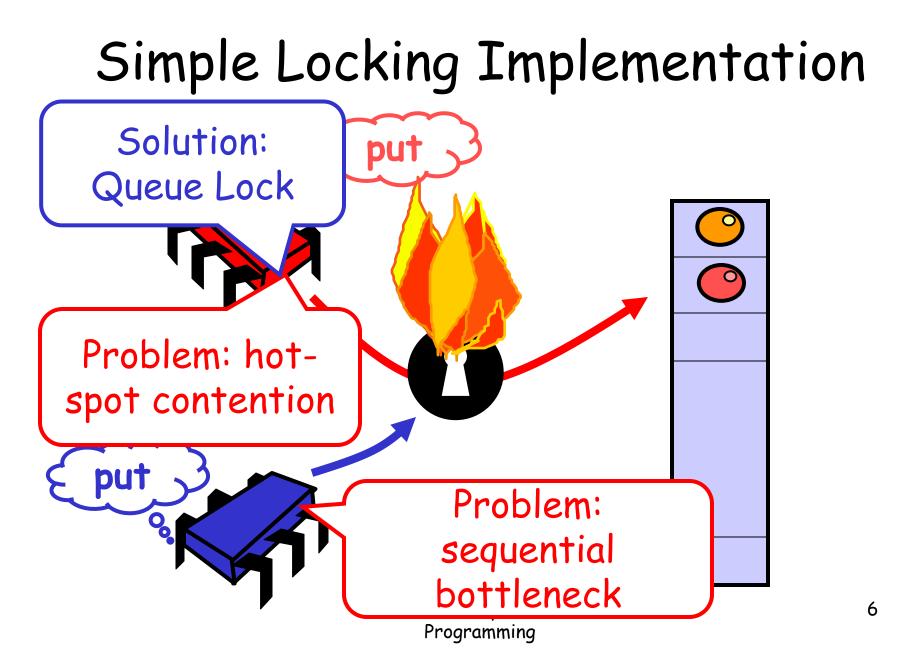


Simple Locking Implementation



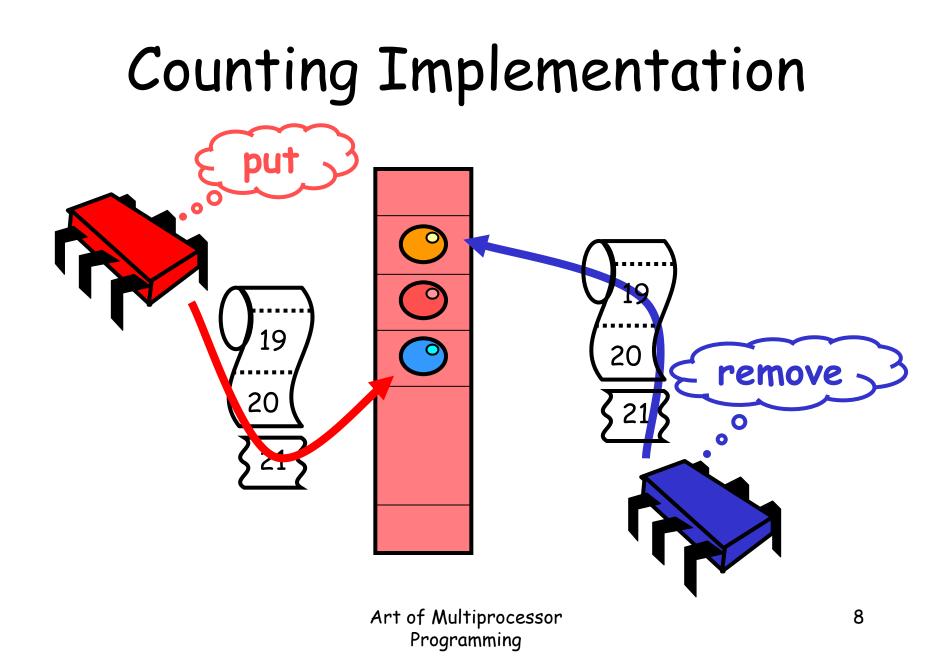
Simple Locking Implementation

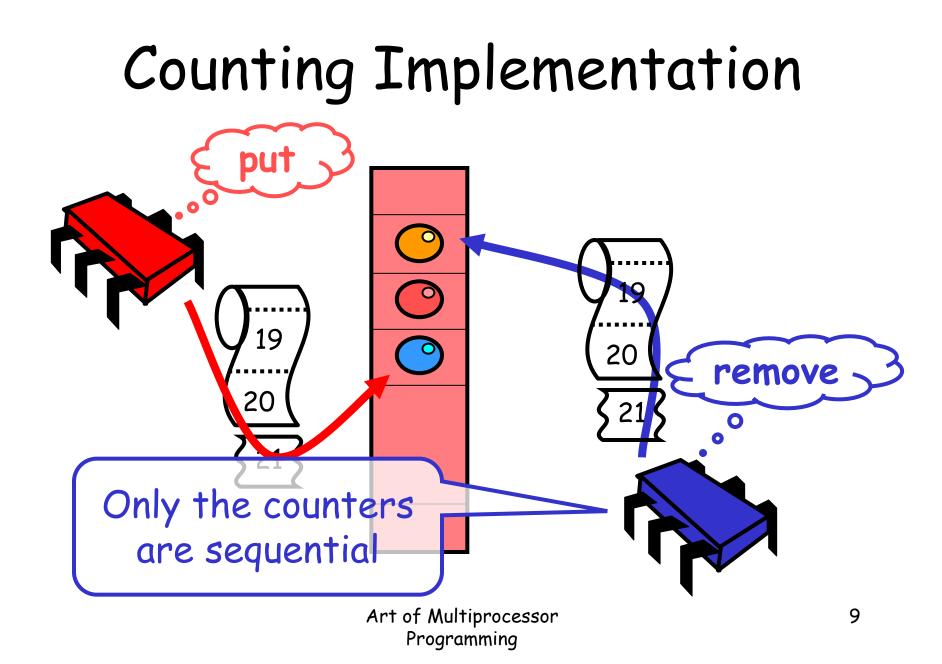




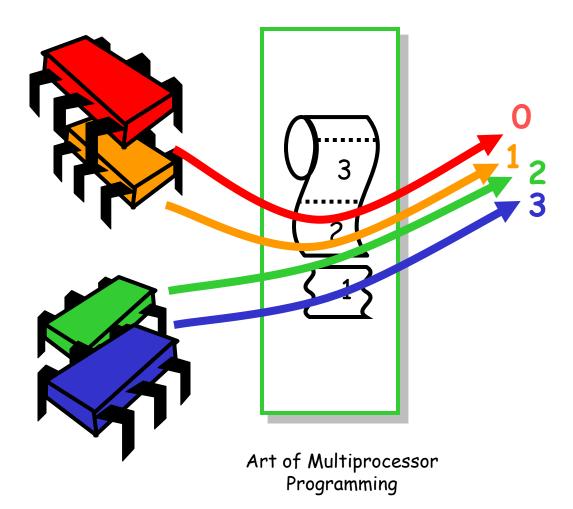
Simple Locking Implementation Solution: put Queue Lock Problem: hotspot contention put **Problem:** sequential Solution bottleneck ??? Programming

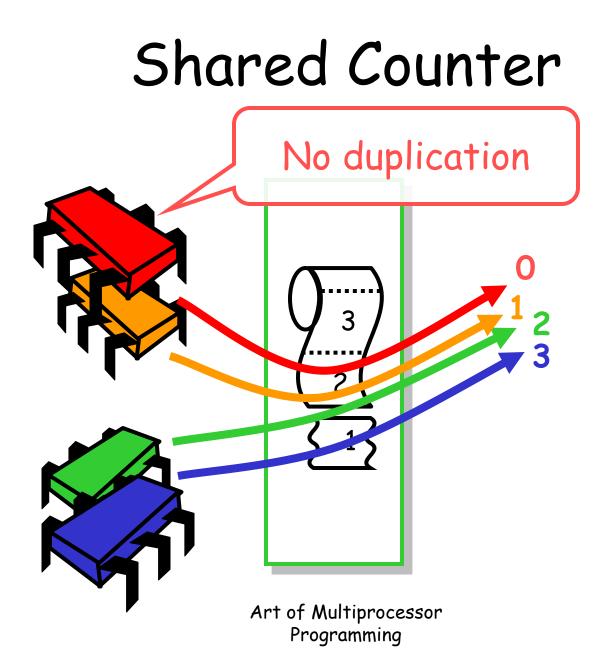
7

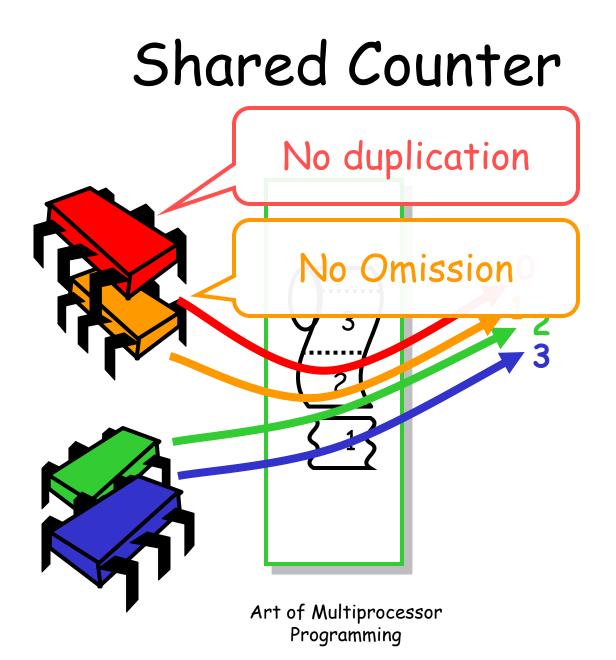


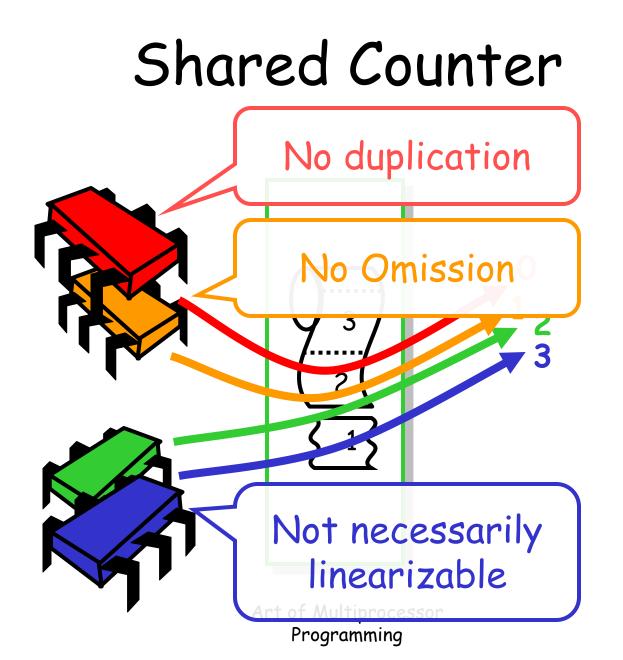


Shared Counter



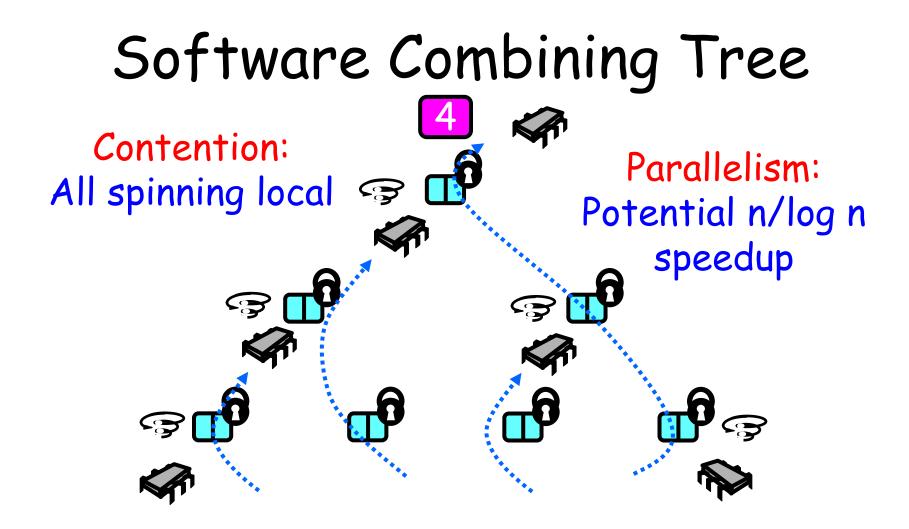


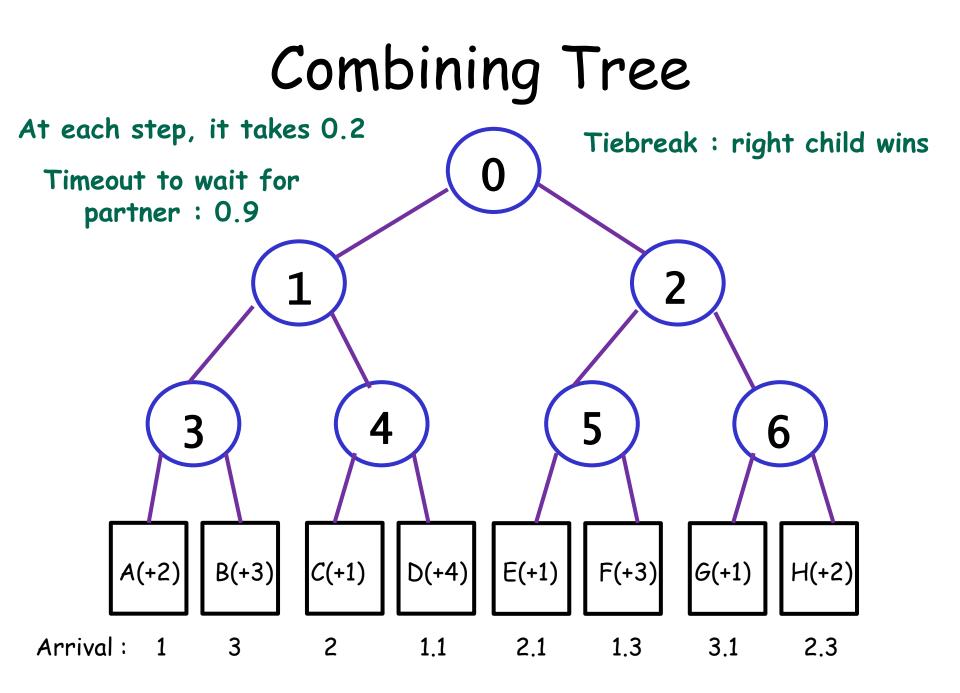


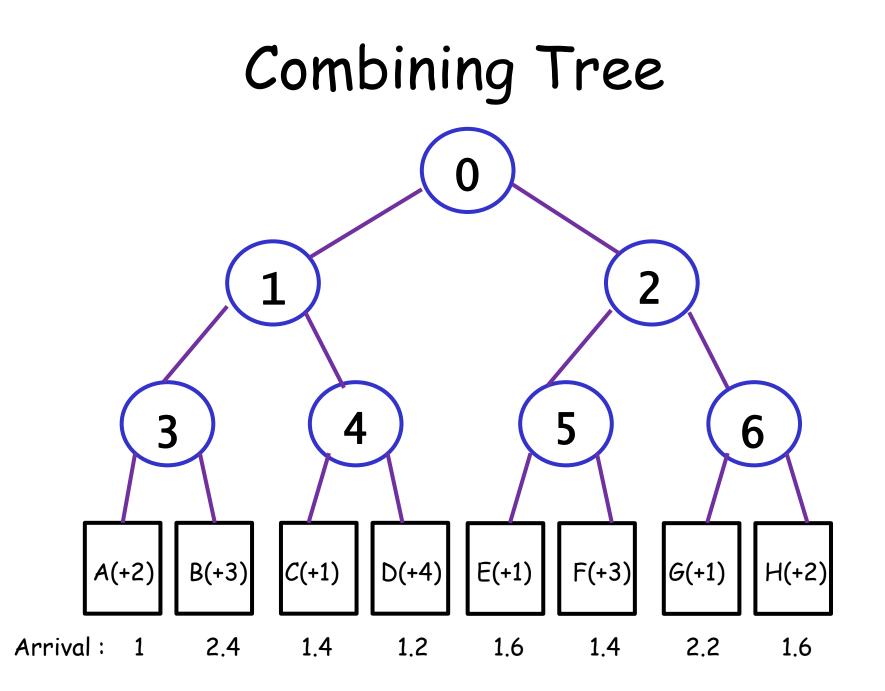


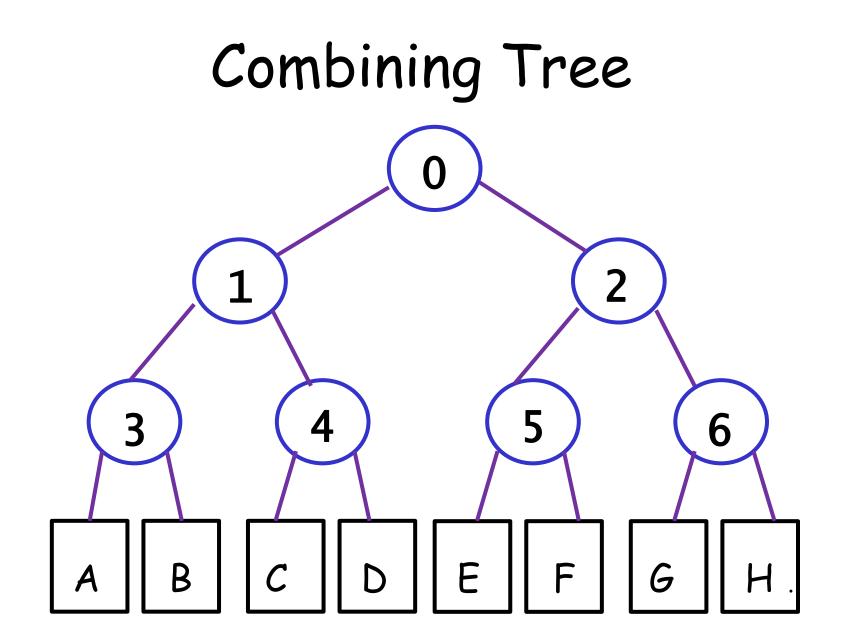
Shared Counters

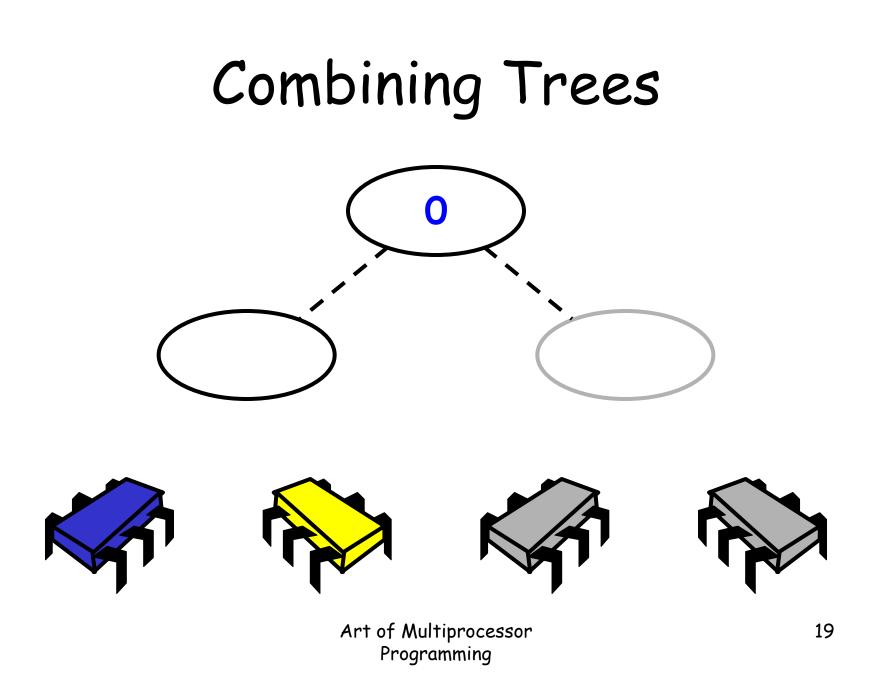
- Can we build a shared counter with
 - Low memory contention, and
 - Real parallelism?
- Locking
 - Can use queue locks to reduce contention
 - No help with parallelism issue ...

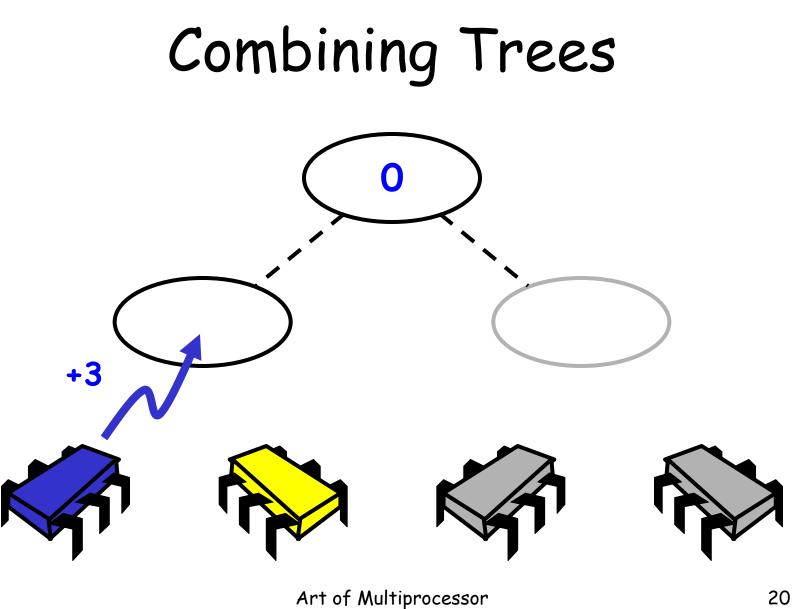




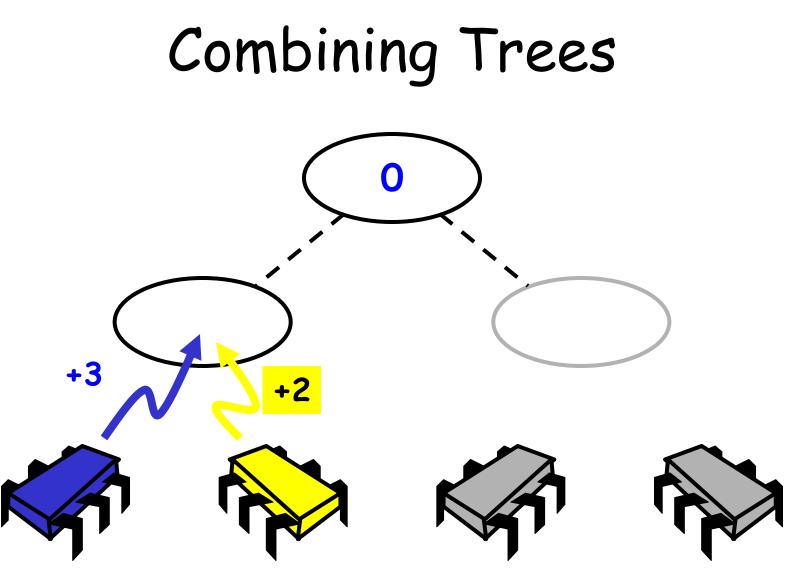


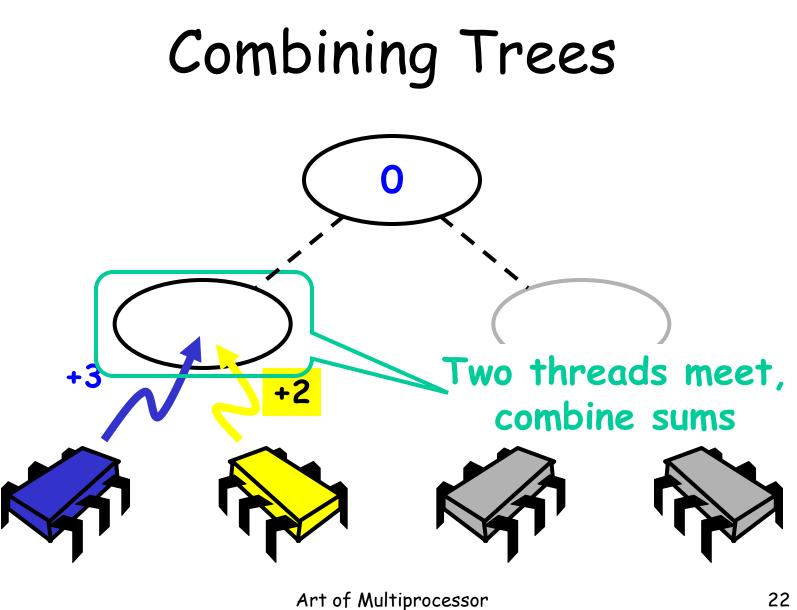


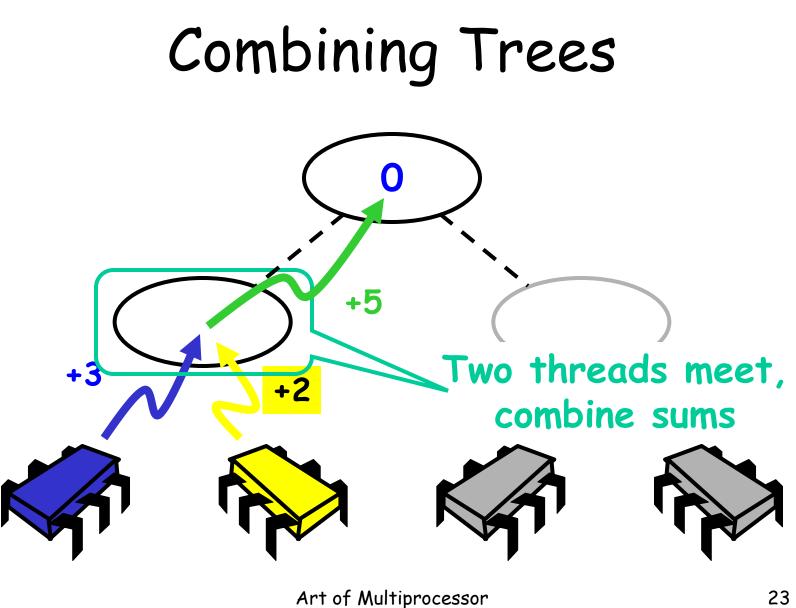




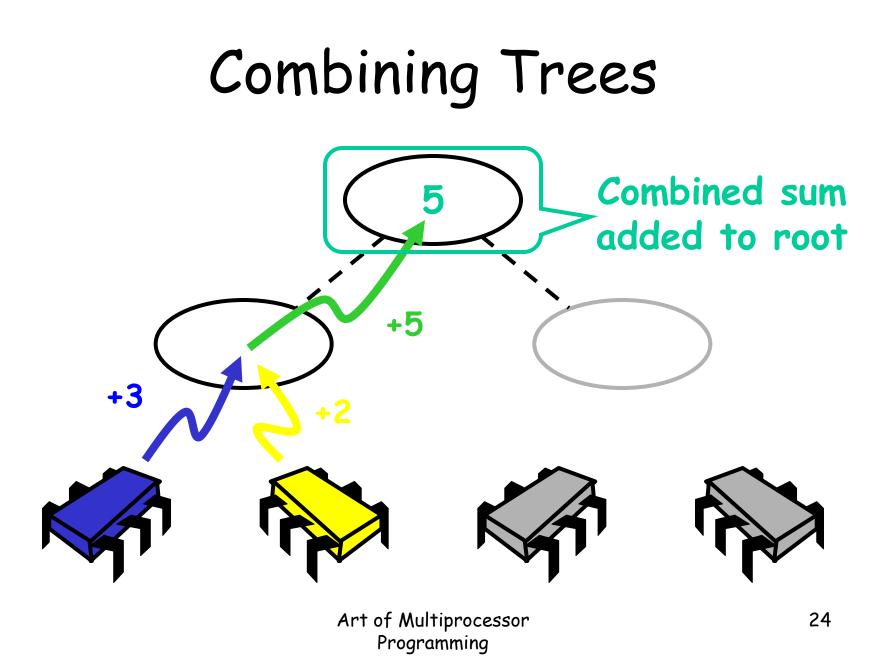
Programming

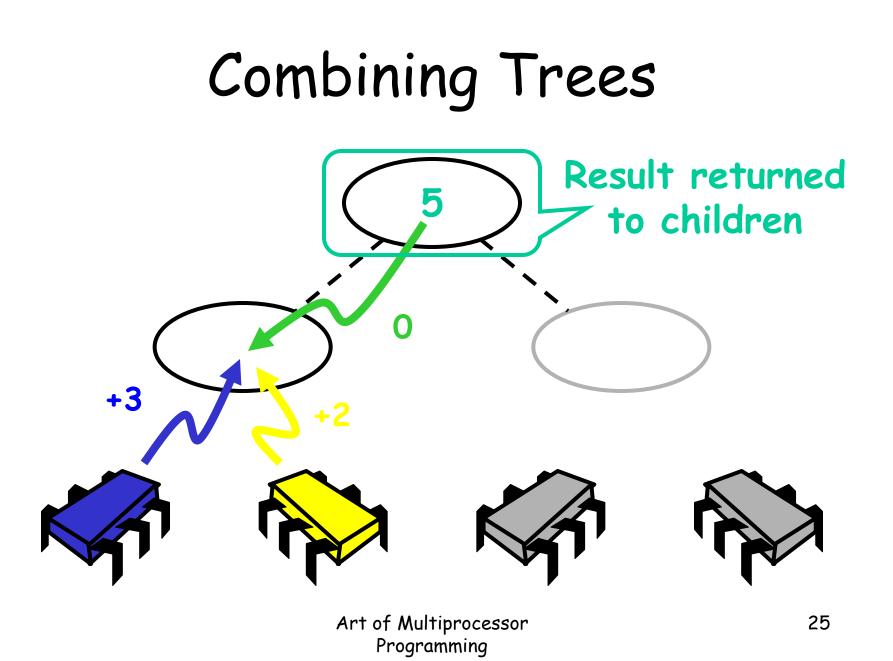


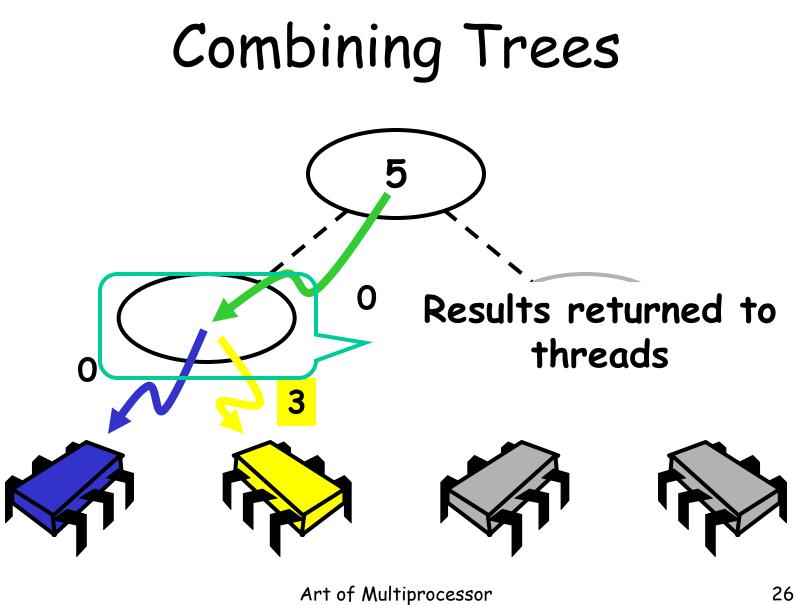




Programming







Programming

Devil in the Details

• What if

- threads don't arrive at the same time?
- Wait for a partner to show up?
 - How long to wait?
 - Waiting times add up ...
- Instead
 - Use multi-phase algorithm
 - Try to wait in parallel ...

enum CStatus{
 IDLE, FIRST, SECOND, DONE, ROOT};

enum CStatus{ IDLE, FIRST, SECOND, DONE, ROOT}; Nothing going on

IDLE, FIRST, SECOND, DONE, ROOT};

enum CStatus{





enum CStatus{ IDLE, FIRST, SECOND, DONE, ROOT}; 1st thread has completed operation & deposited result for 2nd thread

enum CStatus{ IDLE, FIRST, SECOND, DONE, ROOT};

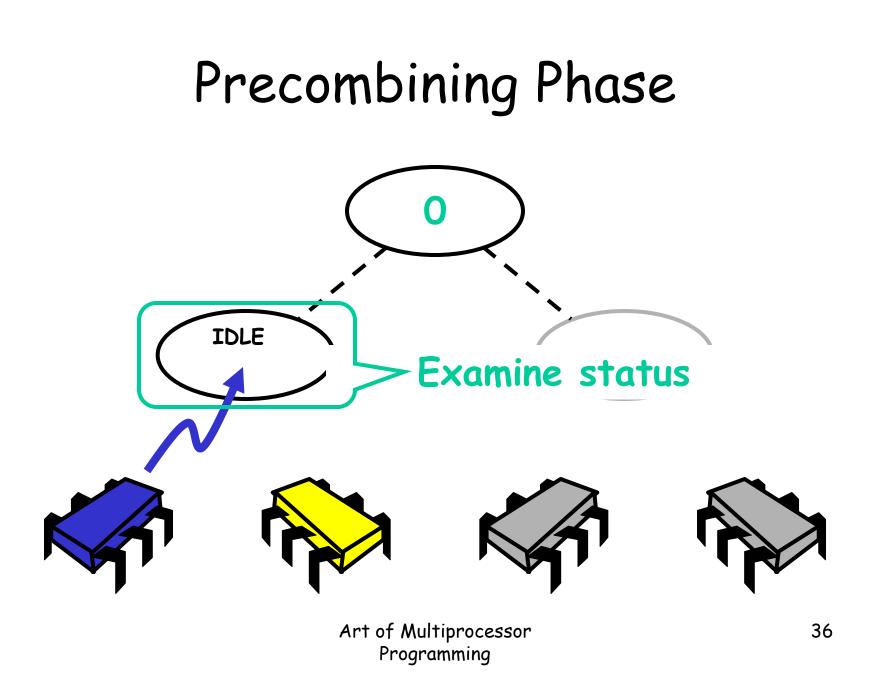
Special case: root node

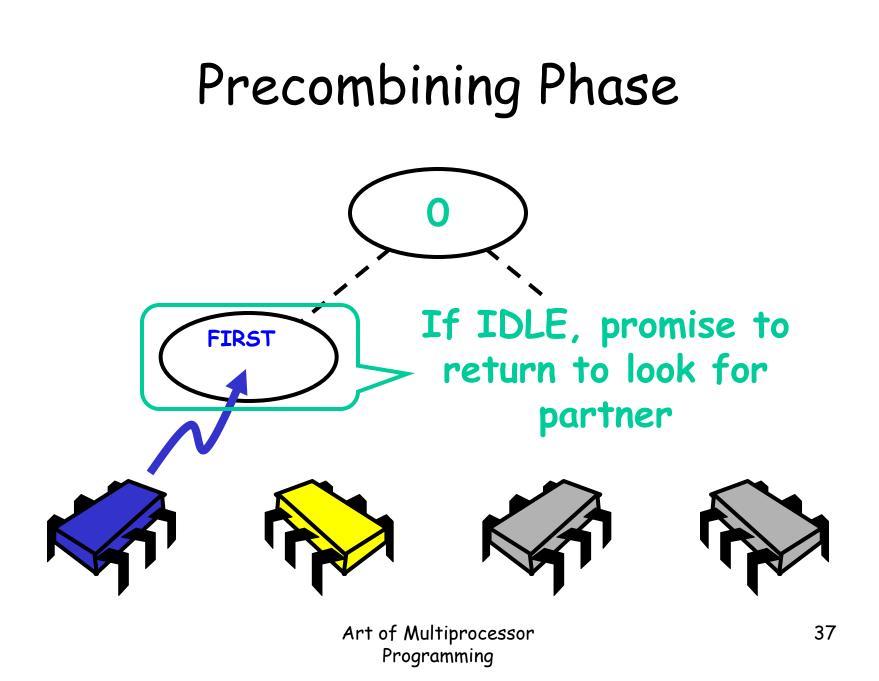
Node Synchronization

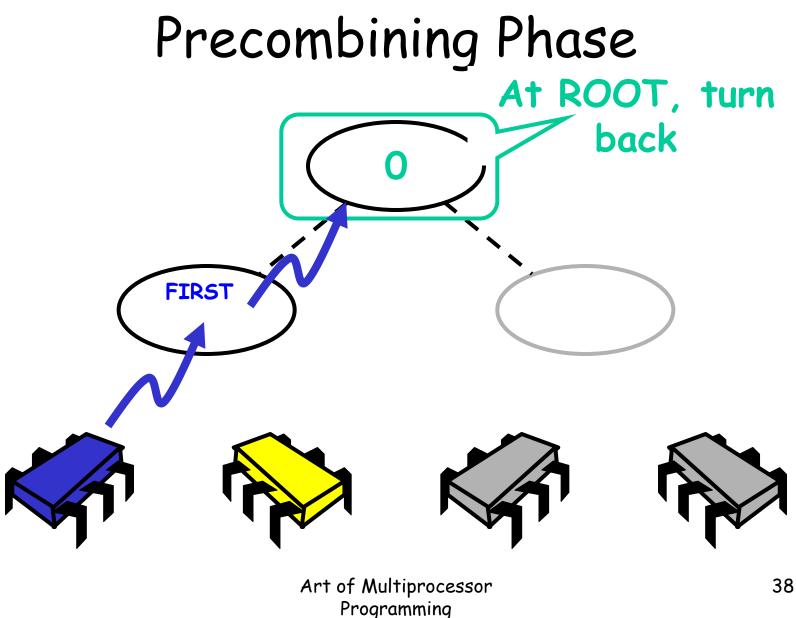
- Short-term
 - Synchronized methods
 - Consistency during method call
- Long-term
 - Boolean locked field
 - Consistency across calls

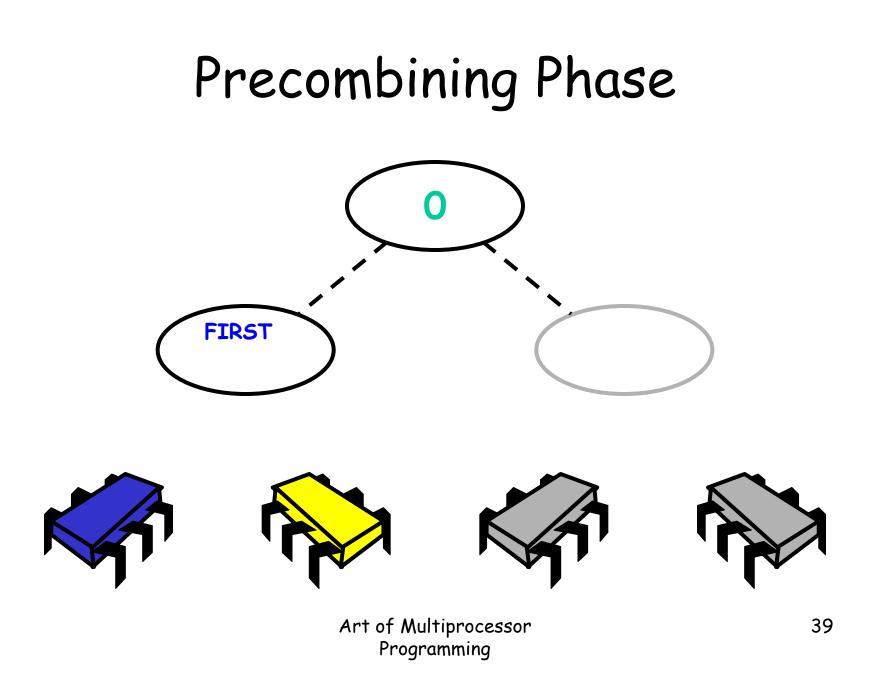
Phases

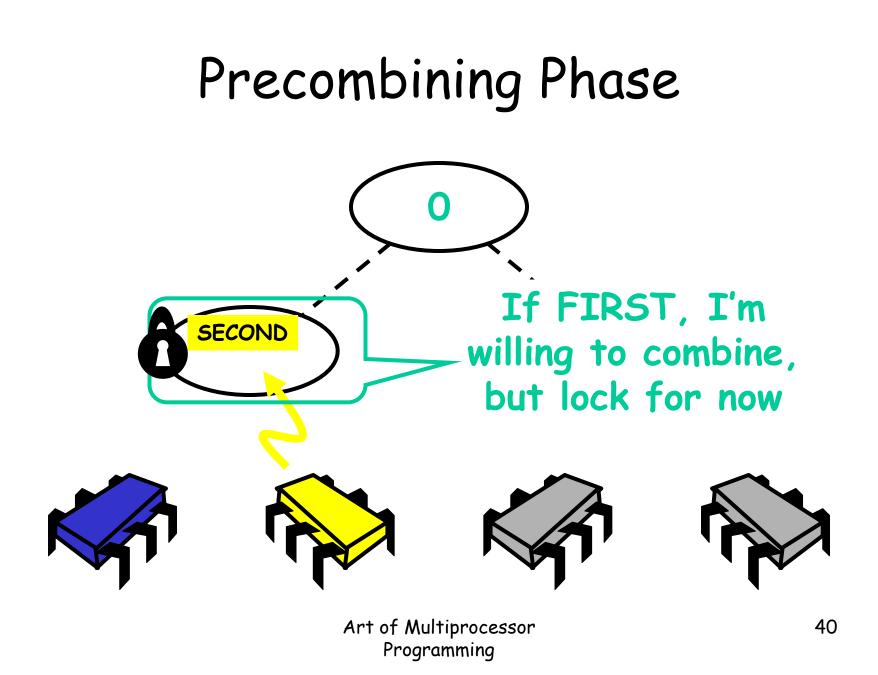
- Precombining
 - Set up combining rendez-vous
- Combining
 - Collect and combine operations
- Operation
 - Hand off to higher thread
- Distribution
 - Distribute results to waiting threads











Code

- Tree class
 - In charge of navigation
- Node class
 - Combining state
 - Synchronization state
 - Bookkeeping

Precombining Navigation

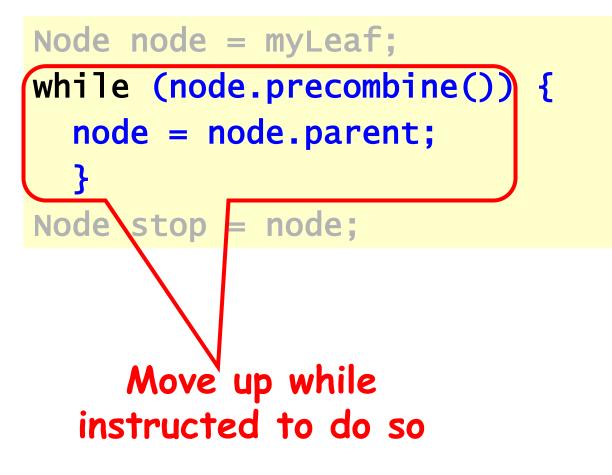
```
Node node = myLeaf;
while (node.precombine()) {
    node = node.parent;
    }
Node stop = node;
```



Node node = myLeaf; while (node.precombine()) { node = node.parent; } Node stop = node;

Start at leaf

Precombining Navigation



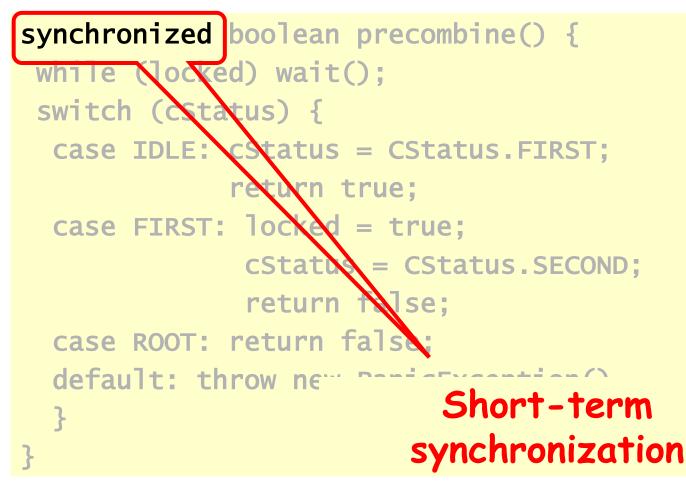
Art of Multiprocessor Programming

Precombining Navigation

```
Node node = myLeaf;
while (node.precombine()) {
  node = node.parent;
Node stop = node;
            Remember where we
                  stopped
```

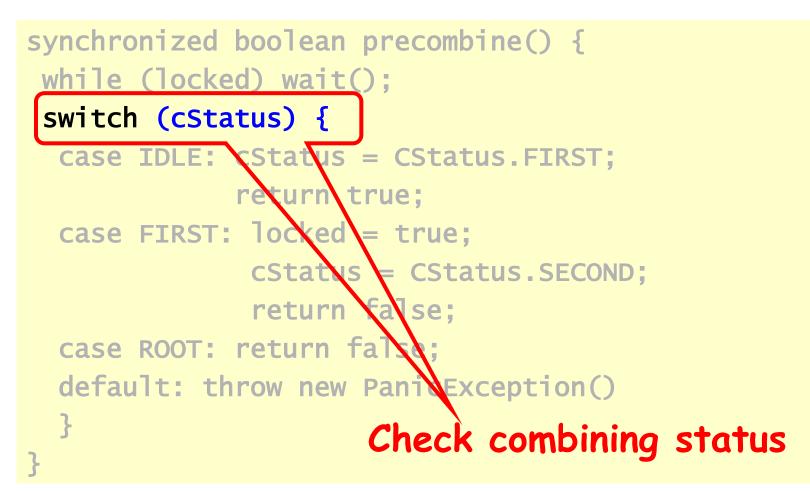
Art of Multiprocessor Programming

```
synchronized boolean precombine() {
while (locked) wait();
 switch (cStatus) {
  case IDLE: cStatus = CStatus.FIRST;
             return true;
  case FIRST: locked = true;
              cStatus = CStatus.SECOND;
              return false;
  case ROOT: return false;
  default: throw new PanicException()
  }
```

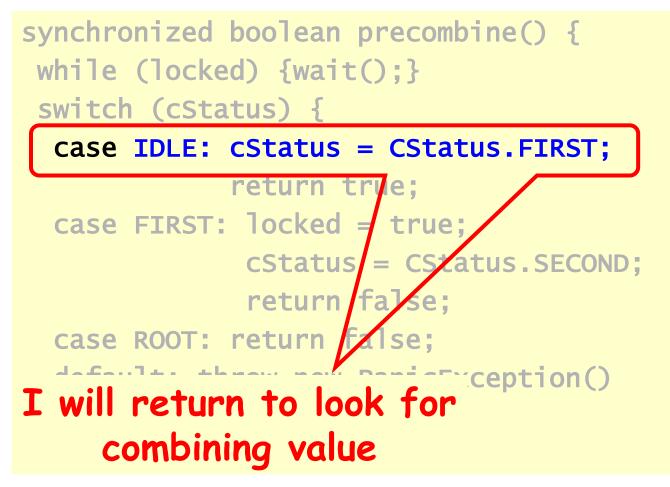


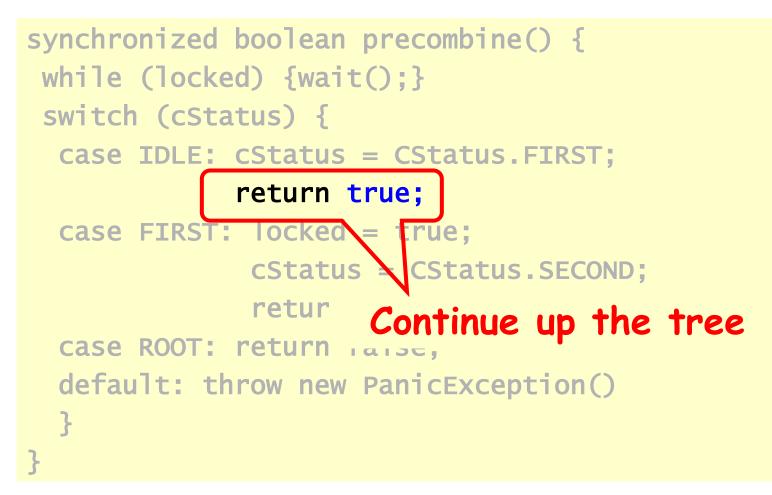
Synchronization

```
synchronized boolean precombine() {
while (locked) wait();
switch (cStatus)
 case IDLE: cStatus = CStatus.FIRST;
             return tri
 case FIRST: locked =
                        rue;
                         CStatus.SECOND;
              cStatus
              return fa
 case ROOT: return false
 default: throw ne
                       Wait while node is
                              locked
```



Node was IDLE

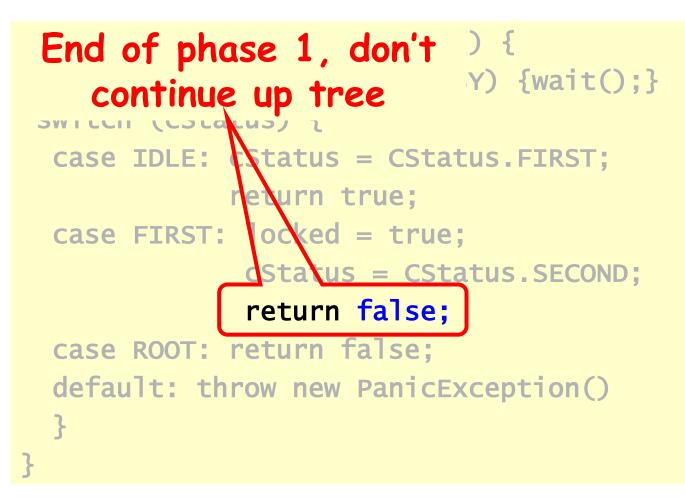




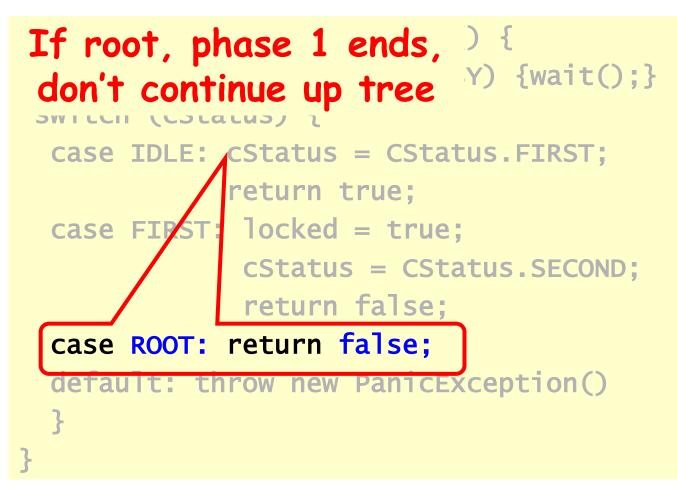
I'm the 2nd Thread

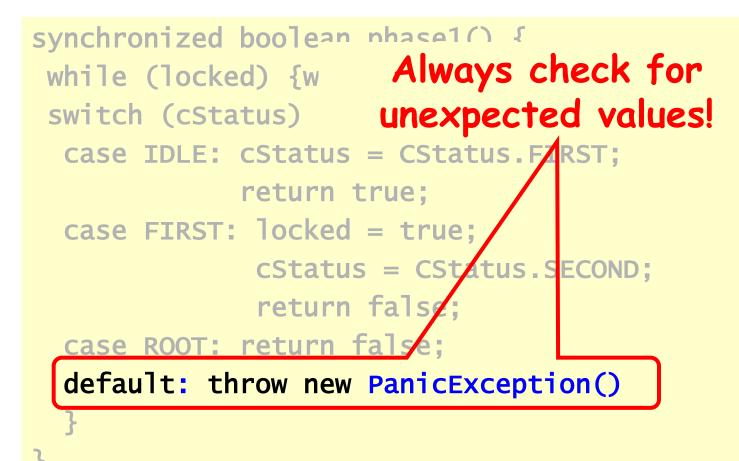
```
synchronized boolean precombine() {
while (locked) {wait();}
 switch (cStatus) {
  case IDLE: cStatus = CStatus.FIRST;
             return true:
  case FIRST: locked = true;
              cStatus = CStatus.SECOND;
              return false
  case ROOT: return false
                              ~~+; ~~ ()
  If 1<sup>st</sup> thread has promised to return,
  lock node so it won't leave without me
```

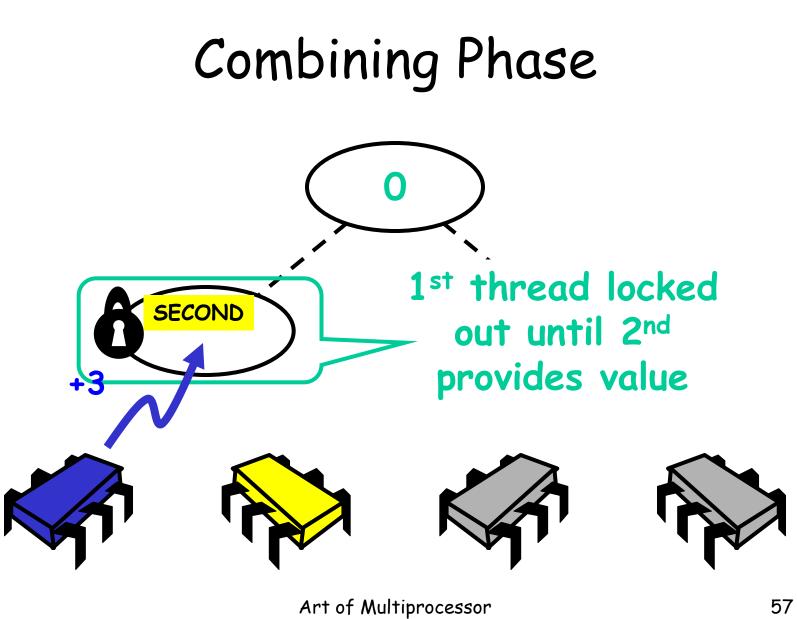
```
synchronized boolean precombine() {
while (locked) {wait();}
 switch (cStatus) {
  case IDLE: cStatus = CStatus.FIRST;
             return true;
  case FIRST: locked = true;
              cStatus = CStatus.SECOND;
              return false;
  case ROOT: return false;
  default: throw new
                       Prepare to deposit 2<sup>nd</sup>
                                 value
```



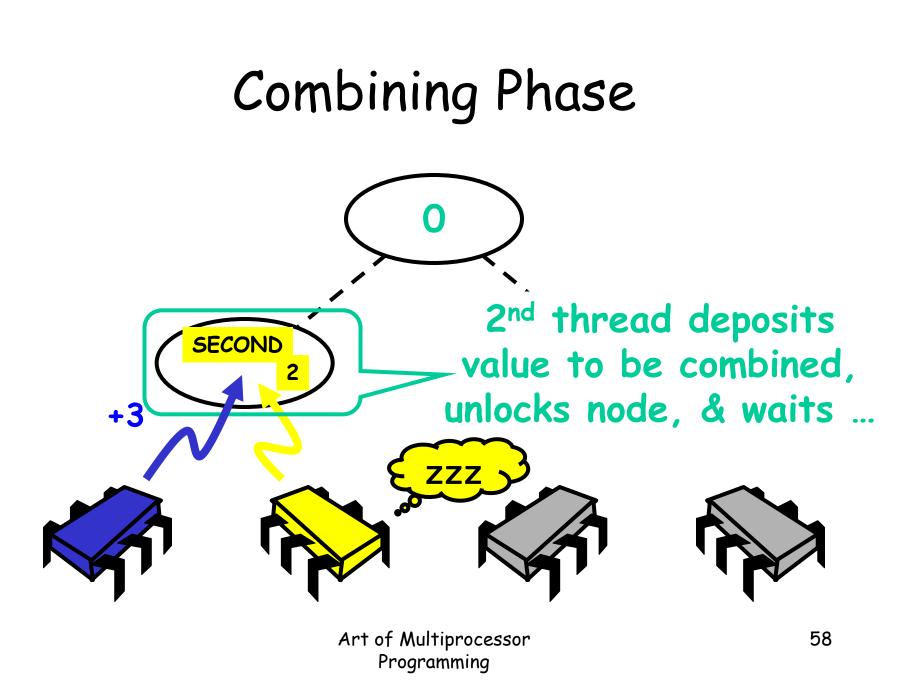
Node is the Root

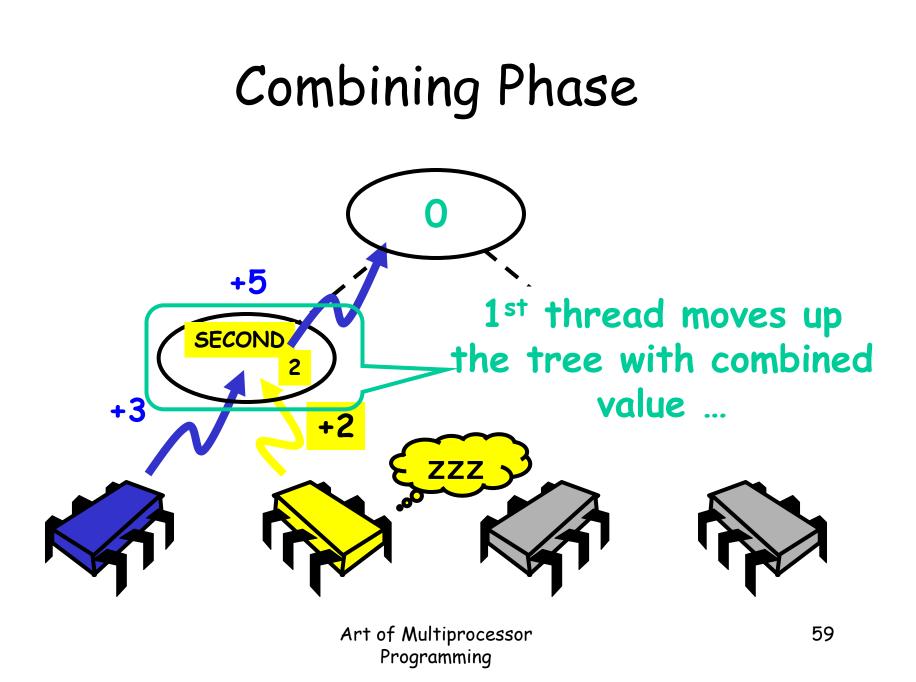


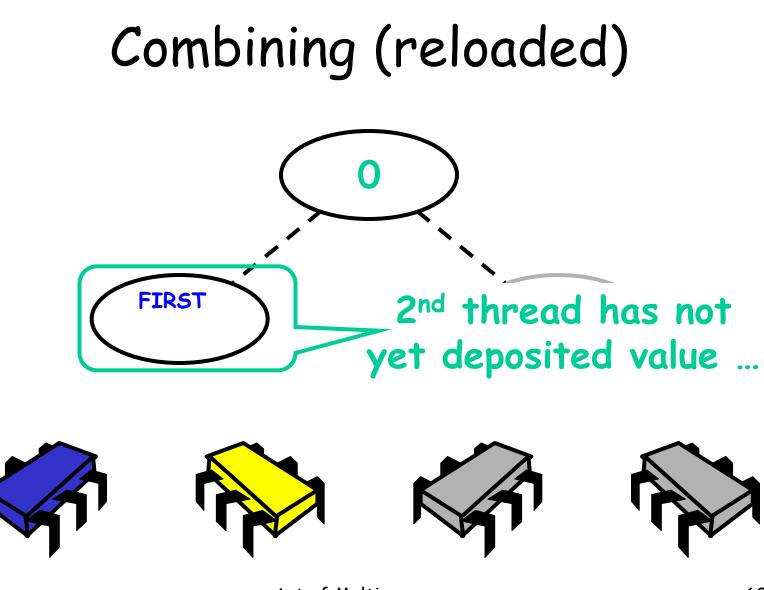




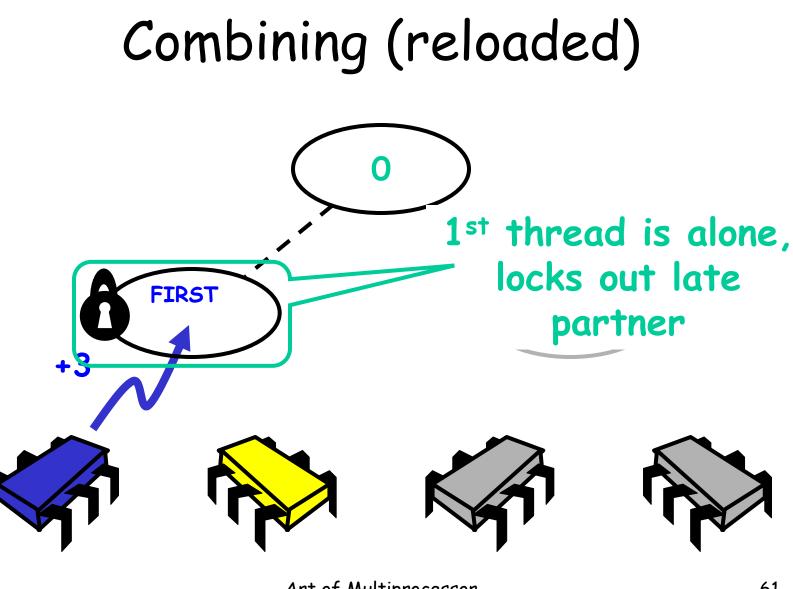
Programming



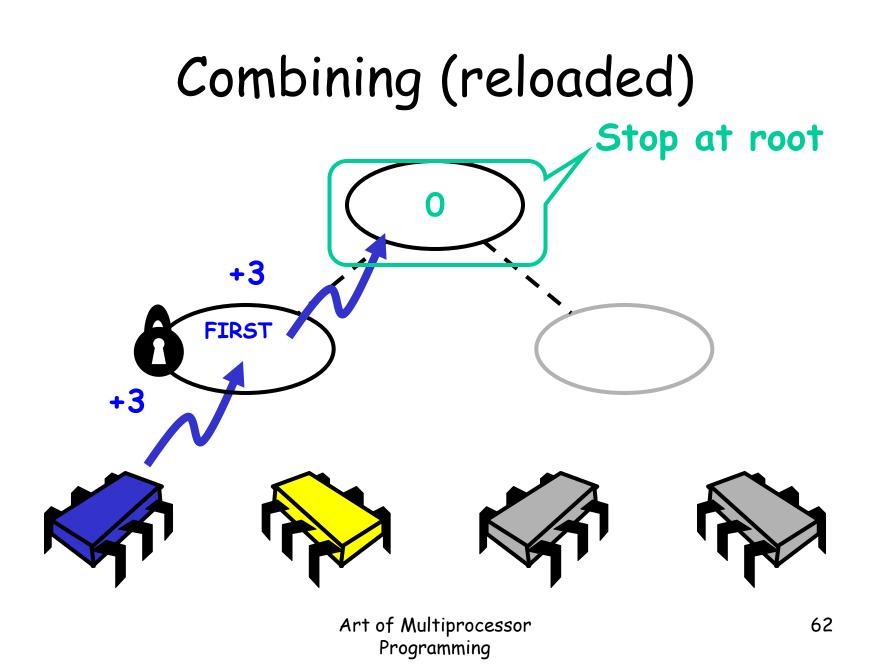


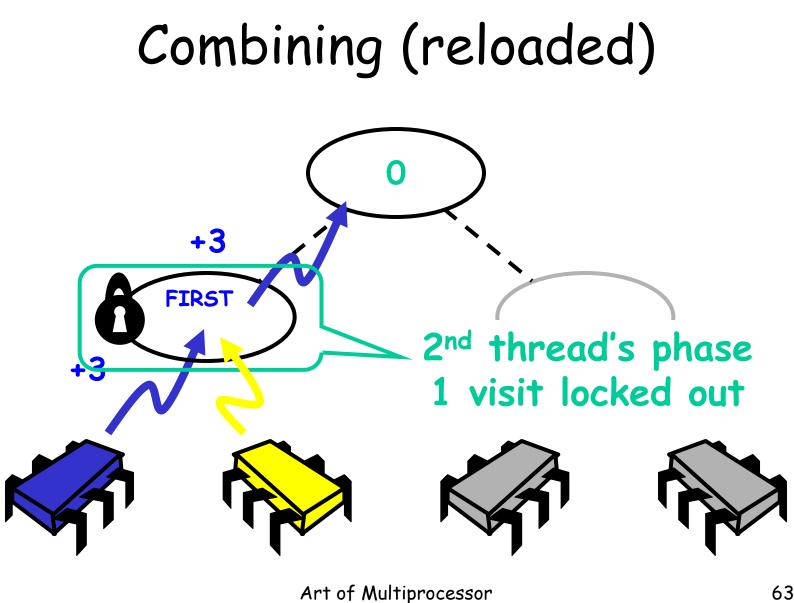


Art of Multiprocessor Programming



Art of Multiprocessor Programming



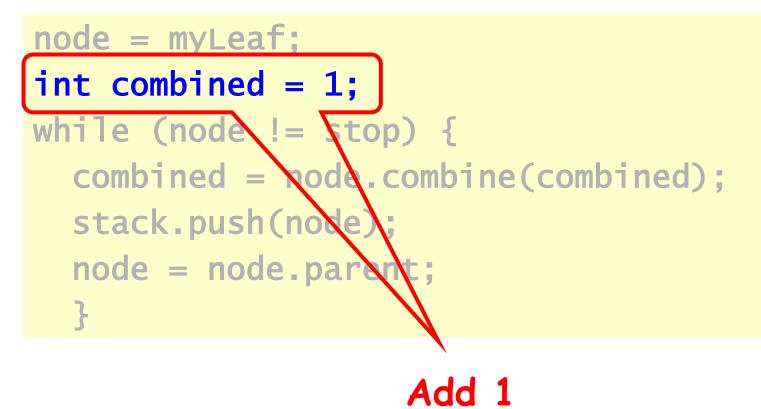


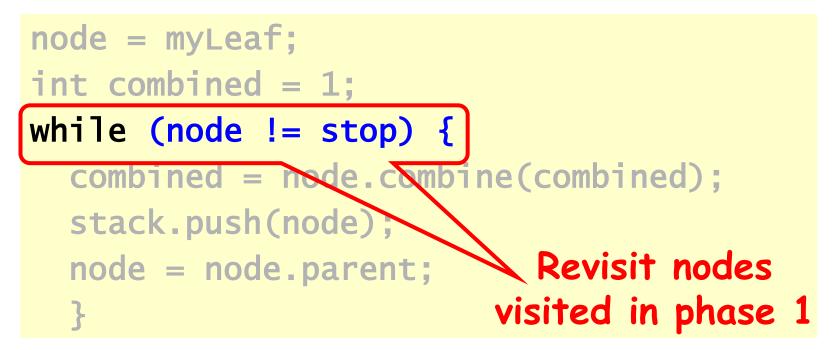
Programming

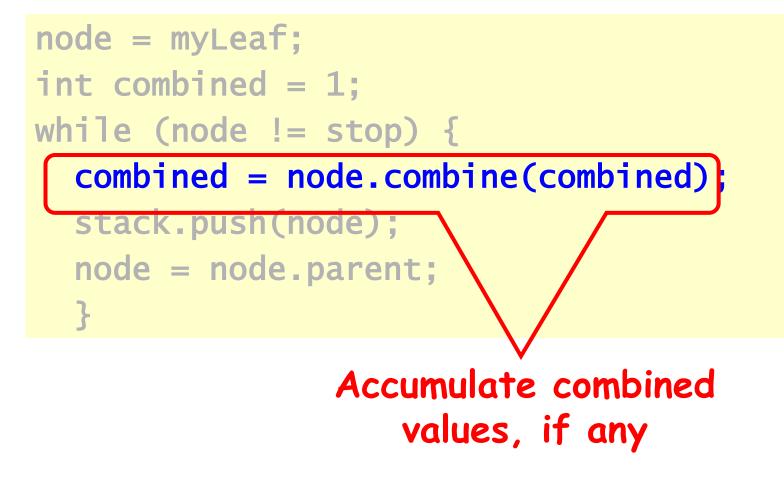
```
node = myLeaf;
int combined = 1;
while (node != stop) {
   combined = node.combine(combined);
   stack.push(node);
   node = node.parent;
   }
```

node = myLeaf; int combined = 1; while (node != stop) { combined = node.combine(combined); stack.push(node); node = node.parent; }

Start at leaf







Art of Multiprocessor Programming

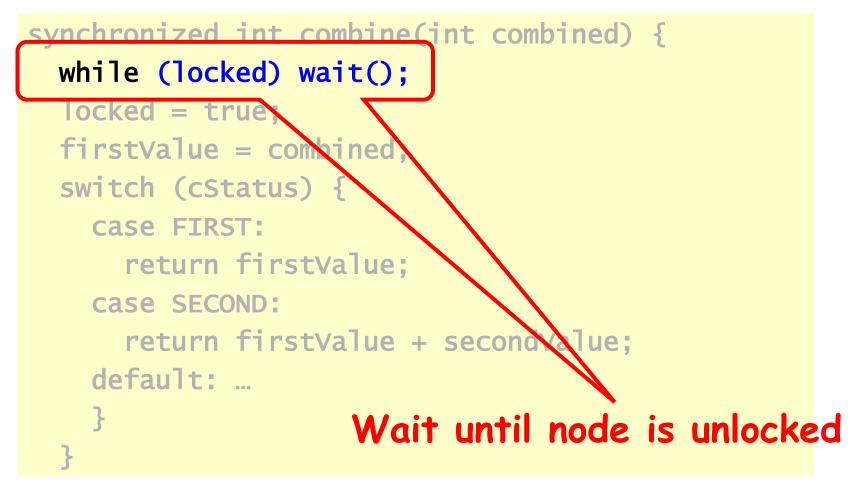
node = myLeaf; We will retraverse path in int combined = 1; reverse order ... while (node != stop) { combined = node combine(combined); stack.push(node); node = node.parent; }

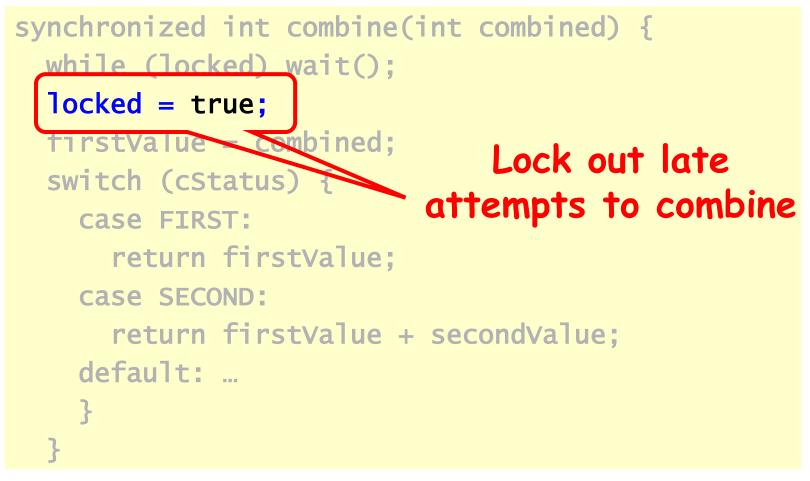
```
node = myLeaf; Move up the tree
int combined = 1;
while (node != stop) {
   combined = node.combine(combined);
   stack.push(node);
   node = node.parent;
```

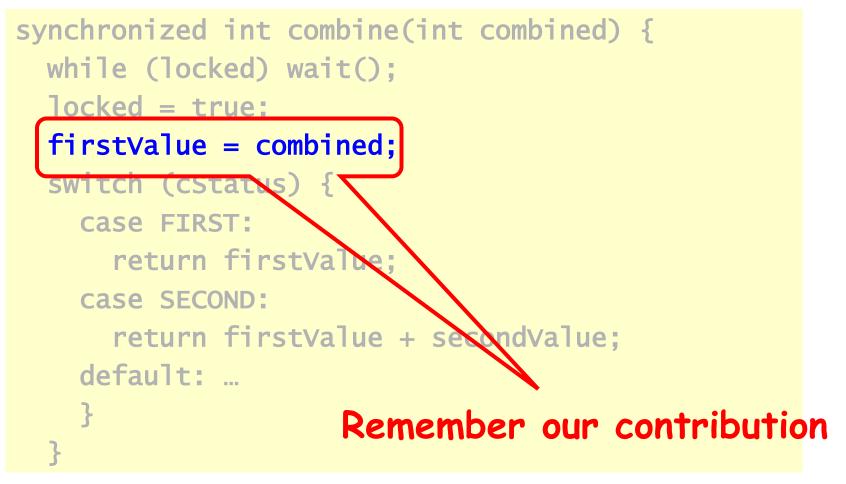
Combining Phase Node

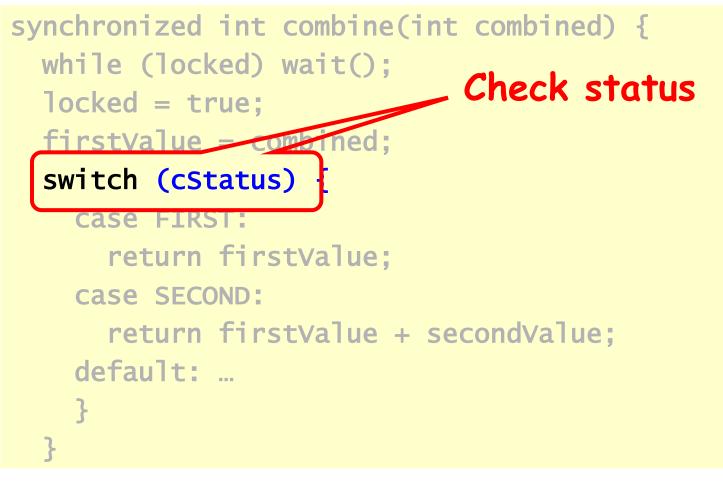
```
synchronized int combine(int combined) {
  while (locked) wait();
  locked = true;
  firstValue = combined;
  switch (cStatus) {
    case FIRST:
      return firstValue;
    case SECOND:
      return firstValue + secondValue;
    default: ...
```

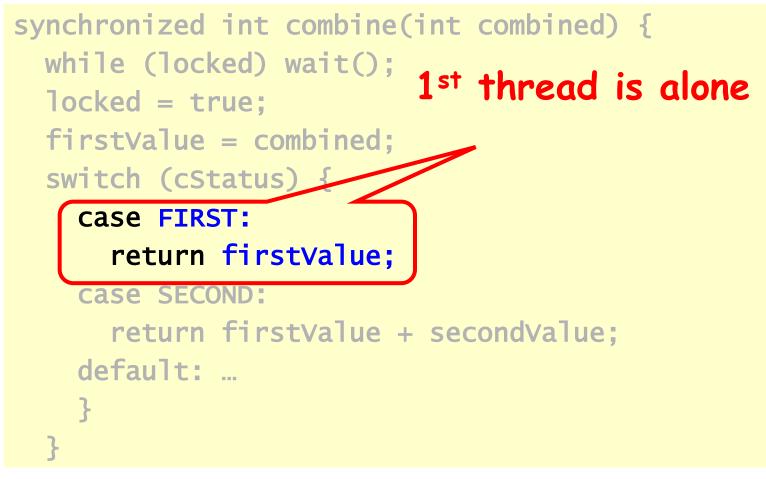
Combining Phase Node



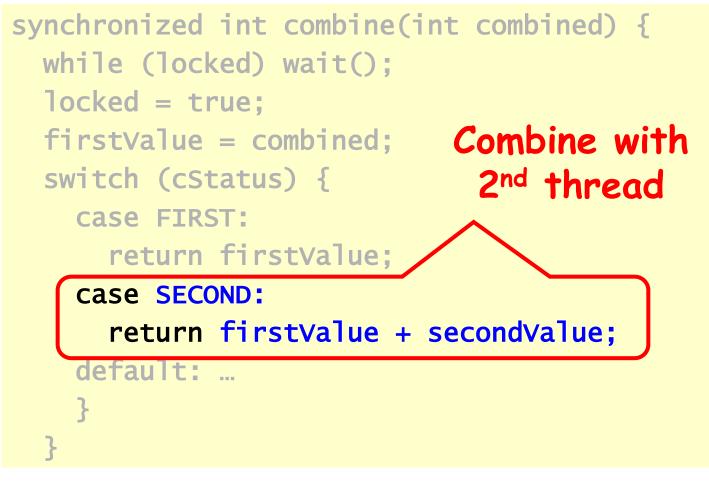


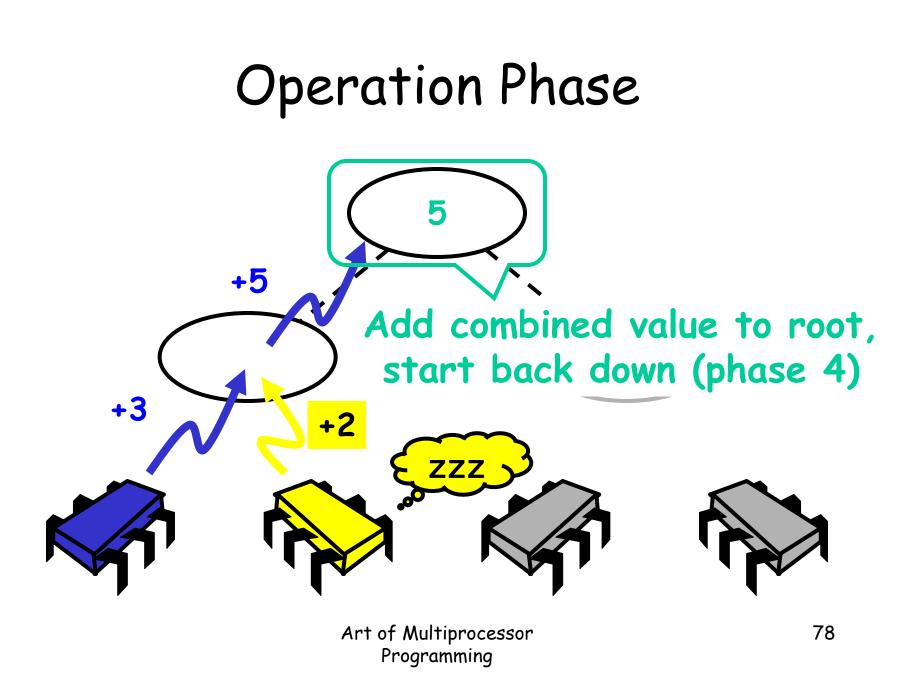




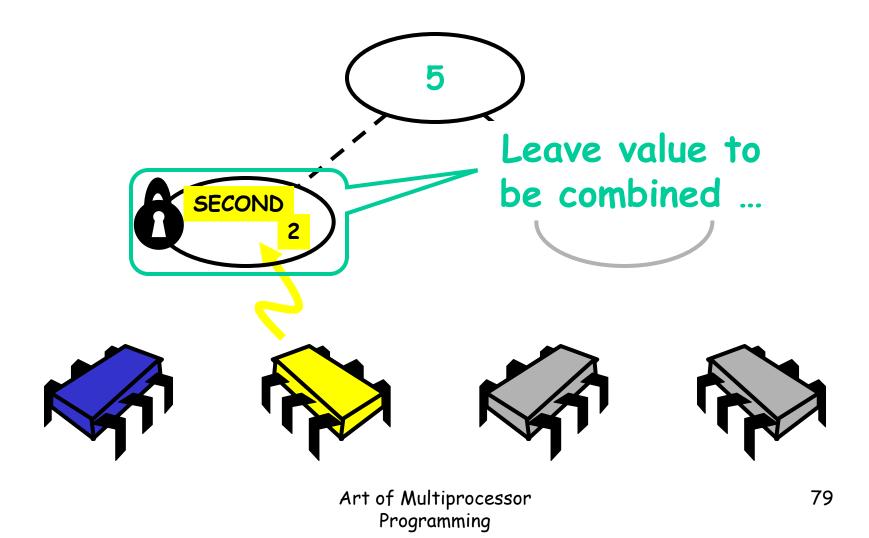


Combining Node

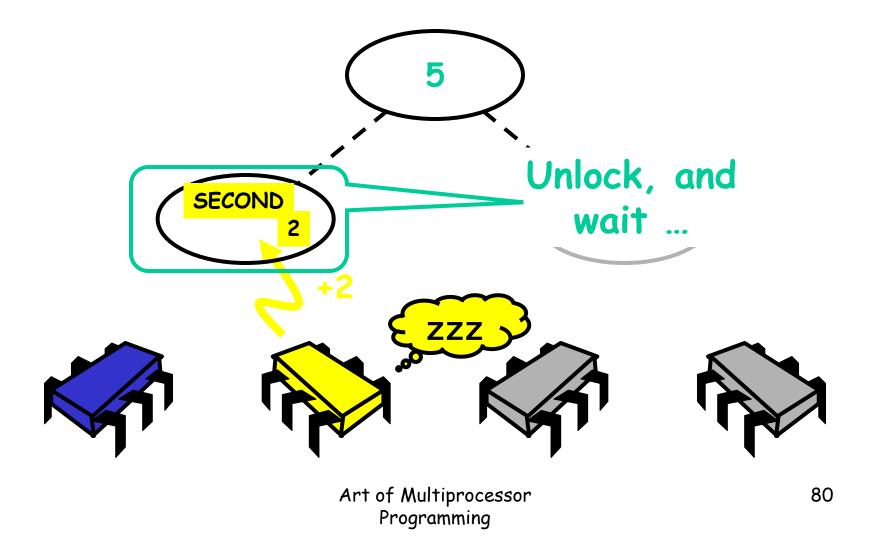




Operation Phase (reloaded)

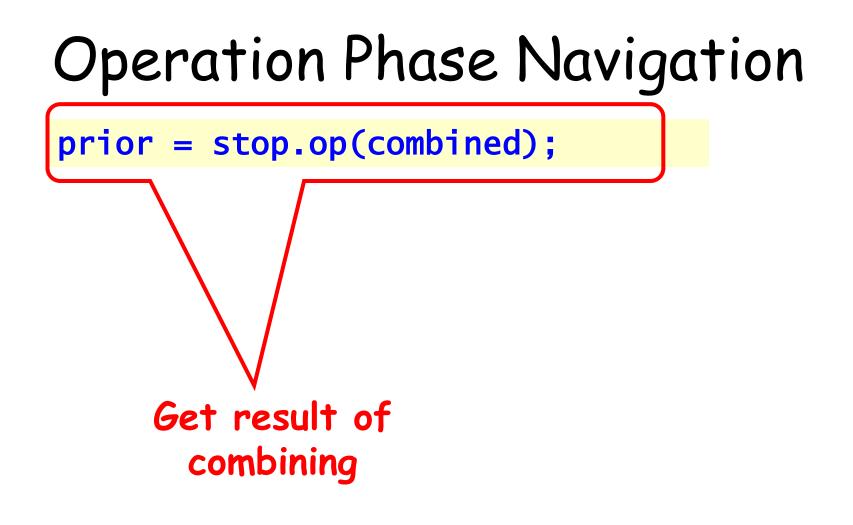


Operation Phase (reloaded)



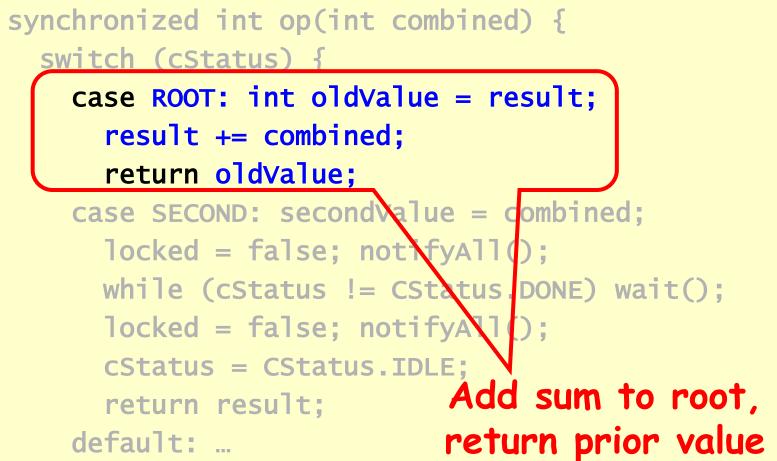
Operation Phase Navigation

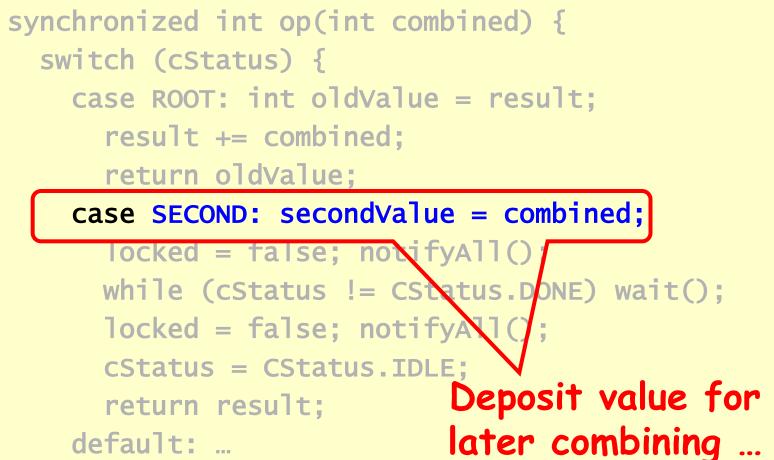
prior = stop.op(combined);



Operation Phase Node synchronized int op(int combined) { switch (cStatus) { case ROOT: int oldValue = result; result += combined; return oldvalue; case SECOND: secondValue = combined; locked = false; notifyAll(); while (cStatus != CStatus.DONE) wait(); locked = false; notifyAll(); cStatus = CStatus.IDLE; return result; default: ...

At Root

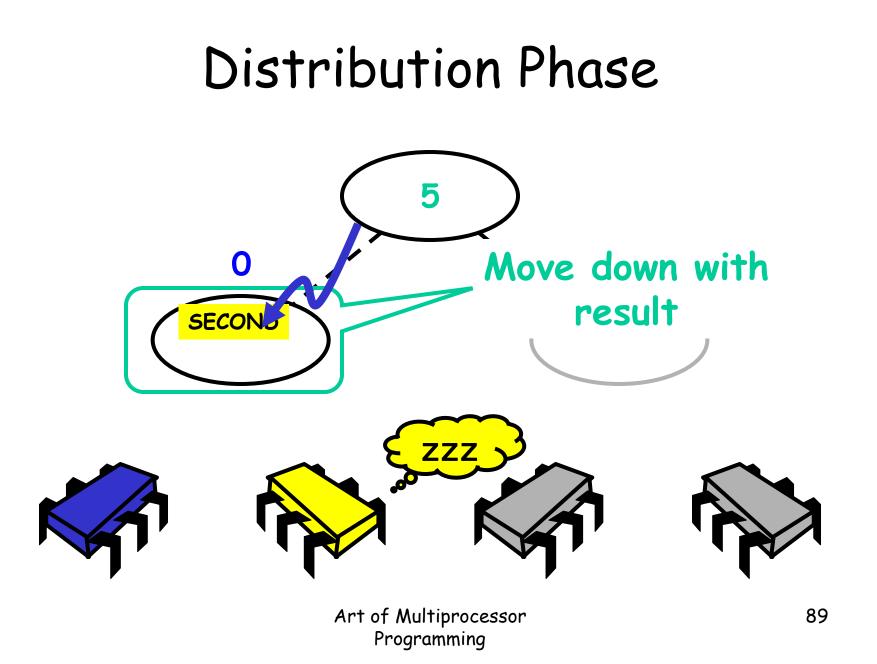




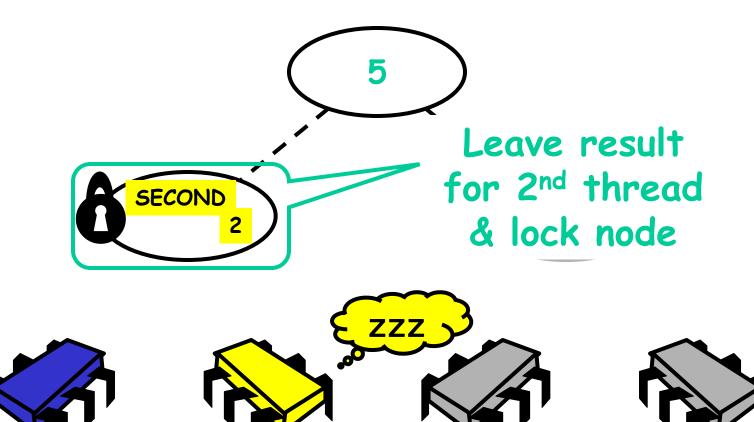
```
synchronized int op(int combined) {
  switch (cStatus) {
    case ROOT: int oldValue = result;
      result += combined;
      return oldValue;
    case SECOND: secondValue = combined;
      locked = false; notifyAll();
      while (CStatus != CStatus.DONE) wait():
      locked = false; notify
      cStatus = CStatus.IDLE:
                            Unlock node, notify
      return result;
                                 1<sup>st</sup> thread
    default: ...
```

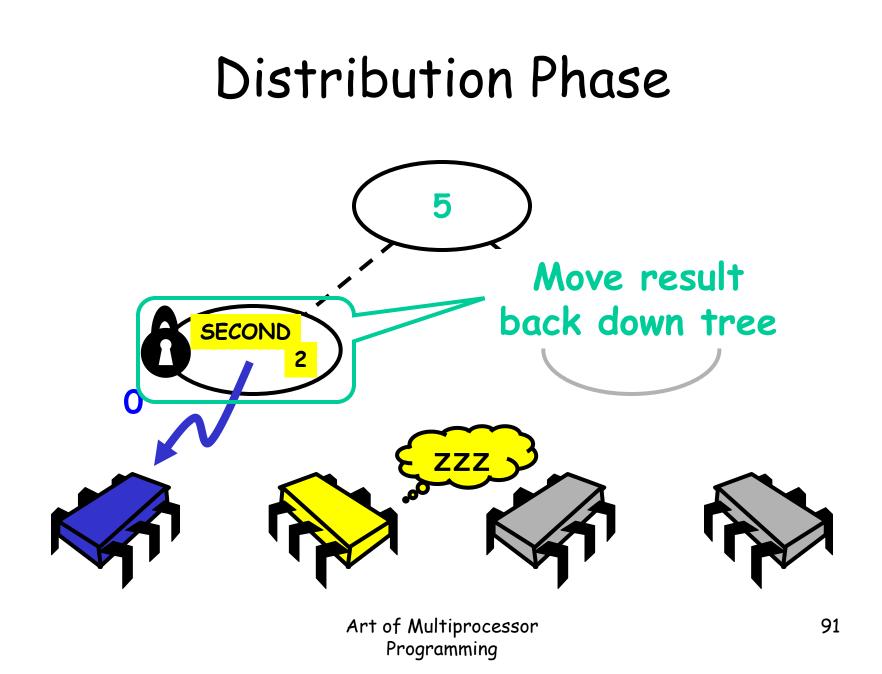
```
synchronized int op(int combined) {
  switch (cStatus) {
                                Wait for 1<sup>st</sup>
    case ROOT: int oldValue
                             thread to deliver
      result += combined;
                                    results
      return oldValue;
    case SECOND: secondValue =
                                combined:
      locked = false; notifyAl
      while (cStatus != CStatus.DONE) wait();
      10CKed = Tarse; notrivario;
      cStatus = CStatus.IDLE;
      return result;
    default: ...
```

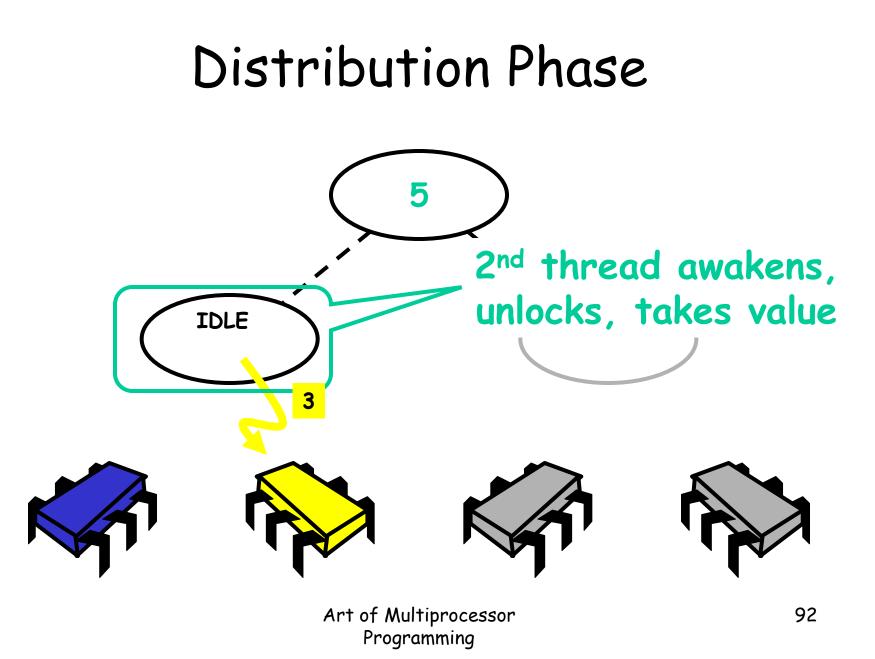
synchronized int op(int combined) { switch (cStatus) { Unlock node & case ROOT: int oldValue return result += combined; return oldValue; case SECOND: secondValue = ____mbined; locked = false; notifyA while (cStatus != CStatus.DONE) wait(); locked = false; notifyAll(); cStatus = CStatus.IDLE; return result; default: ...



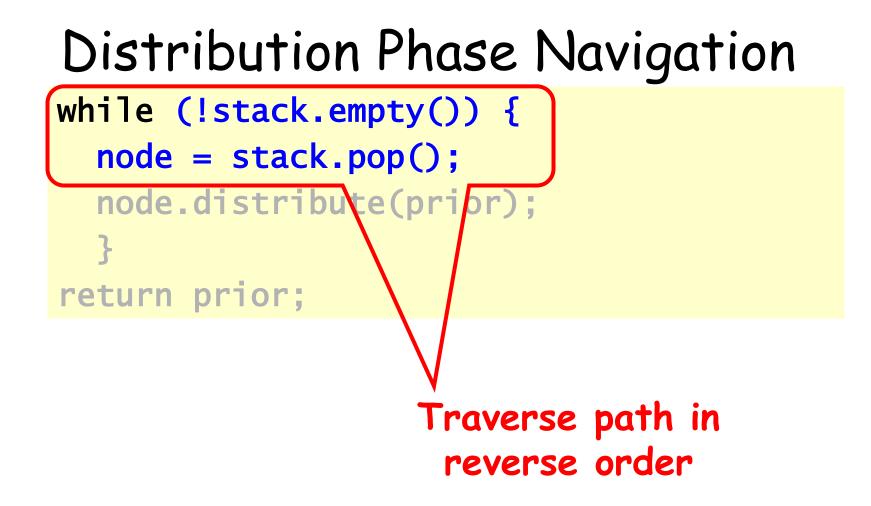
Distribution Phase

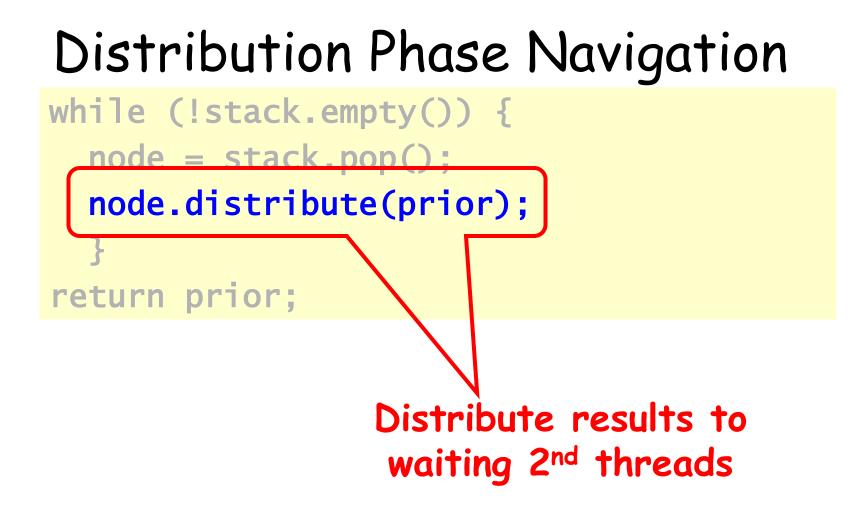






```
Distribution Phase Navigation
while (!stack.empty()) {
    node = stack.pop();
    node.distribute(prior);
    }
return prior;
```





Distribution Phase Navigation

while (!stack.empty()) {
 node = stack.pop();
 node.distribute(prior);

return prior;

Return result to caller

Distribution Phase

```
synchronized void distribute(int prior) {
   switch (cStatus) {
     case FIRST:
       cStatus = CStatus.IDLE;
       locked = false; notifyAll();
       return;
     case SECOND:
       result = prior + firstValue;
       cStatus = CStatus.DONE; notifyAll();
       return;
     default: ...
```



synchronized void distribute(int prior) {
 switch (cStatus) {

case **FIRST**:

cStatus = CStatus.IDLE;

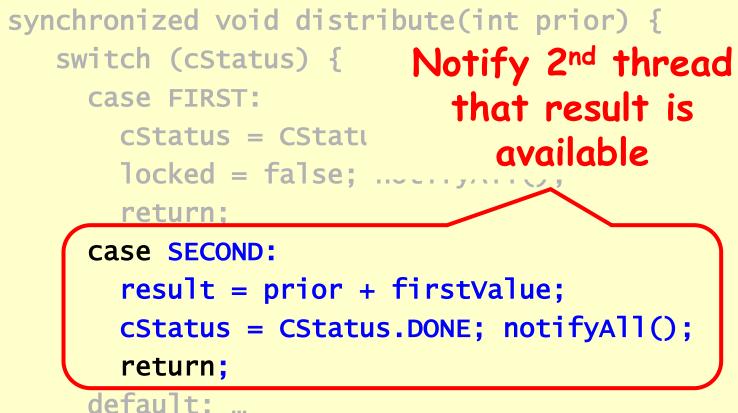
locked = false; notifyAll();

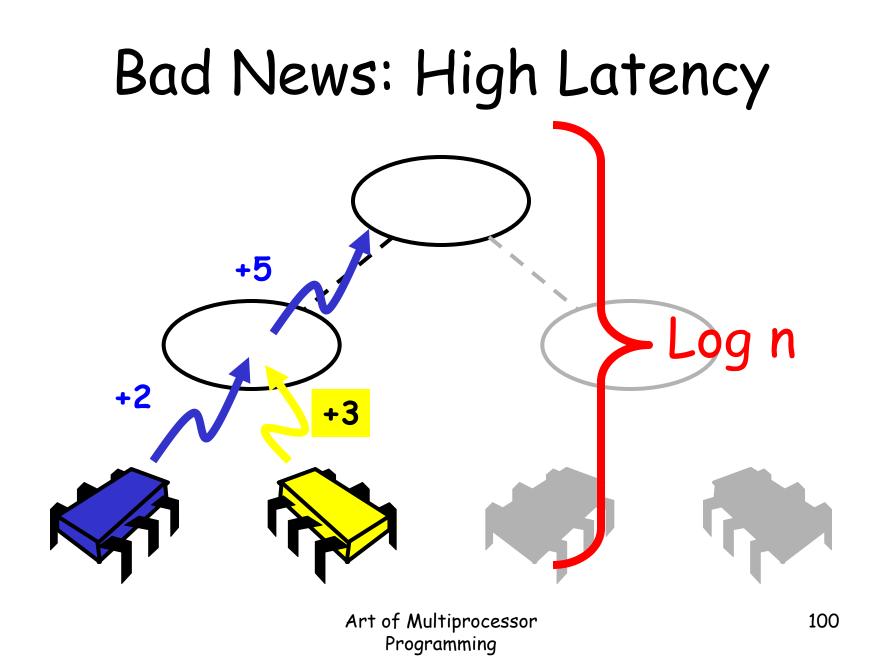
return;

case SECOND:
 result = prior + firstValue;
 cStatus = CStatus DONE; notifyAll();
 return;
 Mo combining, unlock
default: ...

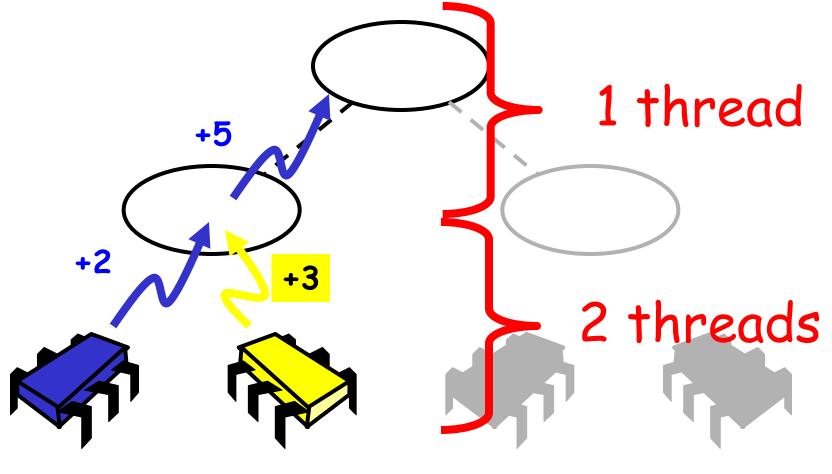
node & reset

Distribution Phase





Good News: Real Parallelism



Throughput Puzzles

- Ideal circumstances
 - All n threads move together, combine
 - n increments in O(log n) time
- Worst circumstances
 - All n threads slightly skewed, locked out
 - n increments in $O(n \cdot \log n)$ time

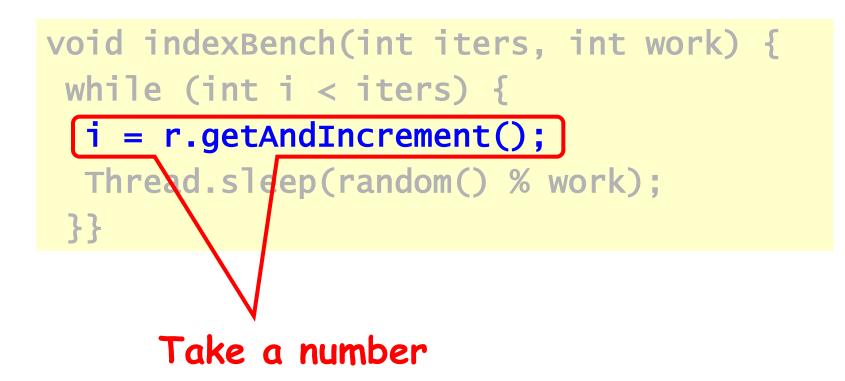
void indexBench(int iters, int work) {
 while (int i < iters) {
 i = r.getAndIncrement();
 Thread.sleep(random() % work);
 }}</pre>

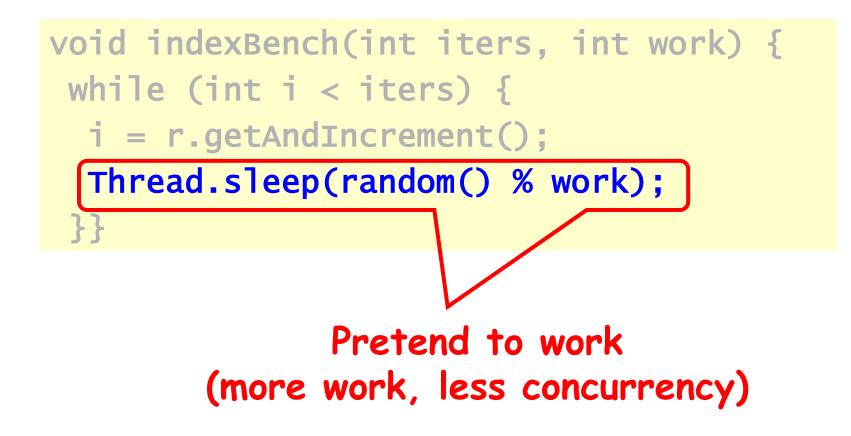
void indexBench(int iters, int work) {
 while (int i < iters) {
 i = r.getAndIncrement();
 Thread.sleep(random() % work);
 }}</pre>

How many iterations

void indexBench(int iters, int work) {
 while (int i < iters) {
 i = r.getAndIncrement();
 Thread.sleep(random() % work);
 }}</pre>

Expected time between incrementing counter



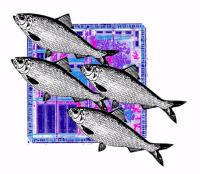


Performance Benchmarks

Alewife

- NUMA architecture
- Simulated

MIT - ALEWIFE



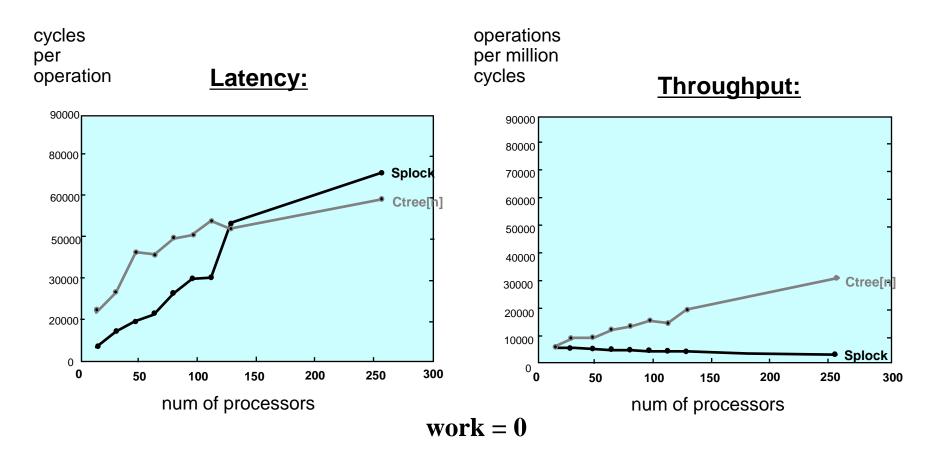
• Throughput:

 average number of inc operations in 1 million cycle period.

• Latency:

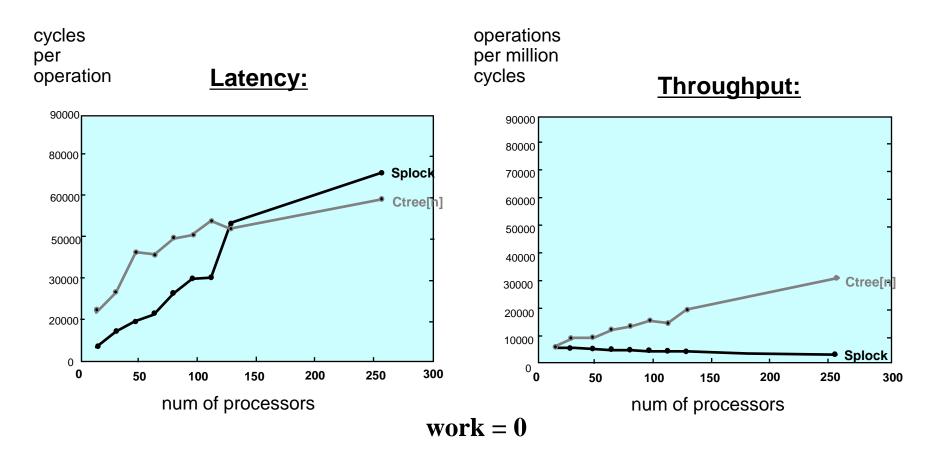
 average number of simulator cycles per inc operation.

Performance



Art of Multiprocessor Programming 109

Performance

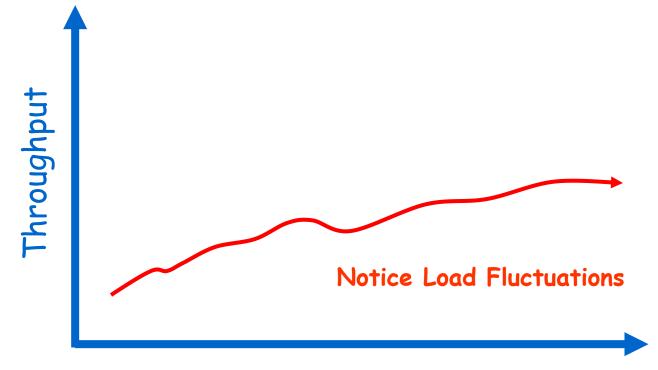


Art of Multiprocessor Programming 110

The Combining Paradigm

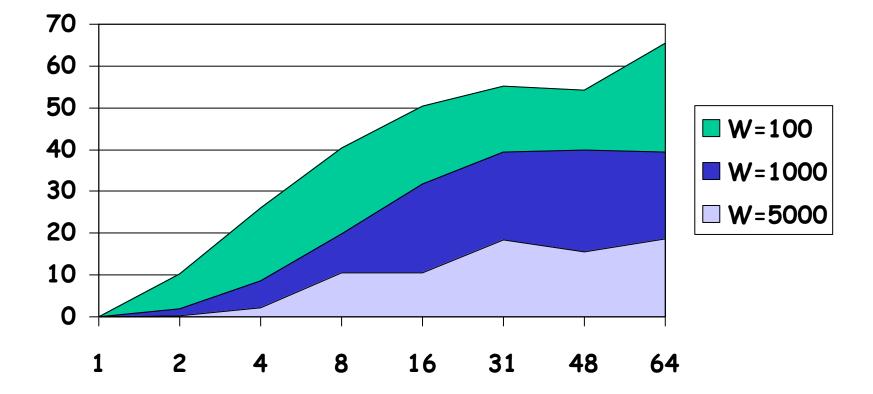
- Implements any RMW operation
- When tree is loaded
 - Takes 2 log n steps
 - for n requests
- Very sensitive to load fluctuations:
 - if the arrival rates drop
 - the combining rates drop
 - overall performance deteriorates!

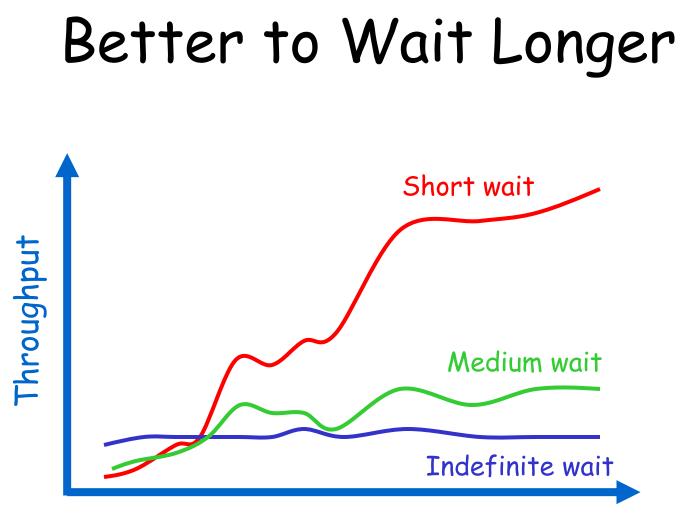
Combining Load Sensitivity



processors

Combining Rate vs Work



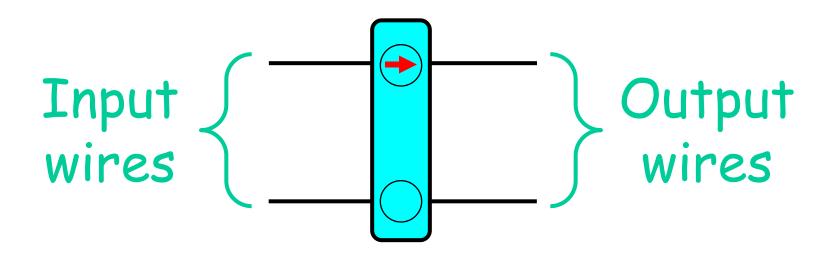


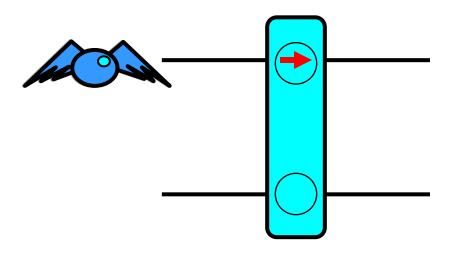
processors

Conclusions

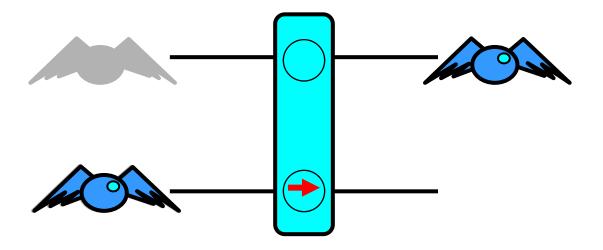
- Combining Trees
 - Work well under high contention
 - Sensitive to load fluctuations
 - Can be used for getAndMumble() ops
- Next
 - Counting networks
 - A different approach ...

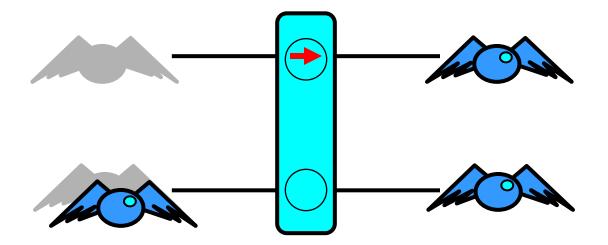
A Balancer

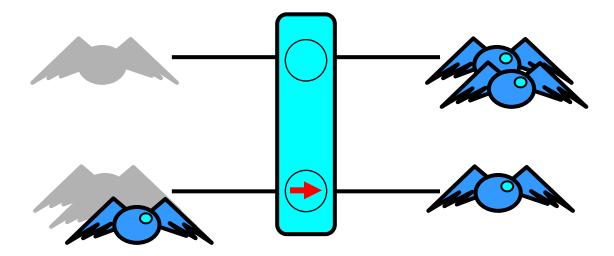


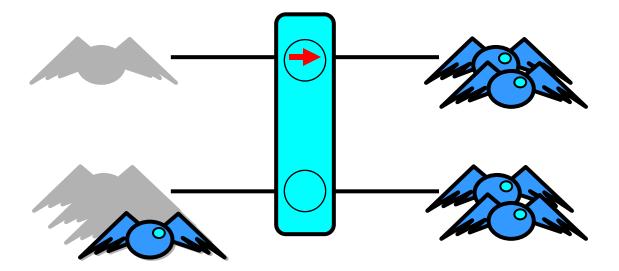


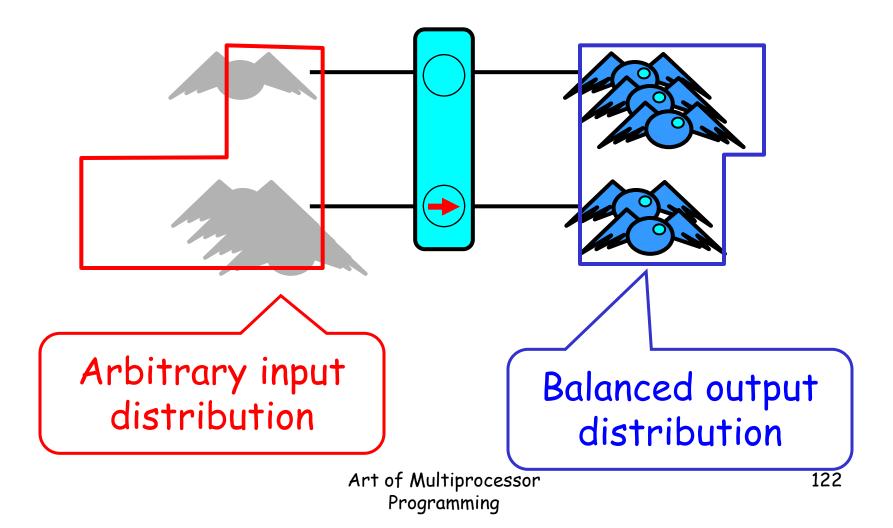
- Token i enters on any wire
- leaves on wire i mod (fan-out)



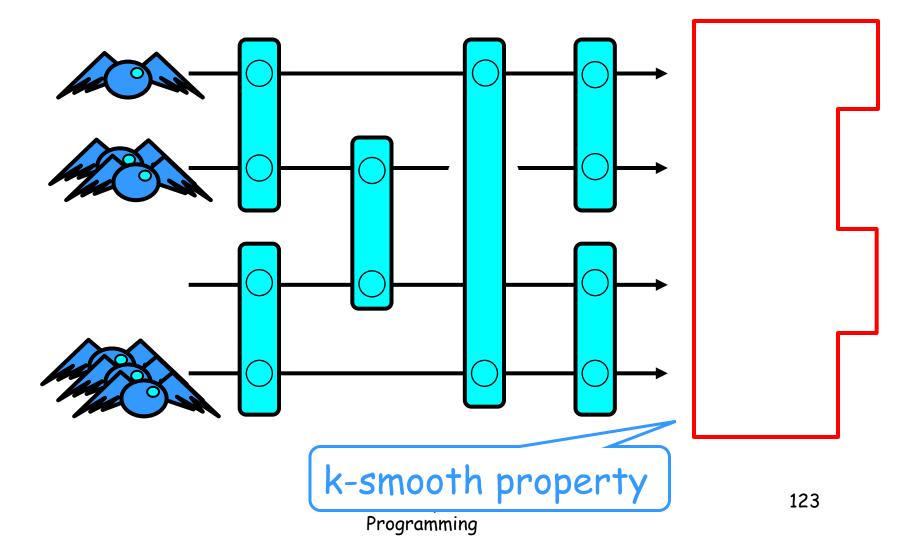




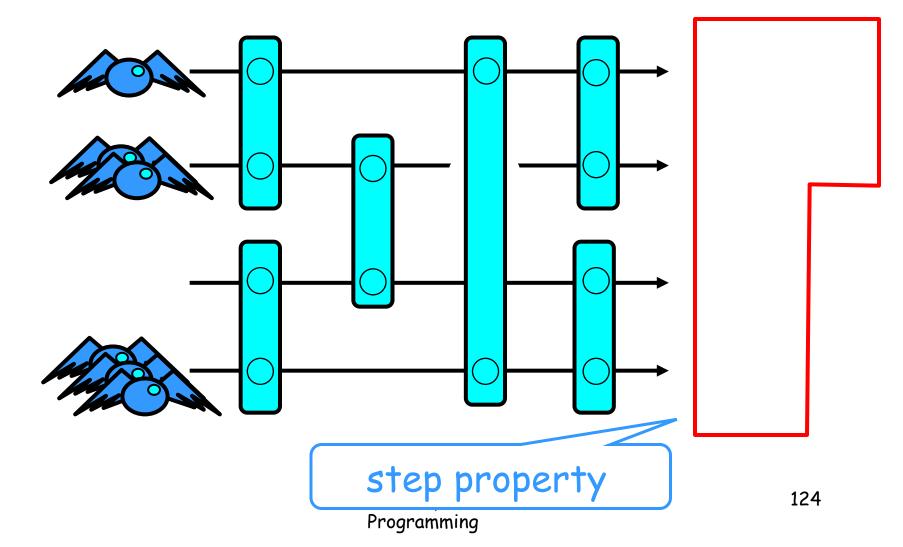




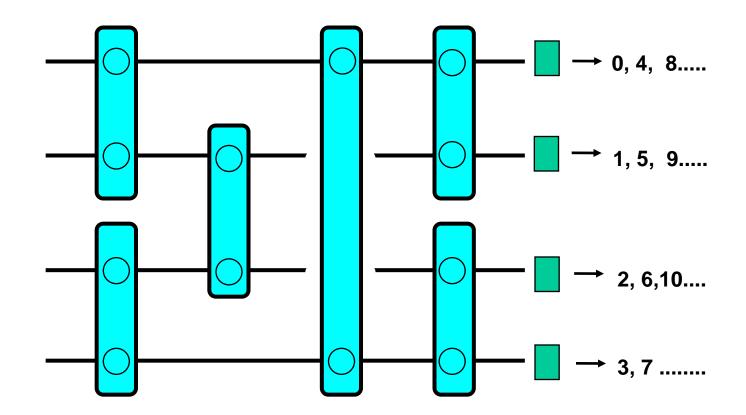
Smoothing Network



Counting Network

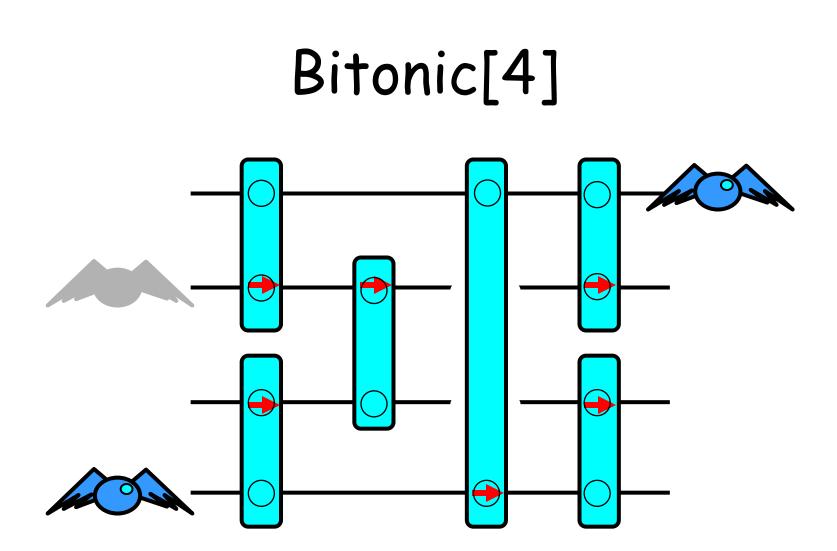


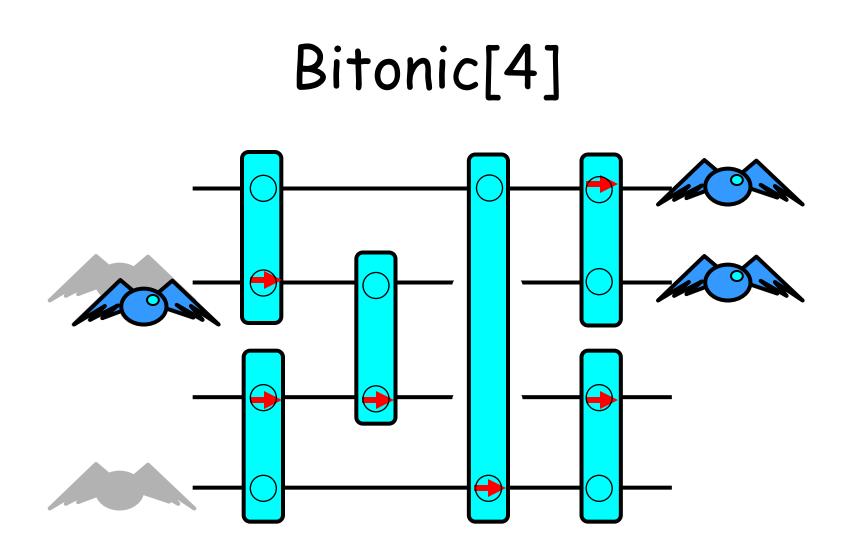
Counting Networks Count!



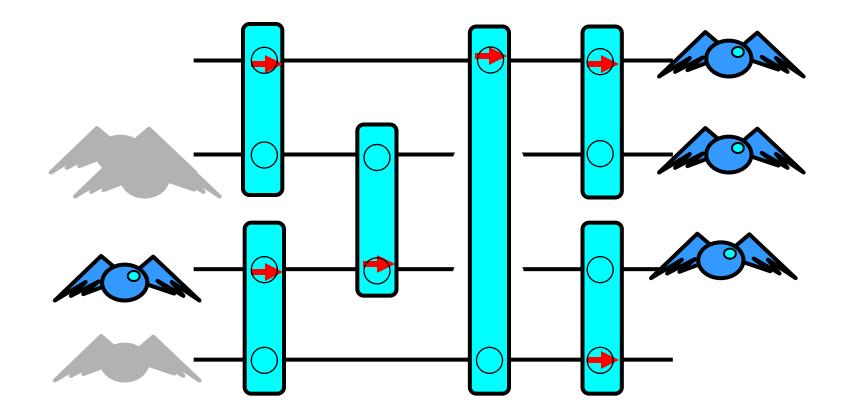
Art of Multiprocessor Programming 125

Bitonic[4] 7





Bitonic[4]



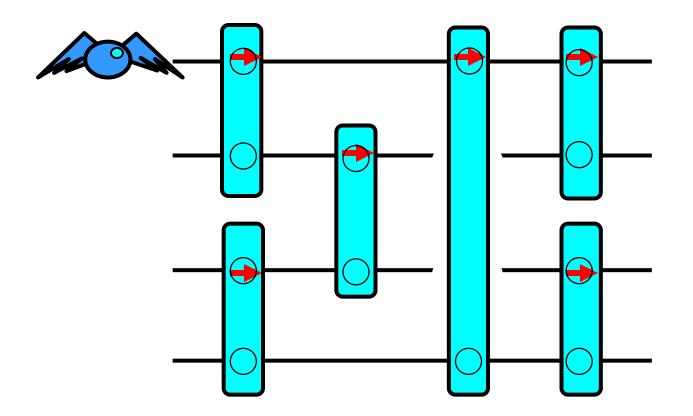
Bitonic[4] • J

Bitonic[4] 7 •

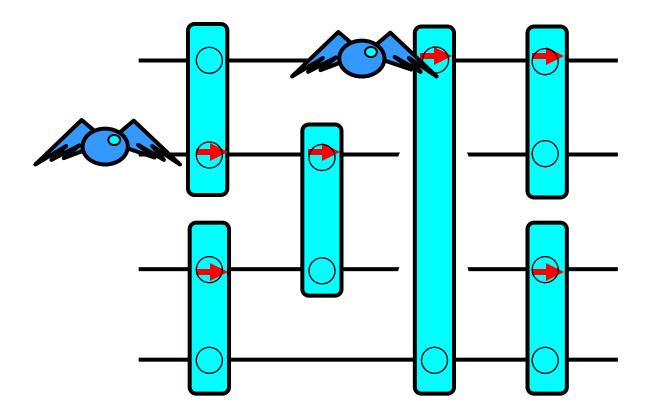
Counting Networks

- Good for counting number of tokens
- low contention
- no sequential bottleneck
- high throughput
- practical networks depth logn

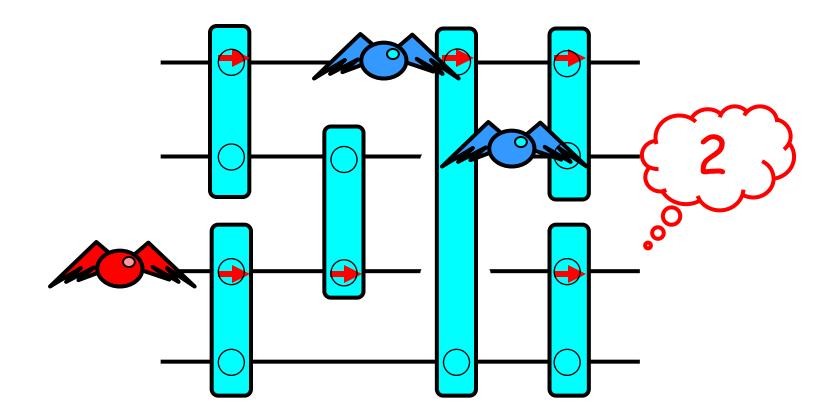
Bitonic[k] is not Linearizable

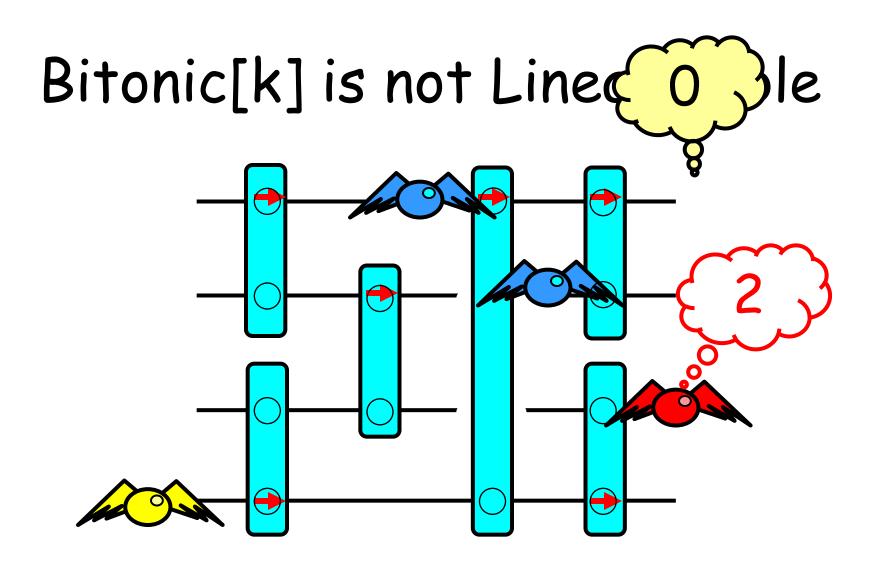


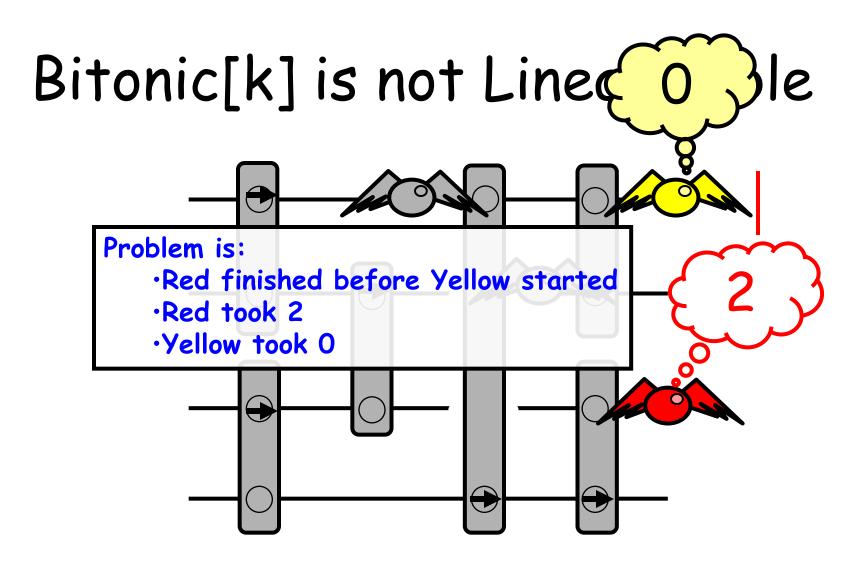
Bitonic[k] is not Linearizable



Bitonic[k] is not Linearizable



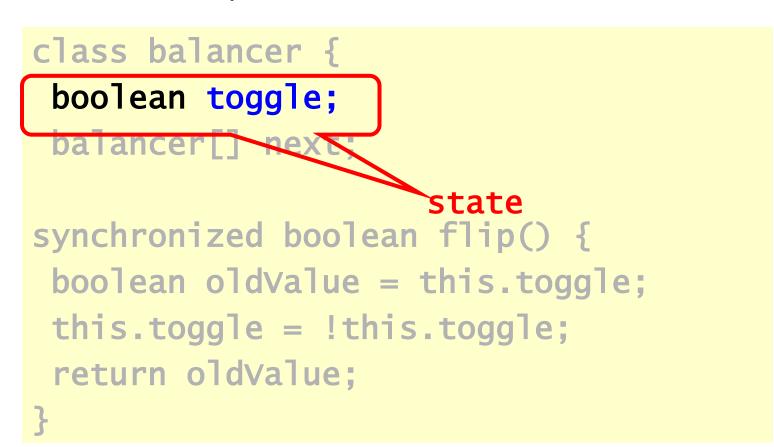


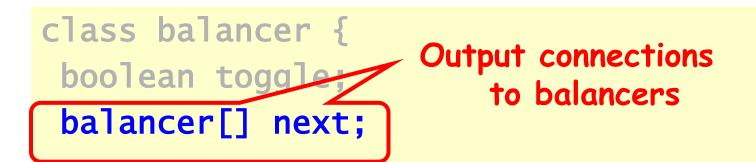


class balancer {
 boolean toggle;
 balancer[] next;

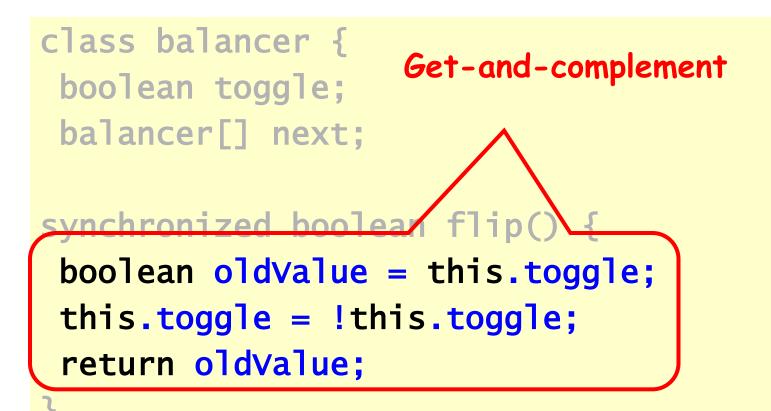
}

synchronized boolean flip() {
 boolean oldValue = this.toggle;
 this.toggle = !this.toggle;
 return oldValue;

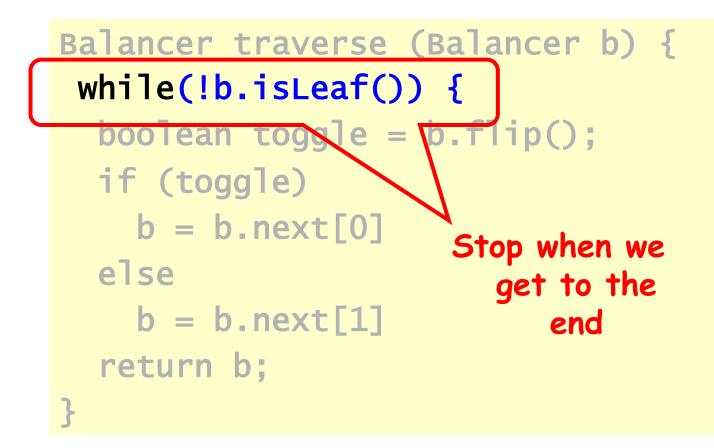


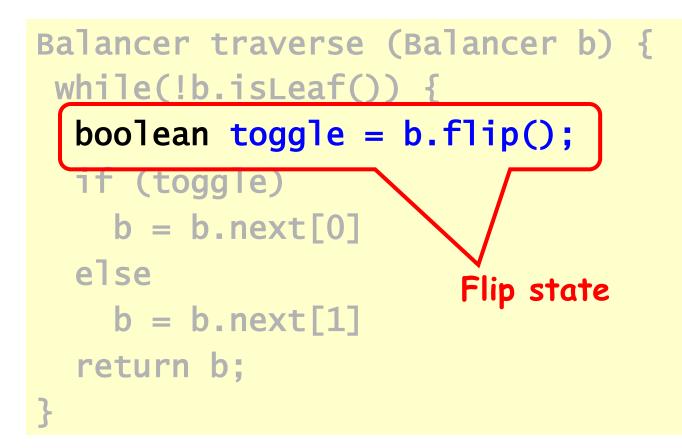


synchronized boolean flip() {
 boolean oldValue = this.toggle;
 this.toggle = !this.toggle;
 return oldValue;

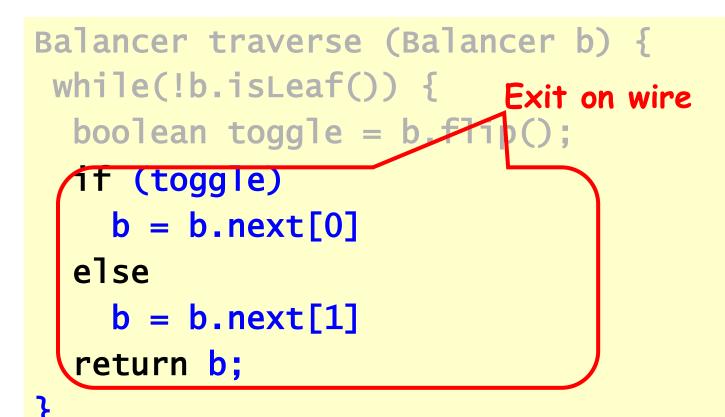


```
Balancer traverse (Balancer b) {
while(!b.isLeaf()) {
  boolean toggle = b.flip();
  if (toggle)
    b = b.next[0]
  else
    b = b.next[1]
  return b;
}
```

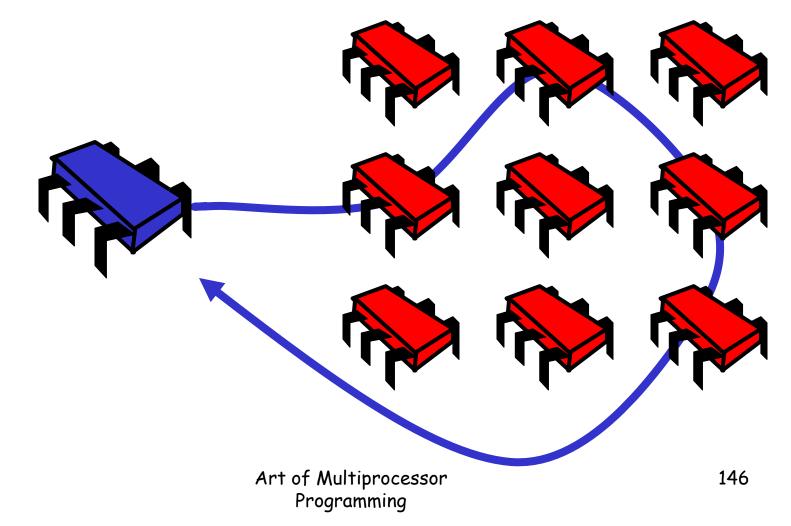




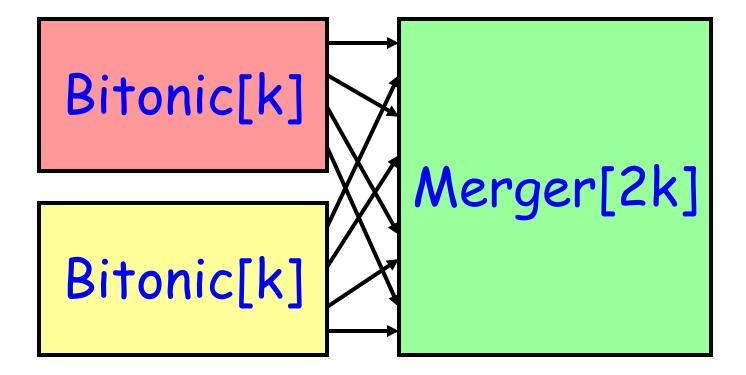
Shared Memory Implementation



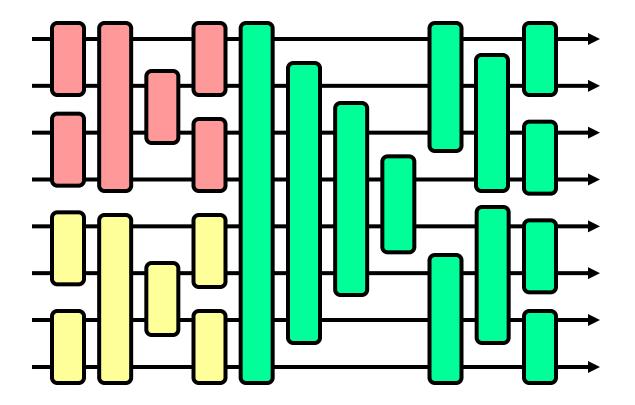
Alternative Implementation: Message-Passing

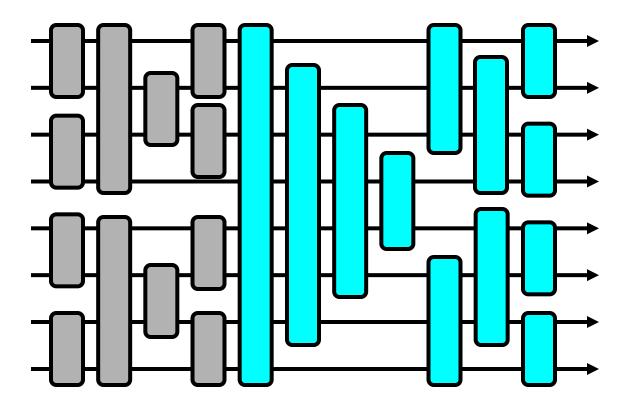


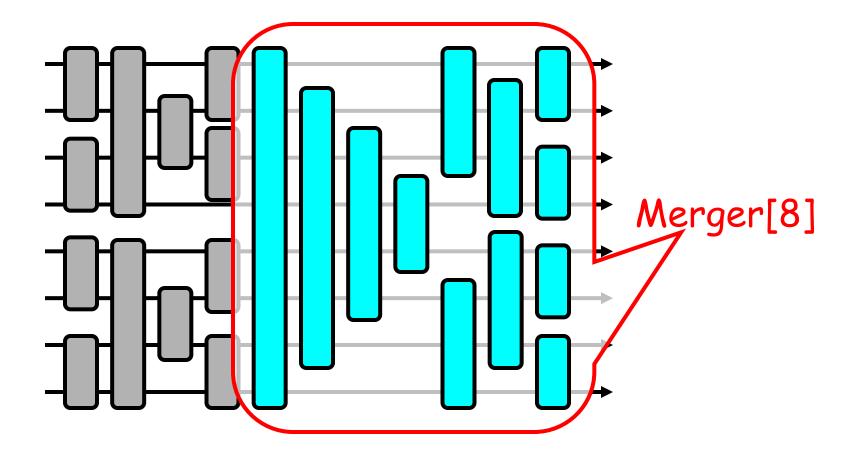
Bitonic[2k] Schematic

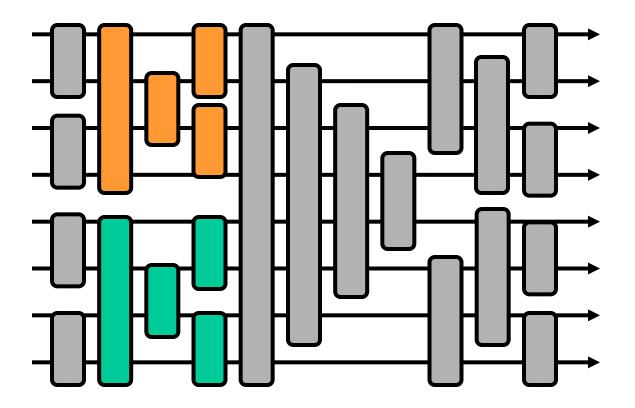


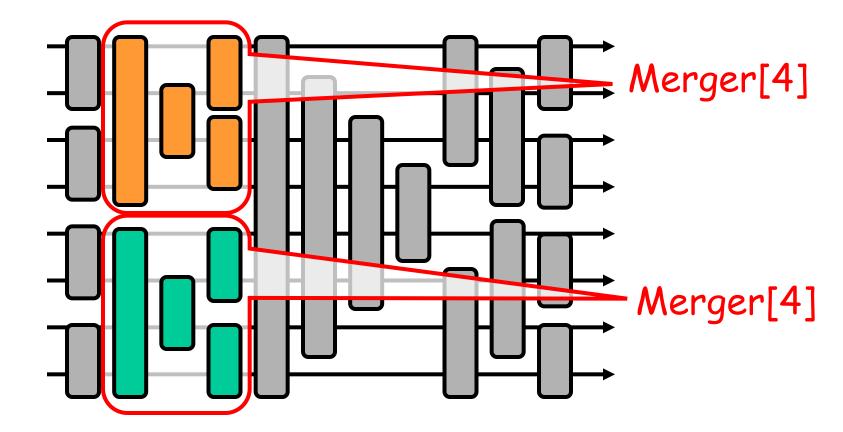
Bitonic[2k] Layout

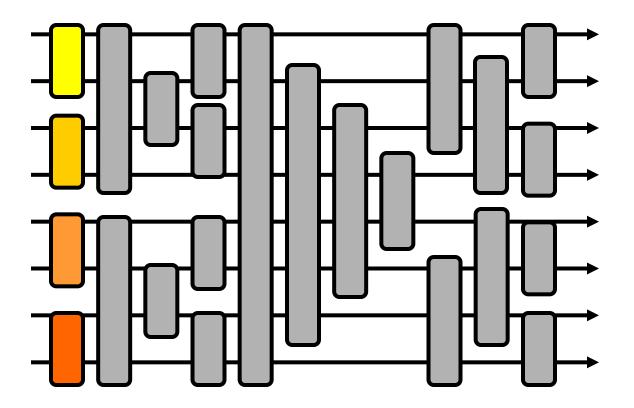


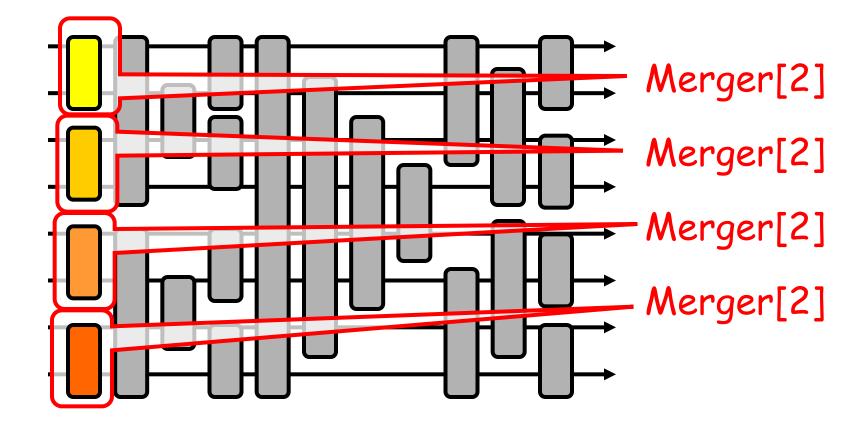








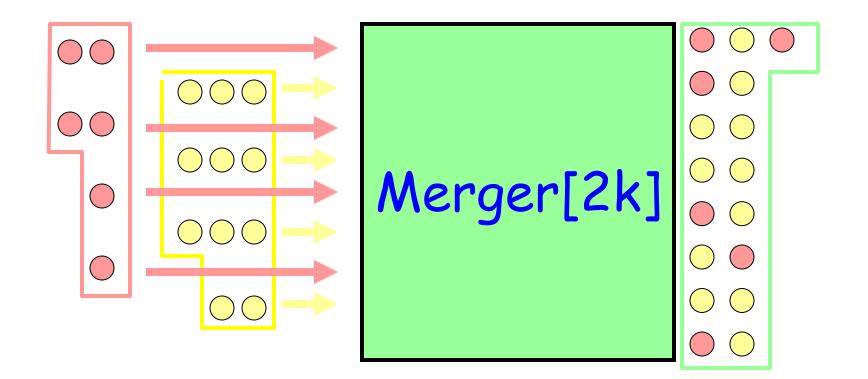




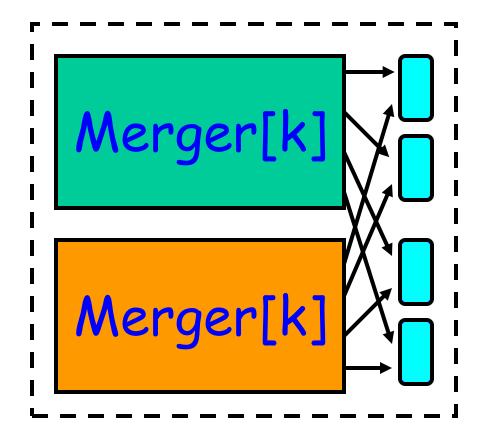
Bitonic[k] Depth

- Width k
- Depth is (log₂ k)(log₂ k + 1)/2

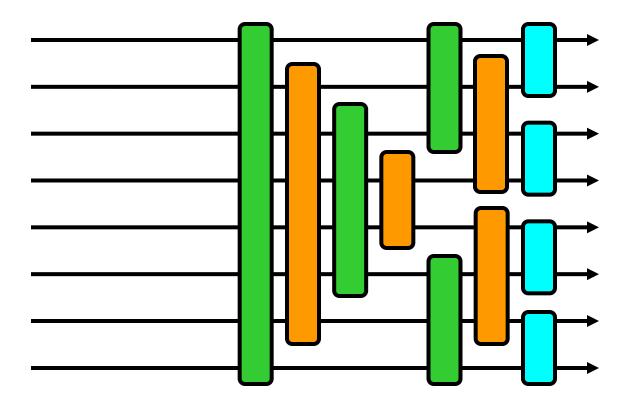
Merger[2k]



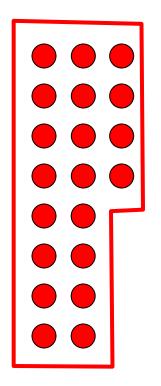
Merger[2k] Schematic



Merger[2k] Layout

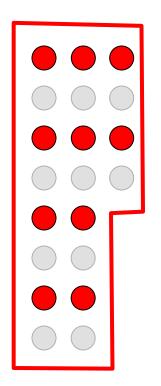


Lemma



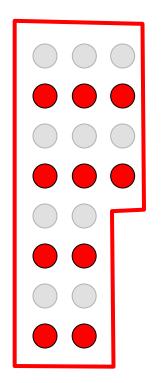
If a sequence has the step property ...

Lemma



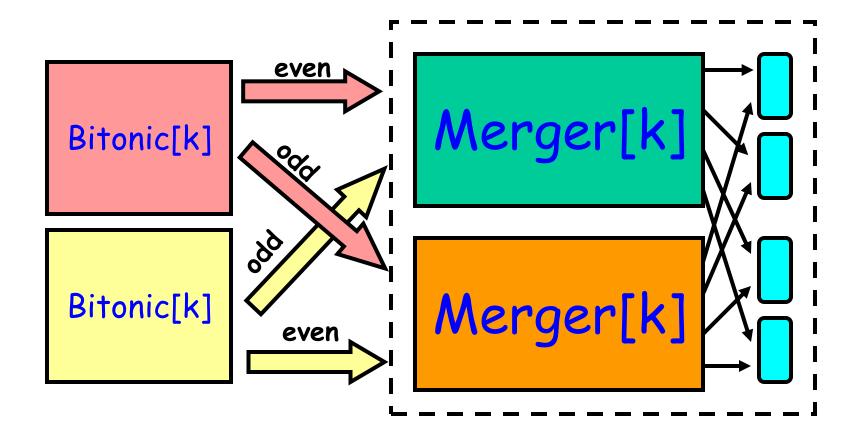
So does its even subsequence

Lemma

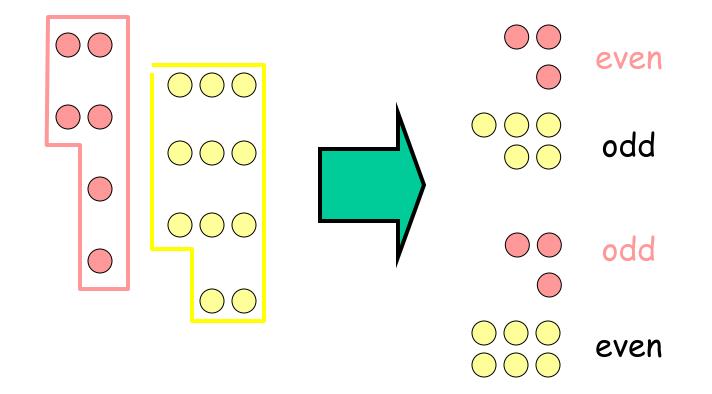


And its odd subsequence

Merger[2k] Schematic



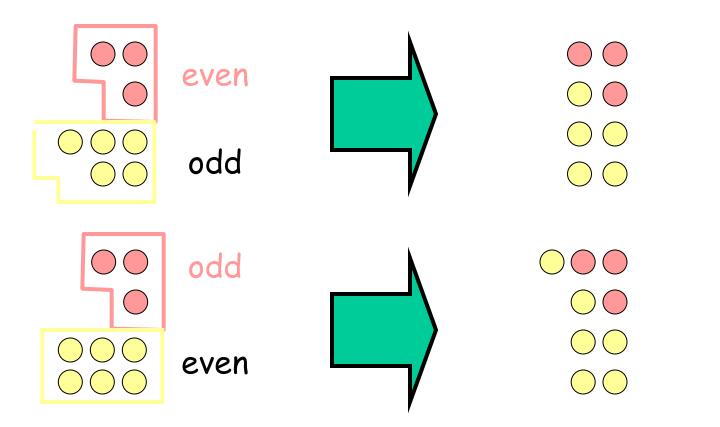
Proof Outline



Outputs from Bitonic[k]

Inputs to Merger[k]

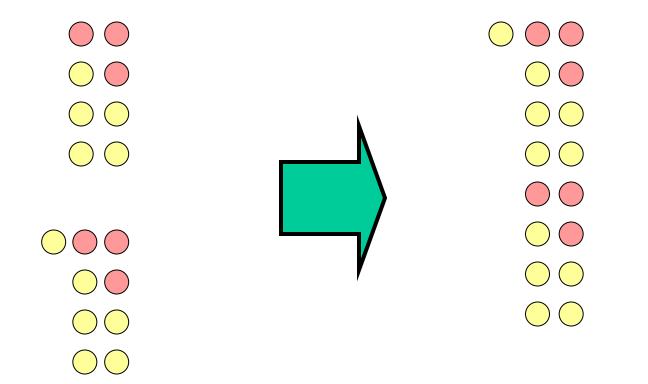
Proof Outline



Inputs to Merger[k]

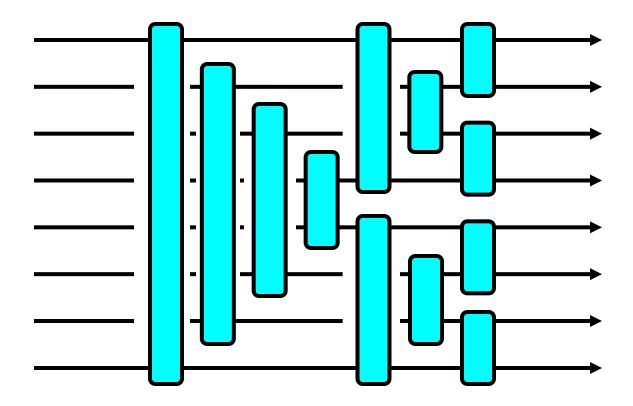
Outputs of Merger[k]

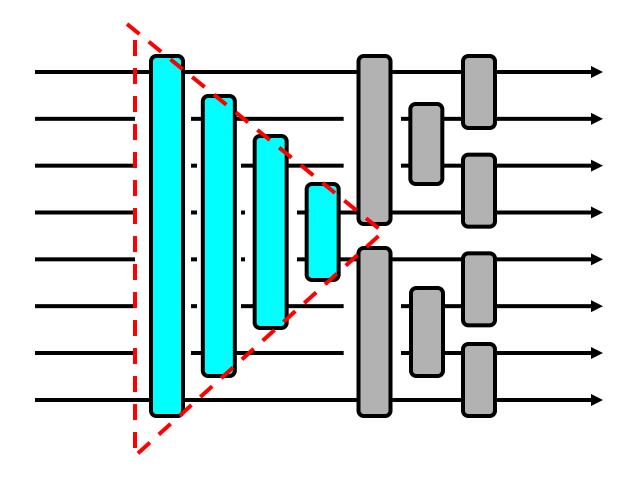
Proof Outline

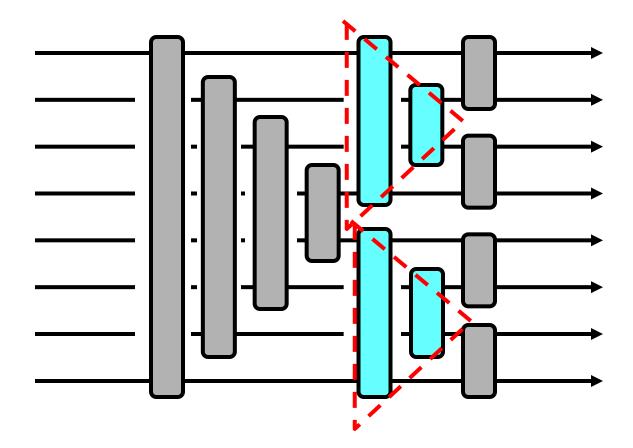


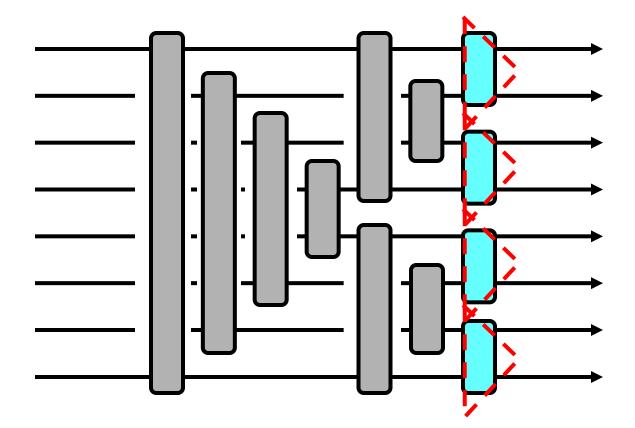
Outputs of Merger[k]

Outputs of last layer

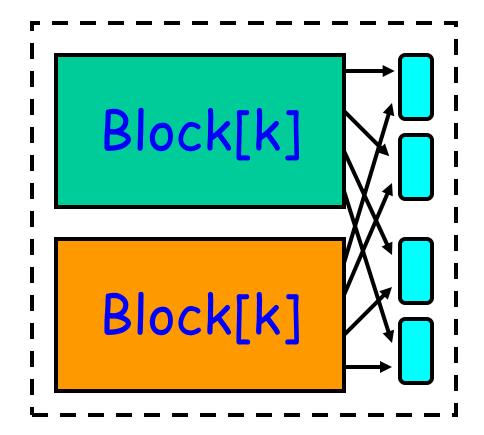




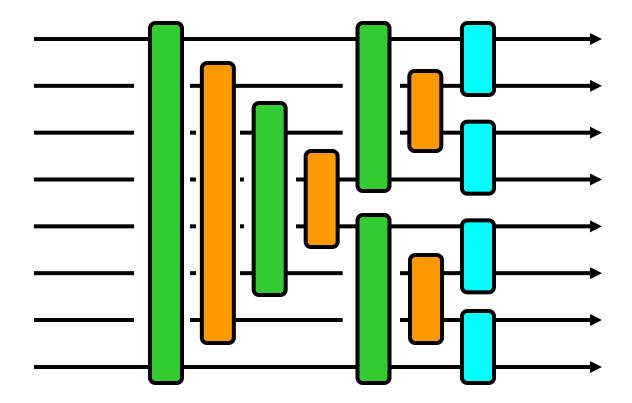




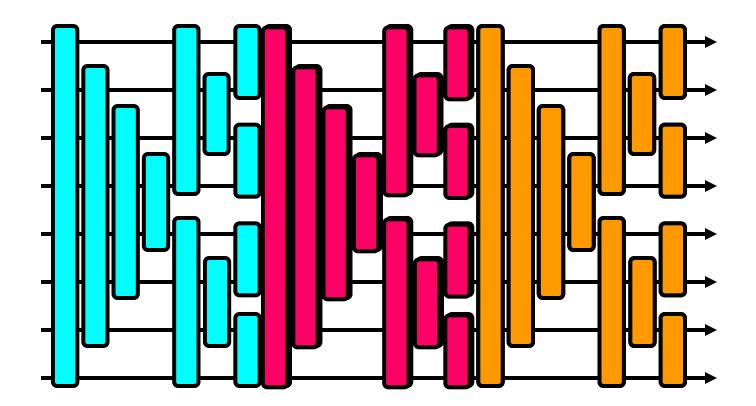
Block[2k] Schematic



Block[2k] Layout



Periodic[8]



Network Depth

- Each block[k] has depth log₂ k
- Need log₂ k blocks
- Grand total of $(\log_2 k)^2$

Lower Bound on Depth

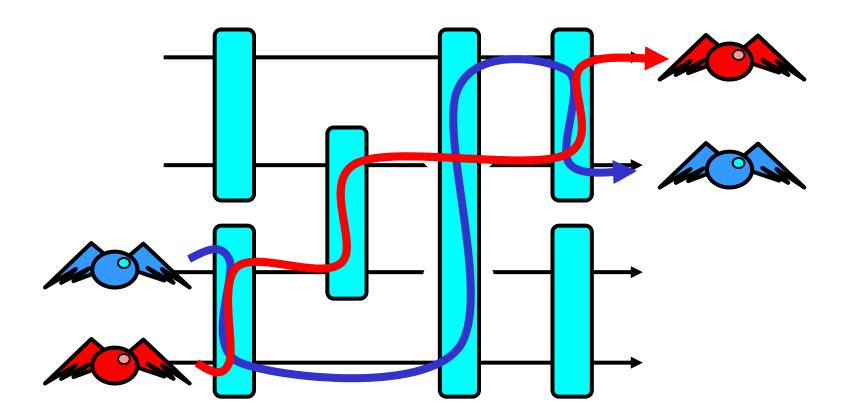
Theorem: The depth of any width w counting network is at least $\Omega(\log w)$.

- Theorem: there exists a counting network of $\Theta(\log w)$ depth.
- Unfortunately, proof is non-constructive and constants in the 1000s.

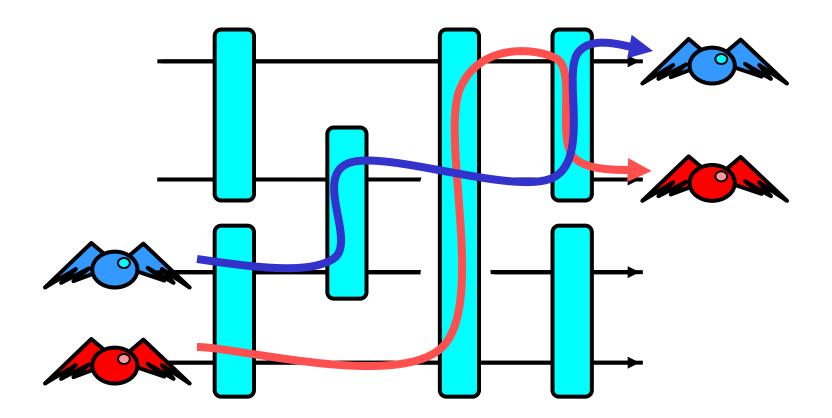
Sequential Theorem

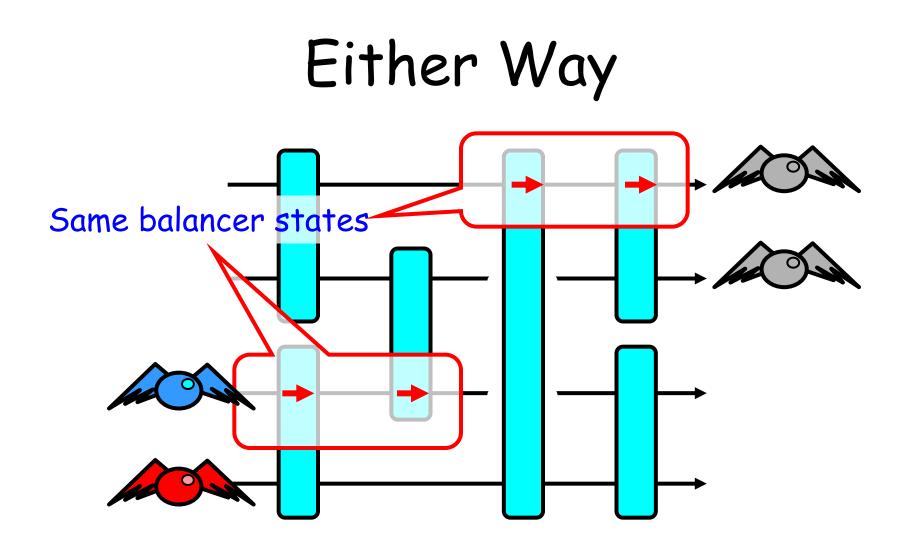
- If a balancing network counts
 - Sequentially, meaning that
 - Tokens traverse one at a time
- Then it counts
 - Even if tokens traverse concurrently

Red First, Blue Second

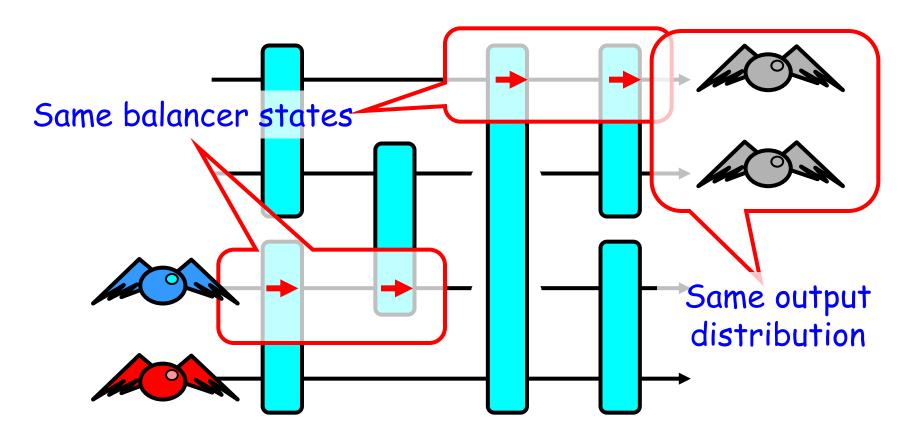


Blue First, Red Second





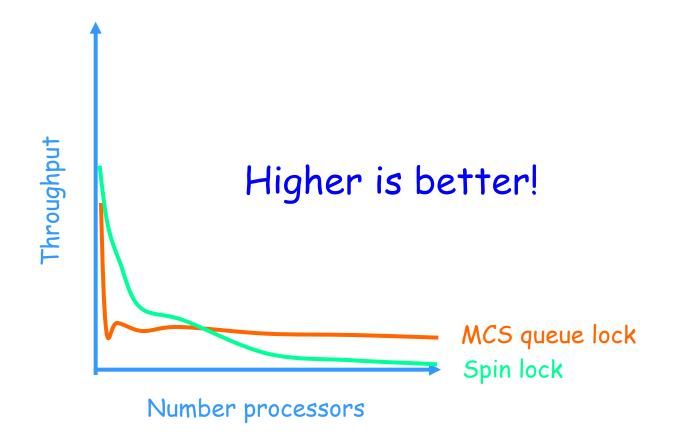
Order Doesn't Matter



Index Distribution Benchmark

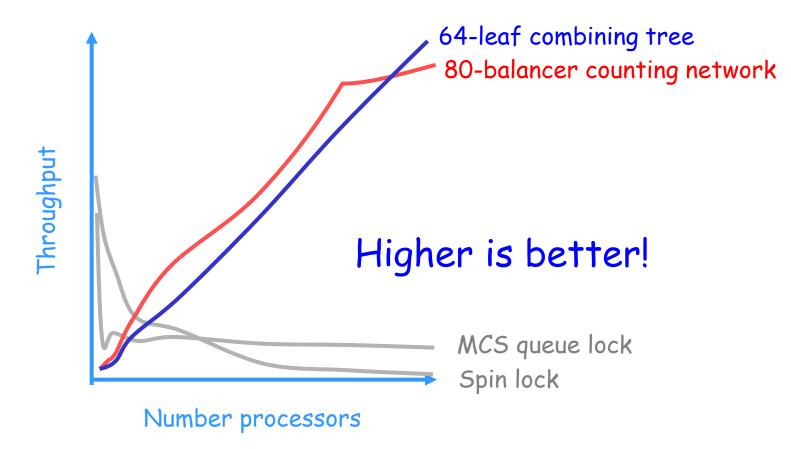
```
void indexBench(int iters, int work) {
  while (int i = 0 < iters) {
    i = fetch&inc();
    Thread.sleep(random() % work);
  }
}</pre>
```

Performance (Simulated)

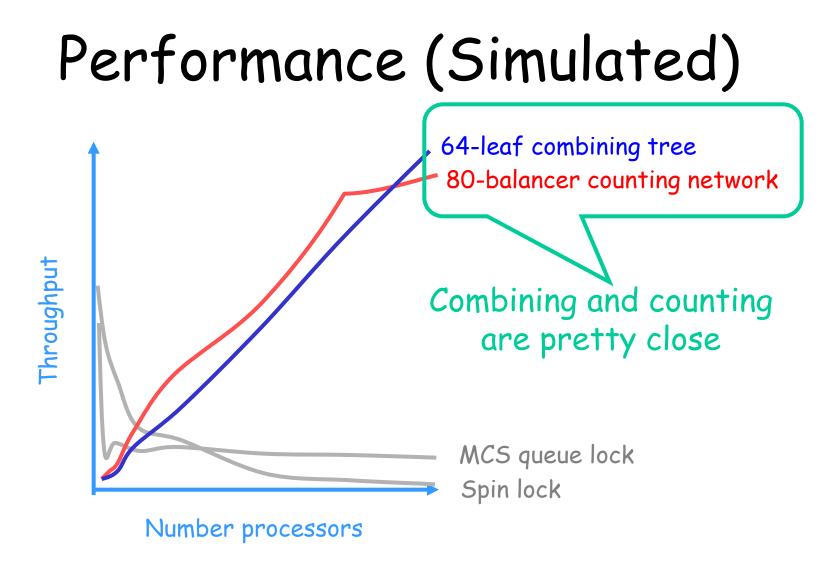


* All graphs taken from Herlihy, Lim, Shavit, copyright ACM.

Performance (Simulated)

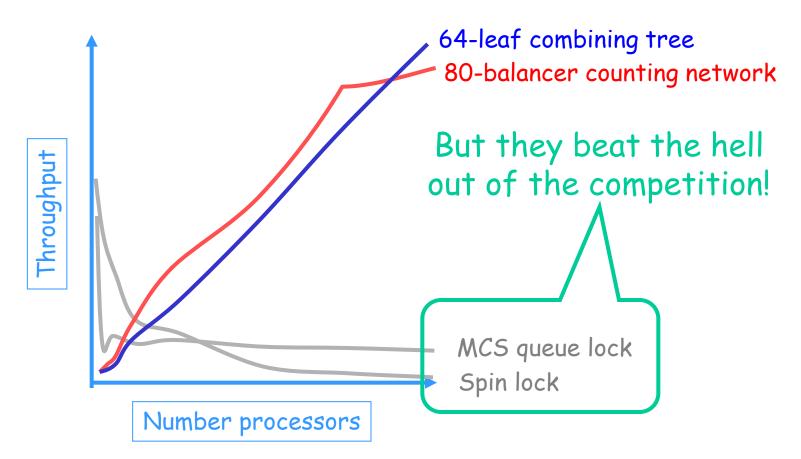


* All graphs taken from Herlihy, Lim, Shavit, copyright ACM.



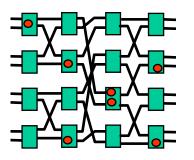
* All graphs taken from Herlihy, Lim, Shavit, copyright ACM.

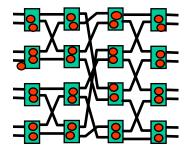
Performance (Simulated)

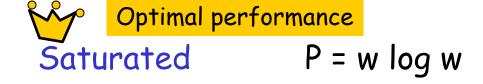


* All graphs taken from Herlihy, Lim, Shavit, copyright ACM.

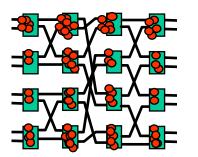
Saturation and Performance





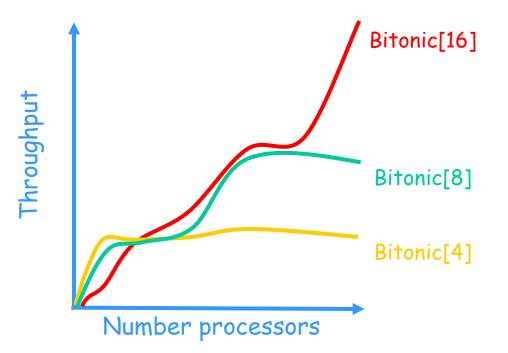


Undersaturated P < w log w

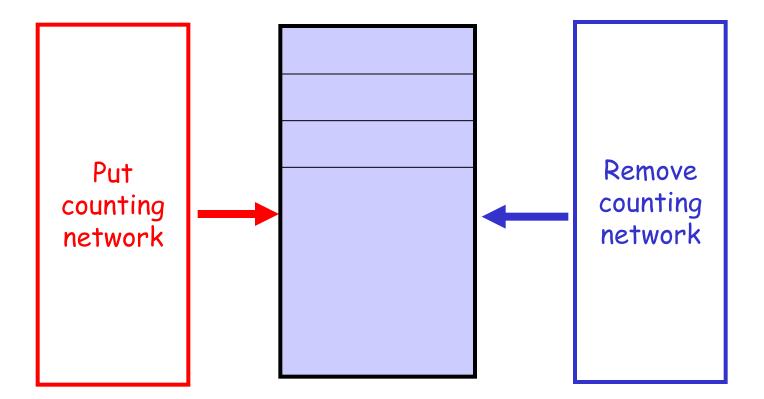


Oversaturated P > w log w

Throughput vs. Size



Shared Pool



Put/Remove Network

- Guarantees never:
 - Put waiting for item, while
 - Get has deposited item
- Otherwise OK to wait
 - Put delayed while pool slot is full
 - Get delayed while pool slot is empty

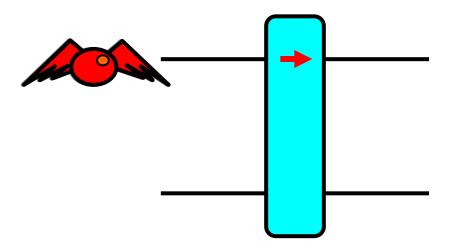
What About

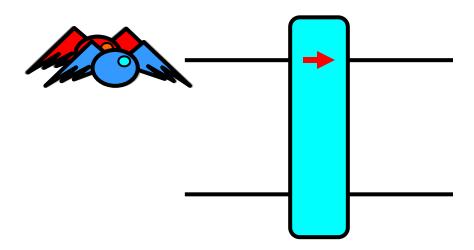
- Decrements
- Adding arbitrary values
- Other operations
 - Multiplication
 - Vector addition
 - Horoscope casting ...

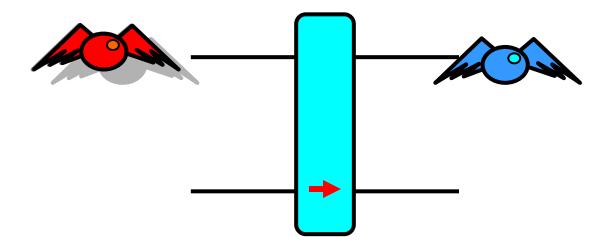
First Step

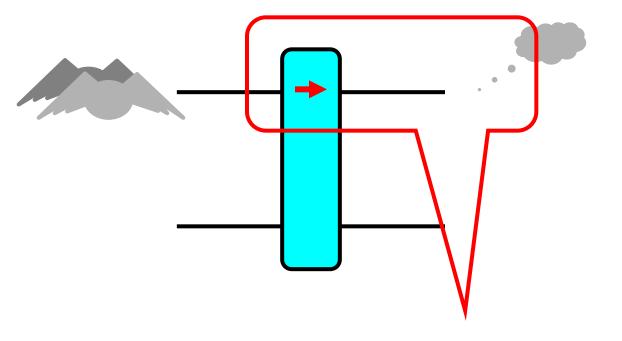
- Can we decrement as well as increment?
- What goes up, must come down ...

Anti-Tokens







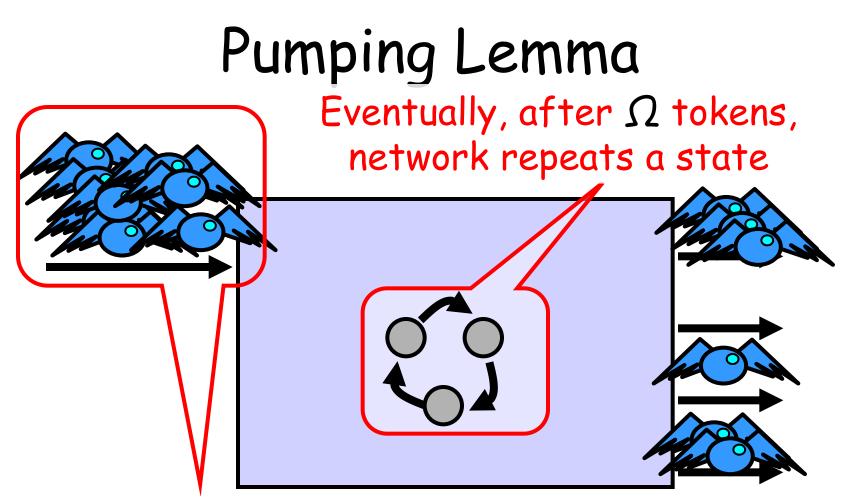


As if nothing happened

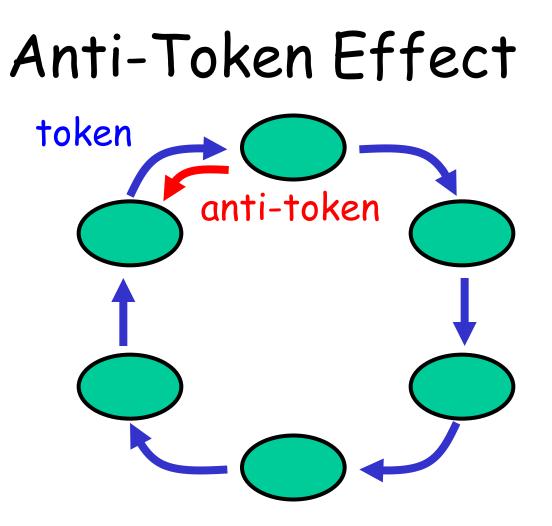
Tokens vs Antitokens

- Tokens
 - read balancer
 - flip
 - proceed

- Antitokens
 - flip balancer
 - read
 - proceed



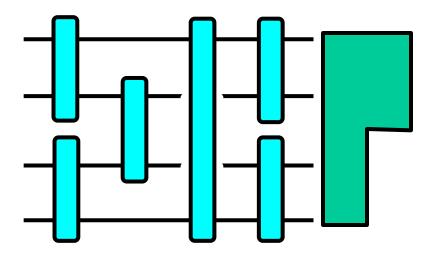
Keep pumping tokens through one wire



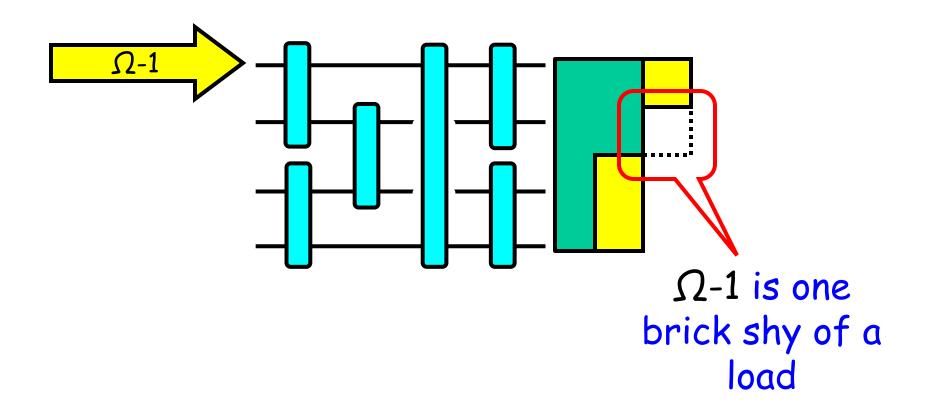
Observation

- Each anti-token on wire i
 - Has same effect as Ω -1 tokens on wire i
 - So network still in legal state
- Moreover, network width w divides Ω
 - So Ω -1 tokens

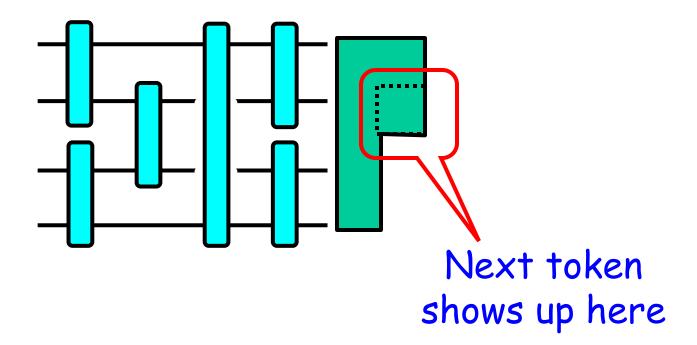
Before Antitoken



Balancer states as if ...



Post Antitoken



Implication

- Counting networks with
 - Tokens (+1)
 - Anti-tokens (-1)
- Give
 - Highly concurrent
 - Low contention



 getAndIncrement + getAndDecrement methods

Adding Networks

- Combining trees implement
 - Fetch&add
 - Add any number, not just 1
- What about counting networks?

Fetch-and-add

- Beyond getAndIncrement + getAndDecrement
- What about getAndAdd(x)?
 - Atomically returns prior value
 - And adds x to value?
- Not to mention
 - getAndMultiply
 - getAndFourierTransform?

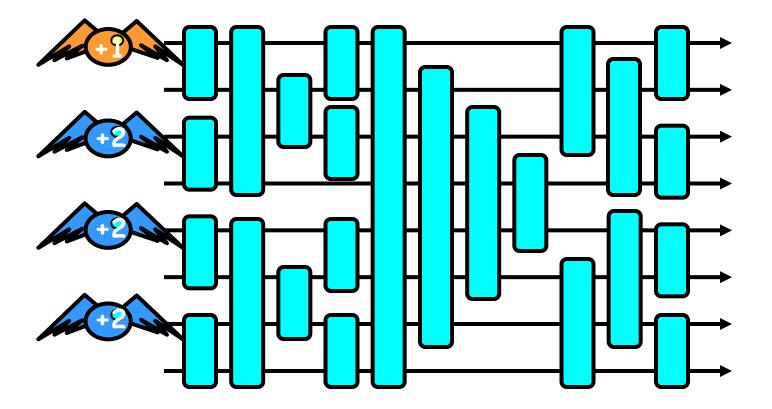
Bad News

- If an adding network
 - Supports n concurrent tokens
- Then every token must traverse
 - At least n-1 balancers
 - In sequential executions

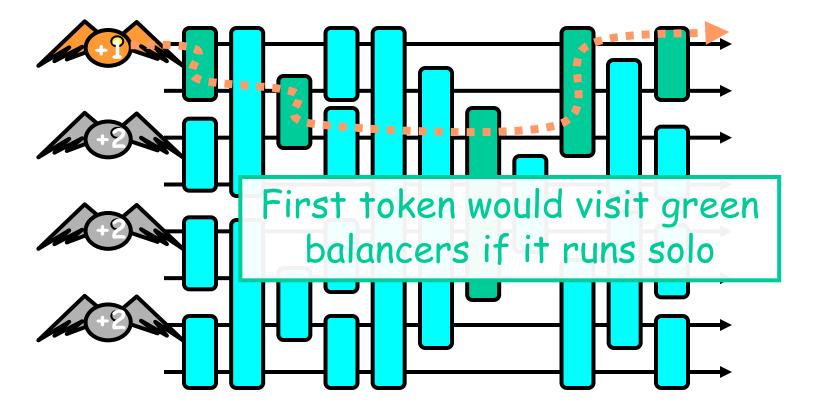
Uh-Oh

- Adding network size depends on n
 - Like combining trees
 - Unlike counting networks
- High latency
 - Depth linear in n
 - Not logarithmic in w

Generic Counting Network



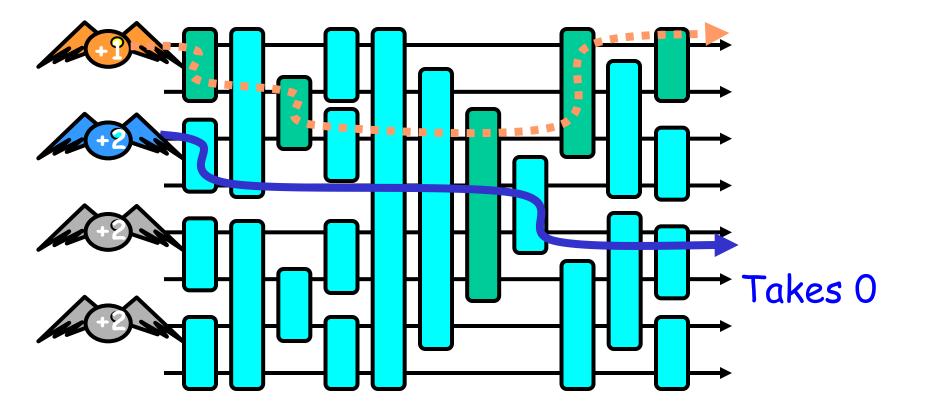
First Token



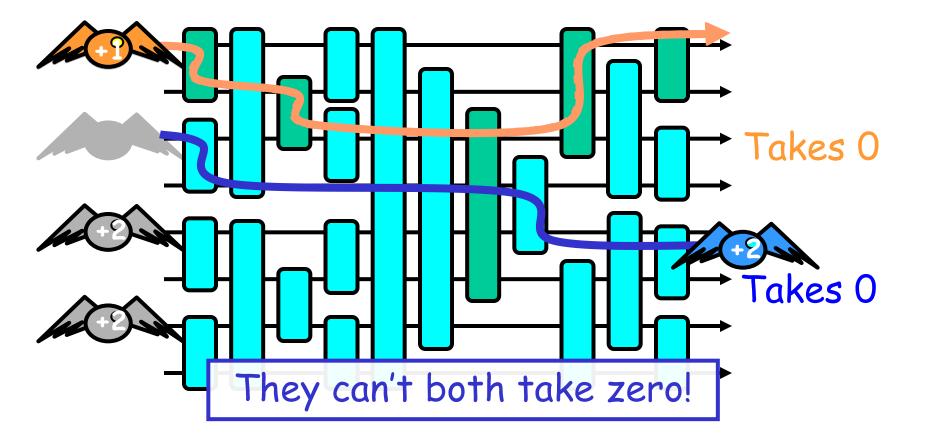
Claim

- Look at path of +1 token
- All other +2 tokens must visit some balancer on +1 token's path

Second Token



Second Token



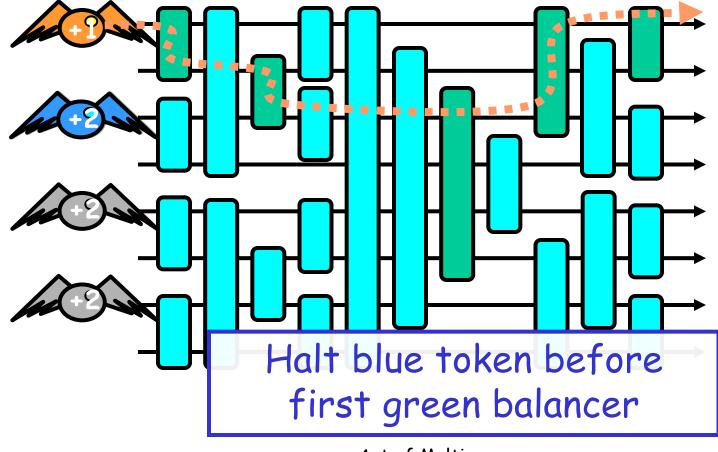
If Second avoids First's Path

- Second token
 - Doesn't observe first
 - First hasn't run
 - Chooses O
- First token
 - Doesn't observe second
 - Disjoint paths
 - Chooses O

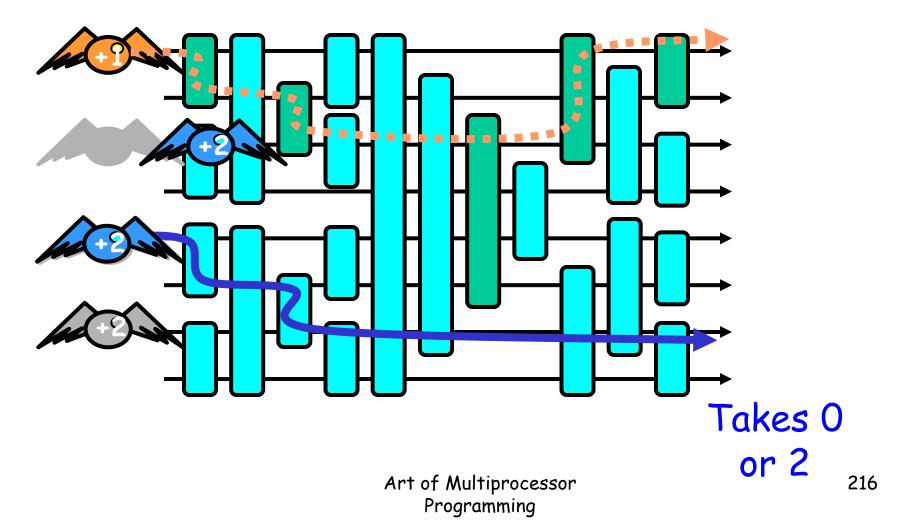
If Second avoids First's Path

- Because +1 token chooses 0
 - It must be ordered first
 - So +2 token ordered second
 - So +2 token should return 1
- Something's wrong!

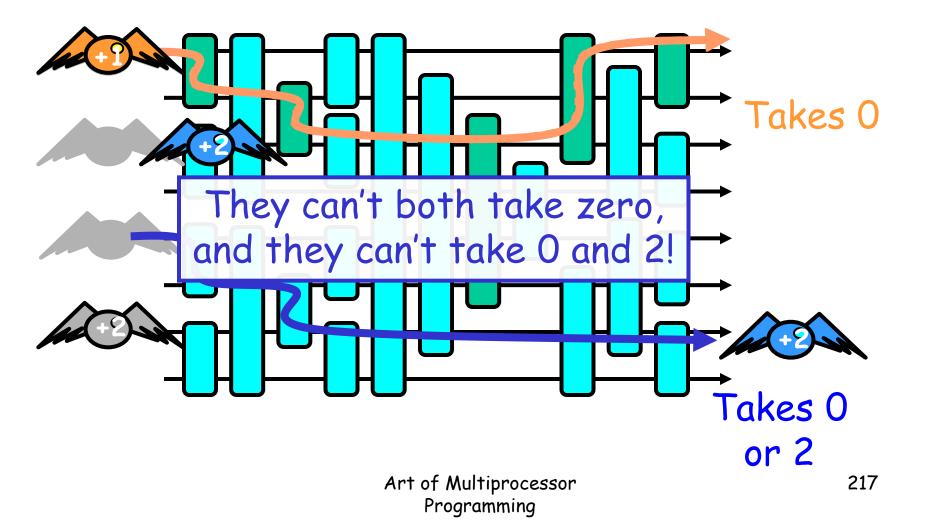
Second Token



Third Token



Third Token



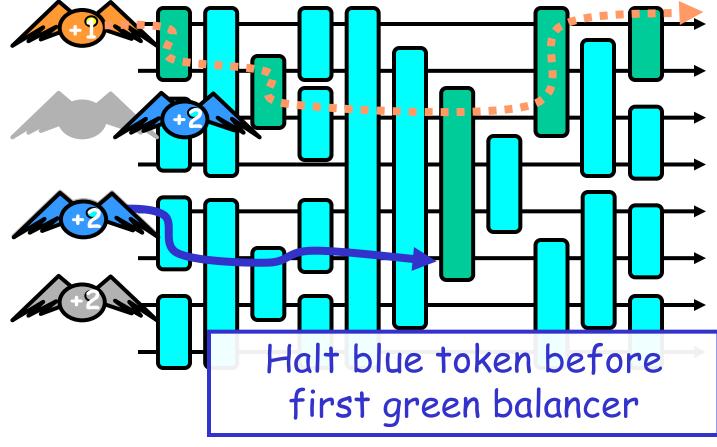
First, Second, & Third Tokens must be Ordered

- Third (+2) token
 - Did not observe +1 token
 - May have observed earlier +2 token
 - Takes an even number

First, Second, & Third Tokens must be Ordered

- Because +1 token's path is disjoint
 - It chooses O
 - Ordered first
 - Rest take odd numbers
- But last token takes an even number
- Something's wrong!

Third Token

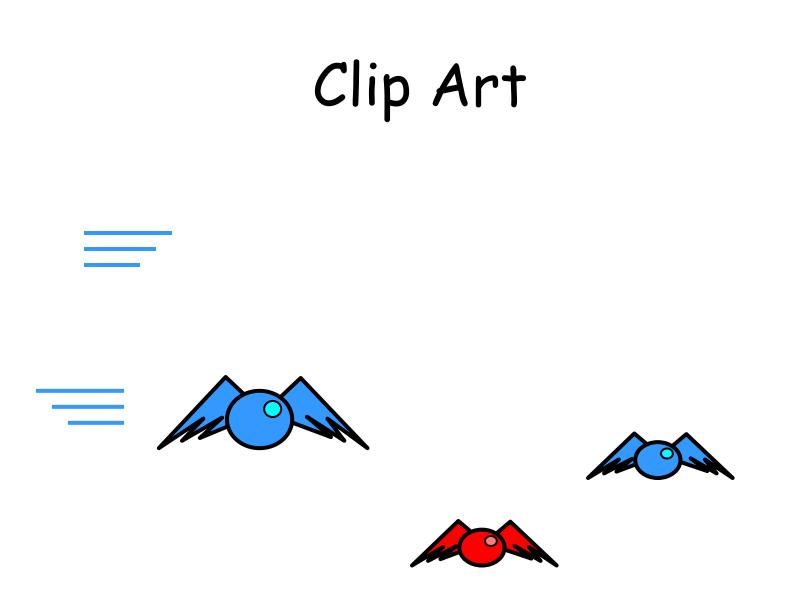


Continuing in this way

- We can "park" a token
 - In front of a balancer
 - That token #1 will visit
- There are n-1 other tokens
 - Two wires per balancer
 - Path includes n-1 balancers!

Theorem

- In any adding network
 - In sequential executions
 - Tokens traverse at least n-1 balancers
- Same arguments apply to
 - Linearizable counting networks
 - Multiplying networks
 - And others



Art of Multiprocessor Programming



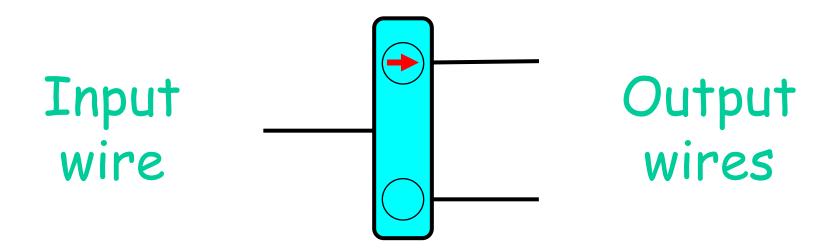
This work is licensed under a <u>Creative Commons Attribution</u> <u>ShareAlike 2.5 License</u>.

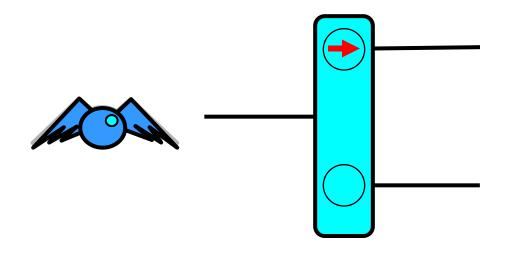
• You are free:

- to Share to copy, distribute and transmit the work
- **to Remix** to adapt the work
- Under the following conditions:
 - Attribution. You must attribute the work to "The Art of Multiprocessor Programming" (but not in any way that suggests that the authors endorse you or your use of the work).
 - Share Alike. If you alter, transform, or build upon this work, you may distribute the resulting work only under the same, similar or a compatible license.
- For any reuse or distribution, you must make clear to others the license terms of this work. The best way to do this is with a link to
 - http://creativecommons.org/licenses/by-sa/3.0/.
- Any of the above conditions can be waived if you get permission from the copyright holder.
- Nothing in this license impairs or restricts the author's moral rights.

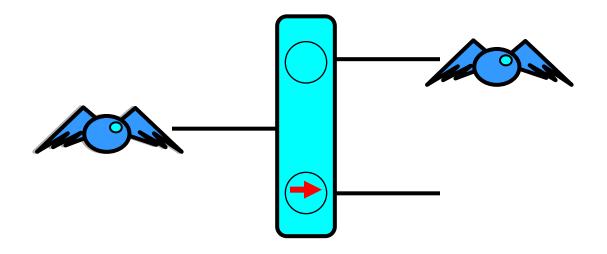
Diffracting Trees

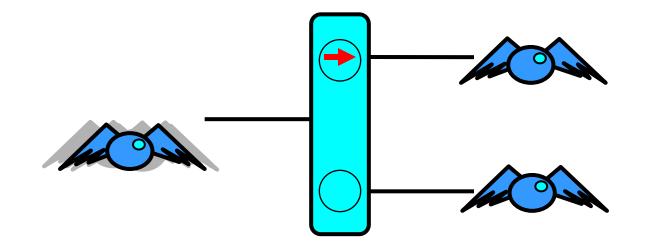
A Simple Balancer

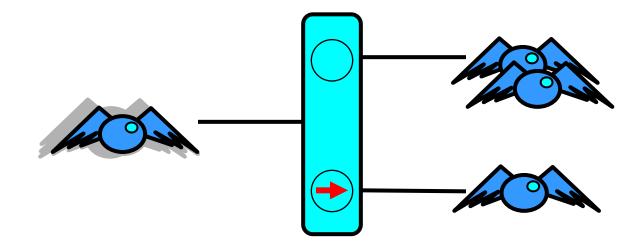


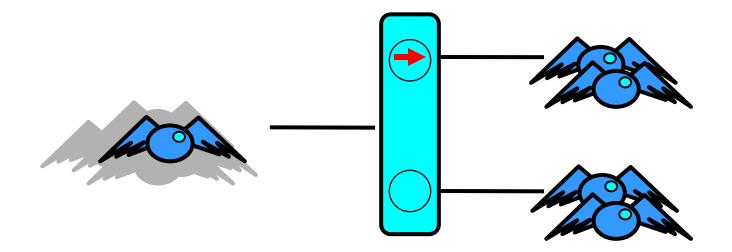


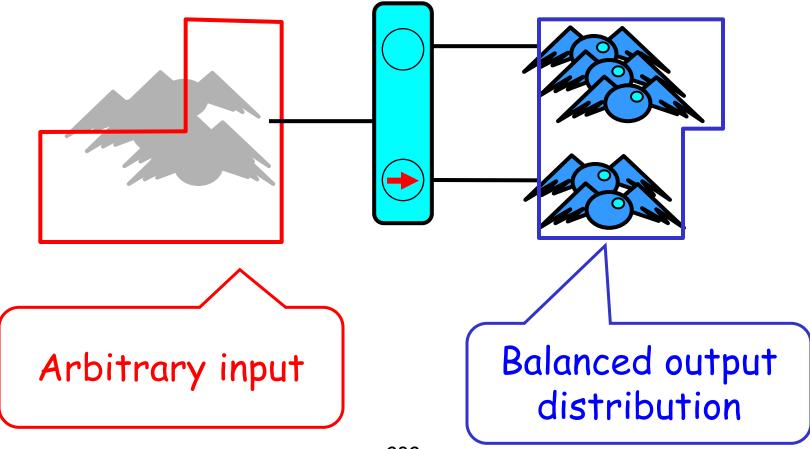
- Token i enters
- leaves on wire i mod (fan-out)





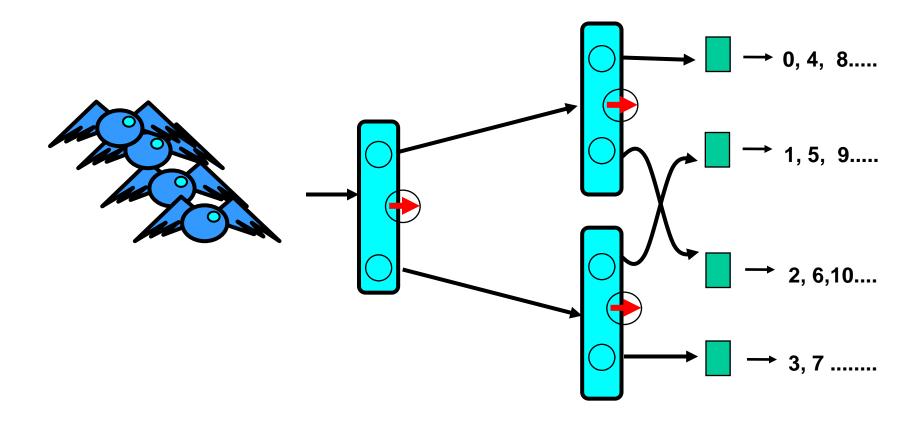


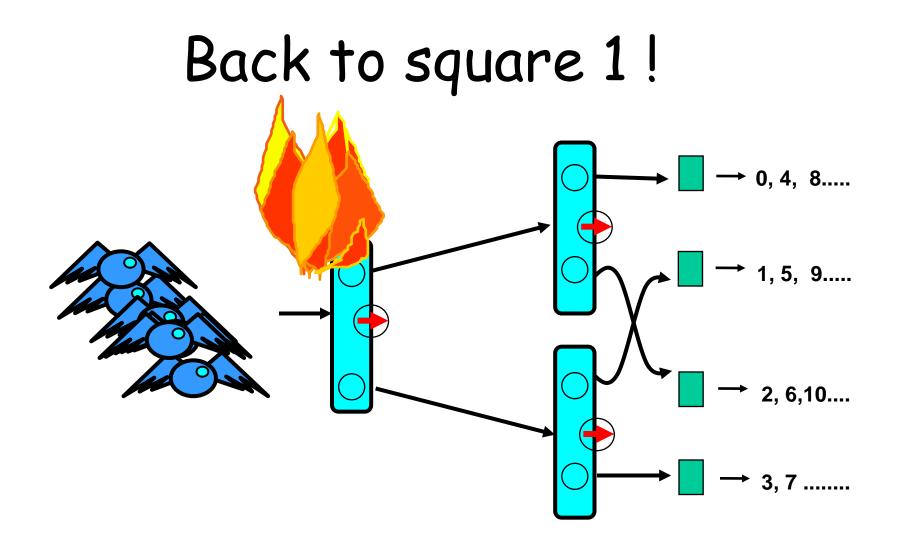




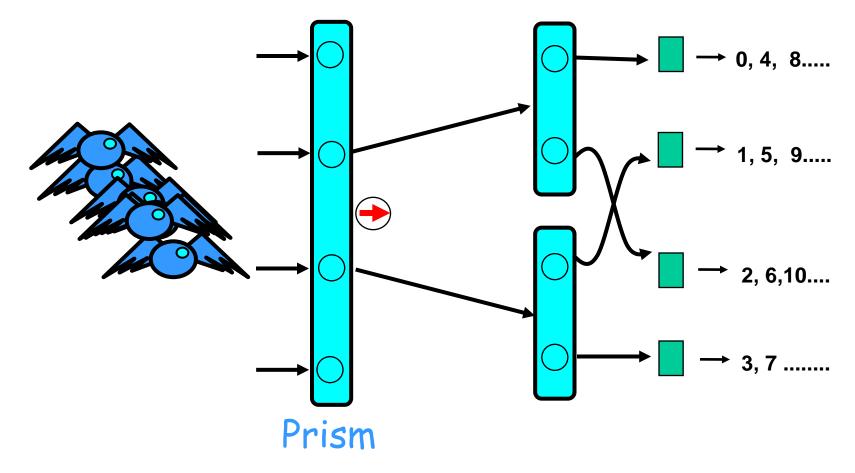
Counting Tree Step property

Counting Tree Counts!

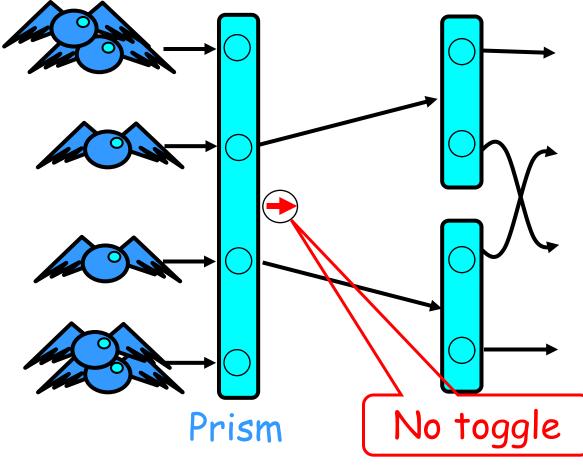




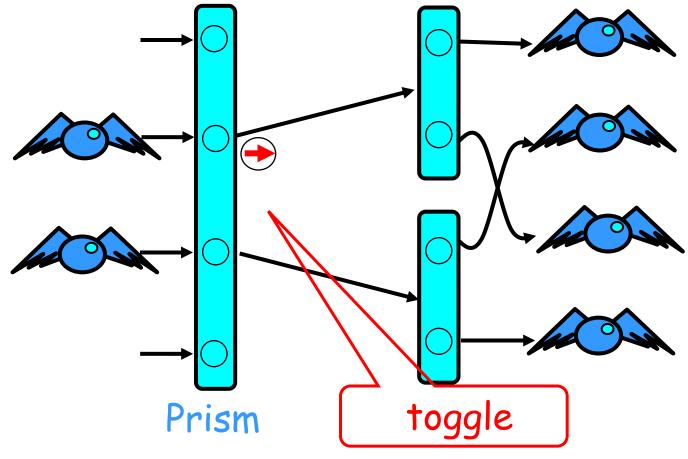
Introducing Diffracting Tree



Diffracting Tree



Diffracting Tree



Diffracting Tree

