Analog Electronic Circuits Department of Electrical and Computer Engineering Seoul National University

2014 Fall

D.K. Jeong

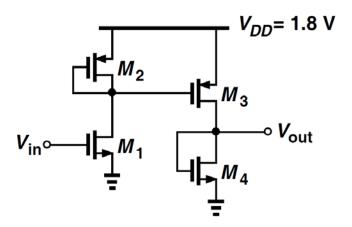
PSPICE Assignments #2

- Due: 2014/10/22(Wed) 3:30 PM

- Submit a hardcopy report.

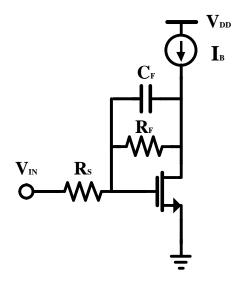
- For any questions, send an e-mail to <u>yhhwang@isdl.snu.ac.kr</u>

1. Answer the following questions. (W=10um, L=0.18um)



- a) Select the input dc level to obtain an output dc level of 0.9V.
- b) Plot the frequency response and compute the low-frequency gain and the -3dB bandwidth.
- c) Calculate the dc gain using the simulation results (dc bias voltage, drain current) and compare the result of b). Assume $\lambda p = 0.5$, $\lambda n = 0.4$.

2. Answer the following questions. (W=2um, L=0.18um, Vdd = 1.8V, Ib = 1mA, CF = 50fF, RF = 100k Ω , Rs = 1k Ω)



- a) Plot the frequency response and compute the low-frequency gain and the -3dB bandwidth when $V_{IN, bias} = 0.7V$.
- b) Repeat a) when $V_{IN, bias} = 0.8V$. Explain what the difference between the results of a) and b) is.

<End of PSPICE Assginments #2>