2007 Fall: Electronic Circuits 2

CHAPTER 8 Feedback

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Introduction

- ♦ In this chapter, we will be covering…
 - Negative Feedback
 - The General Feedback Structure
 - The Four Basic Feedback Topologies
 - Feedback in relation with Stability
 - Feedback in relation with Frequency Response



Introduction

- ♦ Two types of Feedback
 - Positive (Regenerative) Feedback
 - Negative (Degenerative) Feedback

This chapter will focus on Negative Feedback



Introduction – Negative FB



- Desensitized gain
- Reduced non-linear distortion
- Reduced effect of noise
- Controlled input and output impedance
- Extended bandwidth

 These trade-offs take place under the influence of a numeric factor called 'amount of feedback'.







As mentioned in the introduction, negative FB trades off gain for some other desired properties.

It will become apparent explicitly in this section.



Gain Desensitivity



The percentage change in A_f is smaller than the percentage change in A by the amount of feedback ($\equiv 1 + A\beta$).

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Bandwidth Extension

Consider an amplifier with a single pole, then

open - loop TF:
$$A(s) = \frac{A_M}{1 + s / w_H}$$

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$$A_{f}(s) = \frac{A(s)}{1 + A(s)\beta} \longrightarrow A_{f}(s) = \frac{1 + s / w_{H}}{1 + \frac{A_{M}}{1 + s / w_{H}} \cdot \beta} = \frac{A_{f}}{1 + s / (1 + A_{M}\beta)w_{H}}$$

 A_M

 $\therefore w_{Hf} = w_H (1 + A_M \beta)$

The upper 3-dB frequency is increased by a factor equal to the amount of feedback ($\equiv 1+A\beta$), where A_M denotes the midband gain and ω_H is the upper 3-dB frequency. However, the gain-bandwidth product remains constant.



Reduction in Nonlinear Distortion



3. The Four Basic Feedback Topologies

- There are four basic feedback topologies, namely,
 - Series-Shunt
 - Shunt-Series
 - Series-Series
 - Shunt-Shunt

Each has an aptitude on four different kinds of amplifiers discussed in Chapter 1. Correct application of feedback topology idealizes the amplifier's input/output impedance.

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3. The Four Basic Feedback Topologies

- The term 'shunt' is an another expression for 'parallel'.
- Why are the names like that?
 - The word before the dash describes how the feedback signal is 'mixed' into the input.
 - The word after the dash describes how the feedback signal is 'sampled' from the output.
 - Voltage is mixed in series and sampled in parallel.
 - Current is mixed in parallel and sampled in series.
 - Ex) Series-shunt mixes in series and samples in parallel.
 So both input and output has to be a voltage.

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3. The Four Basic Feedback Topologies





Review of Two Port Network



1) h param.(series-shunt) – I_1 and V_2 are the stimuli



2) z param.(series-series) – I_1 and I_2 are the stimuli



3) y param.(shunt-shunt) – V_1 and V_2 are the stimuli



4) g param.(shunt-series) – V_1 and I_2 are the stimuli



- Series-Shunt (voltage amp)
 - => Input is mixed in voltage(series), and output is sampled in voltage(shunt).
 - Whenever voltage is mixed, the input impedance is increased by the amount of feedback.
 - Whenever voltage is sampled, the output impedance is reduced by the amount of feedback.



♦ Ideal Situation (w/o load and source res.)





Practical Situation

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(b) Represented by h parameters



A circuit

(c) Neglecting h21 (similar to ideal case)

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Example 8.1

Q: find the expression s for $A, \beta, V_o / V_s, R_{in}$ and R_{out} .



Example 8.1(Cont'd)

It sample voltage and mixes voltage, so series-shunt...







- Series-Series (transconductance amp.)
 => Input is mixed in voltage(series), and output is sampled in current(series).
 - Whenever voltage is mixed, the input impedance is increased by the amount of feedback.
 - Whenever current is sampled, the output impedance is increased by the amount of feedback.





Ideal Situation(Cont'd)



Practical Situation





Example 8.2

Q: find the expression s for $A, \beta, A_f, V_o / V_s, R_{in}, R_{of}$, and R_{out} .







6.1 The Shunt-Shunt Feedback Amplifier

- Shunt-Shunt (transresistance amp)
 - => Input is mixed in current(shunt), and output is sampled in voltage(shunt).
 - Whenever current is mixed, the input impedance is reduced by the amount of feedback.
 - Whenever voltage is sampled, the output impedance is reduced by the amount of feedback.






6.1 The Shunt-Shunt Feedback Amplifier Example 8.3 Q: Determine $V_o / V_s, R_{in}, R_{of}.(\beta = 100)$ +12 V $V_{c} = 0.7 + (I_{B} + 0.07)47 = 3.99 + 47 I_{B}$ $(\beta + 1)I_B + 0.07 \downarrow = (\beta + 1)I_B + 0.07$ $R_c = 4.7 \text{ k}\Omega$ $R_f = 47 \text{ k}\Omega$ 47 kΩ βI_R $R_s = 10 \text{ k}\Omega$ Rof 10 kΩ $I_B \approx 0.015 \ (mA)$ 0.07 mA $I_c \approx 1.5(mA)$ $V_c \approx 4.7(V)$ (a) (b) 9/6/2007 (c) 2007 DK Jeong 39/89



It samples voltage and mixes current, so shunt-shunt...



Example 8.3(Cont'd)



Problem 8.42(p.866)

Q: Identify t he type of feedback used and find V_o/V_s , R_{in} , and R_{out} .



Problem 8.42(p.866) (Cont'd)

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$$\frac{V_o}{V_s} = \frac{A_f}{R_s + R_1} = -3.89 (V / V)$$

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Problem 8.42(p.866) (Cont'd)



- Shunt-Series (current amp)
 - => Input is mixed in current(shunt), and output
 - is sampled in current(series).
 - Whenever current is mixed, the input impedance is reduced by the amount of feedback.
 - Whenever current is sampled, the output impedance is increased by the amount of feedback.







Example 8.4

Q: Find out I_{out}/I_{in} , R_{in} , and R_{out} . ($\beta = 100$, $V_A = 75(V)$)







(b)

-





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Exercise (Cont'd)





7. Determining The Loop Gain

Example – Approach 1.

Q. Determine the loop gain of the following circuit.



(a)

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 $\approx -\frac{R_1}{R_1 + R_2} \cdot \mu$

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7. Determining The Loop Gain

Comparison of the two results

$$\begin{split} A\beta_{1} &= \frac{R_{id}}{R_{id} + R} \cdot \mu \cdot \frac{R_{L} \left\| [R_{2} + R_{1} \right\| (R_{id} + R)]}{r_{o} + R_{L} \left\| (R_{2} + R_{1} \right\| (R_{id} + R)} \cdot \frac{R_{L} \left\| (R_{id} + R) \right\|}{R_{2} + R_{1} \left\| (R_{id} + R) \right|} \\ A\beta_{2} &= -\frac{R_{id}}{R + R_{id} + (R_{1} \| R_{2})} \cdot \mu \cdot \frac{R_{L} \left\| (R_{1} + R_{2}) \right\|}{r_{o} + R_{L} \left\| (R_{1} + R_{2}) \right|} \cdot \frac{R_{1}}{R_{1} + R_{2}} \end{split}$$

When $R_{id} >> (R_1, R_2)$, $r_0 \approx 0$, both values come to

$$A\beta_1 \cong A\beta_2 \cong -\frac{\mu}{R_1 + R_2}$$

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7. Determining The Loop Gain

Example – Approach 3. Shunt-Shunt FB





Feedback systems do not always have a tendency to stabilize.

Under some conditions, the system will diverge and oscillate.



◆ In an iterative process inside the loop…





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Problem 8.63(p.869)

Q. Find the value of k above which the closed-loop amplifier becomes unstable.

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Given
$$\longrightarrow A(s) = \frac{1000}{1 + \frac{s}{10^4}}$$
 $\beta(s) = \frac{k}{(1 + \frac{s}{10^4})^2}$

Nyquist plot example

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Because the magnitude of the loop gain is an even function and the phase is an odd function, the nyquist plot for negative frequency is a mirror image of nyquist plot of the positive frequency.

If the nyquist plot intersects the real axis on the left of (-1, 0), then the system is unstable. -> The plot encircles the point (-1,0), thus the system is unstable.

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Stability and pole location

Amplifier with one pole

Amplifier with two poles

9. Effect of Feedback on The Amplifier Poles

Example 8.5 (Cont'd)

$$\frac{V_{O}}{V_{S}} = \frac{K \cdot \omega_{O}^{2}}{s^{2} + \omega_{O}^{2}/Q} \cdot s + \omega_{O}^{2} \qquad Q = \frac{1}{3 - K}, \qquad \omega_{O}^{2} = \frac{1}{R^{2}C^{2}}$$

 $Q = \frac{1}{\sqrt{2}} \cong \mathbb{C}$ maximally flat – no peaking !!

$$\therefore \frac{1}{3-K} = \frac{1}{\sqrt{2}}$$

$$3-K=\sqrt{2}$$

$$K = 3 - \sqrt{2} = 1.586$$

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♦ Bode Plot



Gain margin and phase margin



Conditions for Stability

- Stable = Phase margin is greater than 0.
- However, system with phase margin close to 0 suffers from severe peaking in its closed loop-gain. (freq. domain)
- Typically, system with phase margin above 45[•] is well accepted to be stable.



♦ Phase margin of 45[•]

 $=\frac{1}{1+e^{-j\theta}}$

$$loopgain = A(j\omega_1)\beta = 1 \times e^{-j\theta}$$
 \leftarrow Loop gain is unity at ω_1 .

(Where $\theta = 180^{\circ}$ - phase margin)

$$A_{f}(j\omega_{1}) = \frac{A(j\omega_{1})}{1 + A(j\omega_{1})\beta} \longrightarrow |A_{f}(j\omega_{1})| = \frac{\beta}{|1 + e^{-j\theta}|}$$
$$\frac{1}{\beta}e^{-j\theta} \qquad |\text{If the phase margin is 45}^{+}$$

$$-> \theta = 135$$

$$\left|A_f(j\omega_1)\right| = 1.3\frac{1}{\beta}$$

Closed loop gain peaks by 30% at ω_1 from the low frequency gain of 1/ β . Peaking would reach ∞ as phase margin approaches 0⁺.

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- Alternative approach for Investigating Stability
 - It would be very tiresome to find the best value for β by numerical iteration.
 - There is an alternative method to graphically estimate the value of β by using,

$$20\log|A(j\omega)| - 20\log\frac{1}{\beta} = 20\log|A\beta| = 1000$$
 gain in dB



Q: System has poles at 10⁵ Hz, 10⁶Hz, and 10⁷Hz. Find β that yields phase margin of 72[•]?



- Draw 20 log |A(jw)|
 Draw the phase graph
 Draw a line for 20 log 1//
- Draw a line for 20 log 1/β so as it intersects 20 log|A(jw)| at the frequency that gives the needed phase margin.

The area enclosed by 20 log |A(jw)| and 20 log $1/\beta$ redrawn with 20 log $1/\beta$ line as f-axis becomes the graph of the loop gain (20 log $|A(jw)\beta|$).

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Rule of thumb for stability



To guarantee stability, the 20 log $1/\beta$ should intersect the 20 log |A| on its -20 dB/dec segment. (then the phase margin > 45⁻)

More generally, if β is a function of frequency...

The difference of slopes (= rate of closure) at the intersection of 20 log 1/β(jw) and 20 log |A(jw)| should not exceed 20dB/dec.

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Theory

 A system can be made stable by introducing a new pole or moving the location of its existing pole.



Implementation

 Assume that the first pole f_{p1} is introduced at the interface between the two stages.





• A miller effect is used in ICs to minimize $C_c(C_f)$.





Miller comp.& pole splitting(Cont'd)

$$\omega_{P_1}' = \frac{1}{C_1 R_1 + C_2 R_2 + C_f (g_m R_1 R_2 + R_1 + R_2)} \cong \frac{1}{g_m R_2 C_f R_1}$$

$$_{2}' \cong \frac{g_{m}C_{f}}{C_{1} + C_{2} + C_{f}(C_{1} + C_{2})}$$

 \mathcal{O}_{P}

As $C_f \uparrow$, $\omega'_{p1} \downarrow$ and $\omega'_{p2} \uparrow$: Pole Splitting

Miller effect

This method not only reduces the size of the needed compensation capacitance with miller effect, but also sends the second pole to a higher frequency. -> Wider bandwidth.

Two goods in one package!!

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Example 8.6

Q: Find the value of C_f needed to make the amplifier with following

open - loop characteri stic stable for $\beta \leq 1$.



♦ Example 8.6(Cont'd) – case 1.



