

# Oxidation

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# Usage of oxide (1)

- Mask against dry or wet etch
- Mask against implant or diffusion of dopant into Si
- Electrical isolation for device isolation
- Gate oxide in MOS structures •
- Surface passivation (corrosion, impurity, stress etc)





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### **Techniques of oxidation**

- RT ~ 200 °C
  - wet anodization, CVD, sputtering
- 250 ~ 600 °C
  - CVD (SiH<sub>4</sub> + O<sub>2</sub>  $\rightarrow$  SiO<sub>2</sub> + 2H<sub>2</sub>)
- 600 ~ 900 °C
  - CVD (pyrolysis of  $Si(OC_2H_5)_4$ ,  $SiH_4$ ,  $SiCl_4$ )
- 900 ~ 1200 °C
  - THERMAL OXIDATION



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#### Growth of thermal oxide

- Thermal oxidation consumes the substrate silicon.
  - Dry Oxidation : Si(s) +  $O_2(g) \rightarrow SiO_2(s)$
  - Wet Oxidation : Si(s) +  $2H_2O(v) \rightarrow SiO_2(s) + H_2(g)$ 
    - 45 % silicon oxidation  $\rightarrow$  100 % SiO<sub>2</sub>





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#### **Thermal oxide properties**

• Thermal oxide properties

DC Resistivity (Ω cm), 25°C Density (g/cm <sup>3</sup> )	10 <sup>14</sup> - 10 <sup>16</sup> 2.27	Melting Point (°C) Molecular Weight	~1700 60.08
Dielectric Constant	3.8 - 3.9	Molecules (/cm <sup>3</sup> )	$2.3 \times 10^{22}$
Dielectric Strength (V/cm)	5 - 10 x 10 <sup>6</sup>	<b>Refrctive Index</b>	1.46
Energy Gap (eV)	~ 8	Specific Heat (J/g °C)	1.0
Etch rate in BHF (Å/min)	1000	Stress in film on Si	<b>2 - 4</b> x 10 <sup>9</sup>
Infrared Absorption Peak	9.3	(dyne/cm²)	(compression)
Linear Expansion Coefficient (cm/ºC)	5.0 x 10 <sup>-7</sup>	Thermal Conductivity (W/cmºC)	0.014



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# **Oxidation kinetics**

- Oxidation Kinetics Model by Deal and Grove:
  - Oxidation proceeds by *the diffusion of* an oxidant (molecular H<sub>2</sub>O or O<sub>2</sub>)
  - Reaction occurs at the  $Si/SiO_2$  interface.
  - Si is consumed and the interface moves into Si





- Concentration of oxidants :
  - C<sub>G</sub> : concentration of oxidant in the bulk of the gas
  - $C_s$ : concentration of oxidant at the oxide surface  $\mathbf{F}_1$
  - $C_0$ : equilibrium C of the oxidant at the oxide surface
  - C<sub>i</sub> : concentration of the oxidant at growth interface
- Flux of oxidant :
  - F<sub>1</sub>: the bulk of the gas  $\rightarrow$  the gas/oxide interface
  - F<sub>2</sub> : the diffusion through the existing oxide
  - F<sub>3</sub>: the reaction. at the SiO2/Si



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# **Oxidation kinetics (flux in gas phase)**

 $F_1$ : Due to the concentration difference between  $C_G$  and  $C_S$ 

 $F_1 = h_G(C_G - C_S)$  $h_{\rm G}$ : mass transfer coefficient

From the ideal gas law PV = NRT 
$$C = \frac{N}{V} = \frac{P}{kT}$$
  $C_G = \frac{P_G}{kT}$   $C_S = \frac{P_S}{kT}$ 

From Henry's law: "The concentration of a species dissolved in a solid at Equilibrium is proportional to the partial pressure of the species at the solid surface"

> $C_0 = K_H P_{Sr} C^* = K_H P_G \qquad K_H :$  Henrian Constant  $C^*$ : equilibrium concentration in the oxide

$$F_1 = h_G(C_G - C_S) = \frac{h_G}{kT}(P_G - P_S) = \frac{h_G}{K_H kT}(C^* - C_0)$$

:. 
$$F_1 = h(C^* - C_0)$$
  $h = h_G/K_H kT$ 



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#### Oxidation kinetics (flux in oxide and silicon)

F<sub>2</sub>: Due to the concentration difference between C<sub>o</sub> and C<sub>i</sub>

From the Fick's first law

$$F_{2} = -D\left(\frac{dC}{dx}\right) = -D\frac{(C_{i} - C_{0})}{x_{0} - 0} = D\frac{(C_{0} - C_{i})}{x_{0}}$$

D : diffusion coefficient of the oxidant in oxide

 $F_3$ : Due to the consumption by the interface reaction at SiO<sub>2</sub>/Si

Proportional to the concentration of the oxidant at the interface

$$F_3 = k_S C_i$$
  $k_s$ : chemical rxn. rate const.



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### Oxidation kinetics (steady-state flux)

Under steady-state condition (no build-up or depletion of oxidizing species)

$$F_{1} = F_{2} = F_{3} = F \qquad \Longrightarrow \qquad C_{i} = \frac{C^{*}}{1 + \frac{k_{s}}{h} + \frac{k_{s}x_{0}}{D}} \qquad C_{0} = \frac{(1 + k_{s}\frac{x_{0}}{D})C^{*}}{1 + \frac{k_{s}x_{0}}{h} + \frac{k_{s}x_{0}}{D}}$$

Y.

\*





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## Oxidation kinetics (rate limiting step)

I. When the diffusion constant D is very small,





II. When the diffusion constant D is very large,





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### **Oxidation kinetics (oxidation rate)**

Oxidation Rate 
$$\frac{dx_0}{dt} = \frac{F}{N} = \frac{1}{N} \frac{DC_0 k_s}{D + k_s x_0}$$
Boundary Condition  
 $x = x_i$ , when  $t = 0$   
 $N : \# \text{ of oxidant molecules per unit volume}$   
 $N(dry) = 2.3 \times 10^{22} \text{ cm}^{-3}$   
 $N(wet) = 2.3 \times 10^{22} \text{ cm}^{-3}$   
 $\int_{x_i}^{x_0} (D + k_s x_0) dx_0 = \frac{DC_0 k_s}{N} \int_0^t dt$   
 $\frac{1}{2} k_s x_0^2 + D x_0 = \frac{DC_0 k_s}{N} t + \frac{1}{2} k_s x_i^2 + D x_i$   
 $x_0^2 + \frac{2D}{k_s} x_0 = \frac{2DC_0}{N} t + x_i^2 + \frac{2D}{k_s} x_i$   
 $A = \frac{2D/k_s r}{B} = \frac{2DC_0 r}{N} r_s r_s$   
 $T = (x_i^2 + A x_i) / B$ 



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#### **Oxidation Kinetics (Oxidation Rate)**





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# Factors affecting oxidation rate

- Oxidant Species (Dry and Wet), temperature
- Oxidant Gas Pressure
- Crystallographic Orientation of Si Substrate
- Substrate Doping
- Gas Ambient



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#### **Oxidation rate (temperature & oxidant)**





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# **Oxidation rate (pressure)**

 High pressure increases the oxide growth rate, by increasing the linear and parabolic rate constants.
 (The increase in the rate constants arises from the increased C\*.)



$$\frac{B}{A} = \frac{k_s C_0}{N} \cong \frac{k_s}{N} C^* = \frac{k_s}{N} K_H P_G$$
$$B = \frac{2DC_0}{N} \cong \frac{2D}{N} C^* = \frac{2D}{N} K_H P_G$$

Trade off:  $\Delta P = 1$  atm  $\Leftrightarrow \Delta T = 30$  °C > Low temperature oxidation can be achieved by high pressure oxidation for the same oxidation rate.

Method

1. Pressurizing water-pumping

2. Producing water by pyrogenic system



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#### **Oxidation rate (crystallographic orientation)**

• SiO2/Si interface is strongly related to the cystallographic orientation of Si.

- i.e., # of available Si-Si bonds per unit area

 The growth rate ratio (v111/v100) decreases at high temperatures, since the parabolic rate constant is predominant.





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# Oxidation rate (doping)

- Group III and V dopants enhance the oxidation rate when heavily doped.
- The oxidation rate depends on
  - > the  $C_{\rm B}$  in SiO<sub>2</sub> for diffusion controlled oxidation (*B* dominates).
  - > the  $C_{\rm B}$  at Si surface for reaction controlled oxidation (*B*/A dominates).



Boron segregated in SiO<sub>2</sub>
weakens the SiO<sub>2</sub> bond structures.
➢ Rapid diffusion of O<sub>2</sub> and H<sub>2</sub>O

Phosphorous piles up at Si surface.
 ➤ Enhanced oxidation rate in the reaction controlled regime



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# Oxidation rate (additional gases)

- Halogenic Oxidation:
  - The presence of chlorine mixed with O<sub>2</sub> gas during dry oxidation
    - Enhance the oxidation rate.
    - Improves device characteristics.
      - \* Chlorine-containing gases: Cl<sub>2</sub>, HCl, TCE, TCA





# Color chart (1)

Film Thickness (microns)	Order (5450 Å )	color and comments			
0.050 0.075		Tan Brown			
0.100 0.125 0.150 0.175		Dark violet to red violet Royal blue Light blue to metallic blue Metallic to very light yellow-green			
0.200 0.225 0.250 0.275		Light gold or yellow slightly metallic Gold with slight yellow orange Orange to melon Red-violet			
0.300 0.310 0.325 0.345 0.350 0.365 0.375 0.390	II	Blue to violet-blue Blue Blue to blue-green Light green Green to yellow-green Yellow-green Green-yellow Yellow			



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# Color chart (2)

Film	Order						
Thickness	(5450Å)	color and comments					
0.412		Light orange					
0.426		Carnation pink					
0.443		Violet-red					
0.465		Red-violet					
0.476		Violet					
0.480		Blue-violet					
0.493		Yellow					
0.502		Blue-green					
0.520		Green(broad)					
0.540		Yellow-green					
0.560	III	Green-yellow					
0.574		Yellow to "yellowish"					
0.585		Light orange or yellow to pink borderline					
0.60		Carnation pink					
0.63		Violet-red					
0.68		"Bluish"					



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# Color chart (3)

Film Thickness (microns)	Order (5450 Å )	color and comments				
0.72	IV	Blue-green to green (quite broad)				
0.77		"Yellowish"				
0.80		Orange(rather broad for orange)				
0.82		Salmon				
0.85		Dull, light red-violet				
0.86		Violet				
0.87		Blue-violet				
0.89		Blue				
0.92	V	Blue-green				
0.95		Dull yellow-green				
0.97		Yellow to "yellowish"				
0.99		Orange				
1.00		Carnation pink				
1.02		Violet-red				
1.05		Red-violet				
1.06		Violet				
1.07		Blue-violet				



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# Color chart (4)

Film Thickness (microns)	Order (5450 Å )	color and comments			
1.10		Green			
1.11		Yellow-green			
1.12	VI	Green			
1.18		Violet			
1.19		Red-violet			
1.21		Violet-red			
1.24		Carnation pink to salmon			
1.25		Orange			
1.28		"yellowish"			
1.32	VII	Sky blue to green-blue			
1.40		Orange			
1.45		Violet			
1.46		Blue-violet			
1.50	VIII	Blue			
1.54		Dull yellow-green			



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### **Rapid Thermal Oxide (RTO)**





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### Wet oxidation recipe

• Wet oxidation recipe

			1000°C					
STANDBY					1 1 1 1 1	1 1 1 1 1		900 <b>°C</b>
900%	PUSH	PRE-HEAT	RAMP UP	STABILIZ,	PRE OXID.	WET OXID.	RAMP DOWN	PULL
Nz	5,00 SLPM	5,00 SLPM	5,00 SLPM	5,00 SLPM		1 1 1	5,00 SLPM	5,00 SLPM
LOW O2	0,2 SLPM	0,2 SLPM	0,2 SLPM	0.2 SLPM		1 1 1 1		1 1 1 1
HIGH O₂		1 1 1 1	1 1 1 1		4,50 SLPM	4,50 SLPM		1 1 1 1
H2		1 1 1 1	1 1 1 1	1 1 1 1	1 1 1 1	6,75 SLPM	1 1 1 1	1 1 1 1
TIME	10 MIN	10 MIN	20 MIN	5 MIN	ЗMIN	144 MIN	30 MIN	10 MIN



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### Furnace at ISRC (CMOS)

- Model No : SELTRON CO. SHF Series
  - Annealing, Wet Oxidation, Dry Oxidation, Reflow, POCl<sub>3</sub>, Drive-in, Alloy
  - Wet oxidation
    - Gas :  $H_2$  ,  $O_2$  ,  $N_2$
    - Process temp. : 800~1000 ℃
    - Wafer size/quantities : 6" or 4" wafer/ 1~25
    - Temperature uniformity :  $\pm 1$  °C
    - Oxide thickness uniformity :  $\pm 1\%$





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#### Furnace at ISRC (MEMS)

- Model No : Sungjin Semitech JSF-2000-T43
  - Annealing , Wet oxidation , Reflow ,  ${\rm POCI}_3$
  - Wet oxidation
    - Gas :  $H_2$  ,  $O_2$  ,  $N_2$
    - Process temp. : 900~1000 ℃
    - Wafer size/quantities : 4"wafer/ 1~25
    - Temperature uniformity :  $\pm 1$  °C
    - Oxide thickness uniformity:  $\pm 1\%$





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### Furnace at ISRC (mini)

- Model No : Seoul Electron SMF-800
  - Dry oxidation, Annealing, Alloy
    - Dry Oxidation : <2000Å, 1000°C (gas :  $N_2$ ,  $O_2$ )
    - Annealing : N+, P+ annealing,  $<1000^{\circ}$ C





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# **RTP/RTA** at **ISRC** (CMOS)

- RTP (Rapid Thermal Process)
  - Model No : NYMTECH.CO., RTA200H-SVP1
    - RTA (Rapid Thermal Annealing), RTO (Rapid Thermal Oxidation), **RTN** (Rapid Thermal Nitridation)
    - Rapid annealing : < 1250℃</li>
    - Temperature uniformity : ± 2.0℃
    - MFC (N<sub>2</sub>, O<sub>2</sub>, Ar, NH<sub>3</sub>)
- RTA (Rapid Thermal Annealing)
  - Model No : Korea Vacuum Tech., KVRTP-020
    - Annealing, Alloy
    - Wafer Size : 4"~6"wafer, chip
    - Temperature uniformity :  $\pm 5^{\circ}$
    - Process time : < 60sec



RTP (Rapid Thermal Process)



RTA (Rapid Thermal Annealing)



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