

The Semantics of Bluespec

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L29-1

Why Semantics?

- ◆ The semantics helps in deciding
 - The meaning of a language construct – especially the corner cases
 - The correctness of the compiler, especially the correctness of optimizations
 - If the meaning of a construct is ambiguous or underspecified
- ◆ Characteristics of useful semantics
 - The “kernel language” should be small
 - The semantics should be concise, easy to understand and remember

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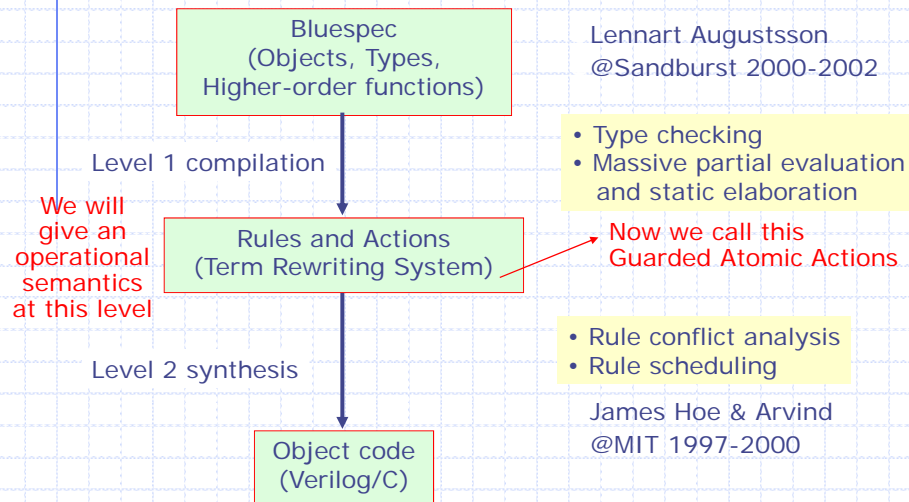
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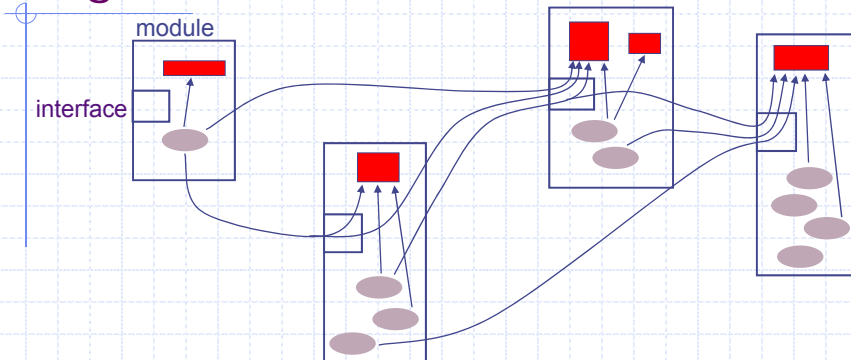
Indirect Semantics

- ◆ Most modern languages are big and their syntax contains many features for user convenience which interfere in understanding the deep semantics
- ◆ Consequently, the semantics are often given in two parts:
 1. A static translation from the source language into a simpler, smaller *kernel* language
 2. An Operational and sometimes the Denotational semantics of the *kernel* language

Bluespec: Two-Level Compilation



Bluespec: State and Rules organized into *modules*



All *state* (e.g., Registers, FIFOs, RAMs, ...) is explicit.
Behavior is expressed in terms of atomic actions on the state:

Rule: condition \rightarrow action

Rules can manipulate state in other modules only *via* their interfaces.

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BTRS: A Language of GAAs

A program is a collection of instantiated modules m_1, m_2, \dots

Module ::= Module name

[Register r]

[Rule R a]

[Action method $g(x) = a$]

[Read method $f(x) = e$]

New

| | |
|--|---|
| $a ::=$ <ul style="list-style-type: none"> $r := e$ $(t = e \text{ in } a)$ $\text{if } e \text{ then } a$ ← Conditional action $m.g(e)$ ← Method call $a \text{ when } e$ ← Guarded action $a \mid a$ ← Parallel Composition $a ; a$ ← Sequential Composition | $e ::=$ <ul style="list-style-type: none"> $r \mid c \mid t$ $\text{Op}(e, e)$ $e ? e : e$ $(t = e \text{ in } e)$ $m.f(e)$ $e \text{ when } e$ |
|--|---|

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Action Connectives: Par vs. Seq

- ◆ Parallel compositions ($a1 \mid a2$)
 - Neither action observes others' updates
 - Writes are disjoint (multiple writes to a register in a rule is an error)
 - Natural in hardware

$(r1 := r2 \mid r2 := r1)$ swaps $r1$ & $r2$

- ◆ Sequential Connective ($a1 ; a2$)
 - $a2$ observes $a1$'s updates
 - Still atomic
 - Not in BSV due to implementation complications (requires modules with "derived" interfaces)

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BSV Guards

- ◆ BSV allows guards only at the top level in rules
- ◆ However, it has implicit guards in method calls
 - For semantics these implicit guards are made explicit using "when"
 - BTRS allows guards everywhere without introducing any additional complexity

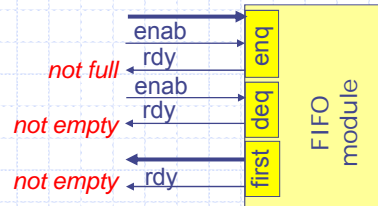
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Guarded Interfaces: An Example FIFO

```
interface FIFO#(type t);
  method Action enq(t);
  method Action deq();
  method t first();
endinterface
```



make implicit
guards explicit

$m.g(e)$

$m.g_B(e)$ when $m.g_G$

Predicating Actions: Guards vs. Ifs

- ◆ Guards affect their surroundings

$$(a1 \text{ when } p1) \mid a2 \implies (a1 \mid a2) \text{ when } p1$$

- ◆ The effect of an "if" is local

$$(\text{if } p1 \text{ then } a1) \mid a2 \implies \text{if } p1 \text{ then } (a1 \mid a2) \text{ else } a2$$

p1 has no effect on a2

Exercise: Understanding Guards

1. $(a1 \text{ when } p1) \mid (a2 \text{ when } p2)$
 $\implies (a1 \mid a2) \text{ when } (p1 \ \&\& \ p2)$
2. $(\text{if } p \text{ then } (a1 \text{ when } q)) \mid a2$
 $\implies ((\text{if } p \text{ then } a1) \mid a2) \text{ when } (q \ \parallel \ !p)$

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GAA Execution model

Repeatedly:

- ◆ Select a rule to execute
- ◆ Compute the state updates
- ◆ Make the state updates

Highly non-deterministic

Hardware Implementation: Needs to restrict this behavior to be deterministic

To get "good" hardware:

- The compiler must schedule multiple rules in a single cycle
- But the effect must look like a serial execution of rules

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Operational Semantics

- ◆ We will first give the semantics of a single rule, i.e., how it modifies the state of the system
 - Give a rule for how each language construct modifies the state
 - Rules must be compositional
- ◆ Then we will give the semantics of multiple rules

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Semantics of a rule execution

- ◆ Specify which state elements the rule modifies
 - Let S represent the value of all the registers before the rule executes
 - Let U be the set of updates implied by the rule execution
 - Let B represent the let-bound values we encounter in execution

$\langle S, U, B \rangle$

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BTRS Action Rules: Register Assignment

$$\text{reg-update} \quad \frac{\langle S, U, B \rangle \vdash e \Rightarrow v, (v \neq \text{NR})}{\langle S, U, B \rangle \vdash (r := e) \Rightarrow \{ \} [v/r]}$$

NR represents the
"not ready" value

BTRS Action Rules: Conditional Action

$$\text{if-true} \quad \frac{\langle S, U, B \rangle \vdash e \Rightarrow \text{true}, \quad \langle S, U, B \rangle \vdash a \Rightarrow U'}{\langle S, U, B \rangle \vdash (\text{if } e \text{ then } a) \Rightarrow U'}$$

$$\text{if-false} \quad \frac{\langle S, U, B \rangle \vdash e \Rightarrow \text{false}}{\langle S, U, B \rangle \vdash (\text{if } e \text{ then } a) \Rightarrow U}$$

BTRS Action Rules: Par and Seq action composition

$$\text{par} \quad \frac{\langle S, U, B \rangle \vdash a_1 \Rightarrow U_1, \quad \langle S, U, B \rangle \vdash a_2 \Rightarrow U_2}{\langle S, U, B \rangle \vdash (a_1 \mid a_2) \Rightarrow \text{pmerge}(U_1, U_2)}$$

Like a set union but blows up if there are any duplicates

$$\text{seq} \quad \frac{\langle S, U, B \rangle \vdash a_1 \Rightarrow U_1, \quad \langle S, U_1 ++ U, B \rangle \vdash a_2 \Rightarrow U_2}{\langle S, U, B \rangle \vdash (a_1 ; a_2) \Rightarrow U_2 ++ U_1}$$

Like a set union but U2 dominates if there are any duplicates

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BTRS Action Rules: Let and Method calls

$$\text{a-let-sub} \quad \frac{\langle S, U, B \rangle \vdash e \Rightarrow v, \quad \langle S, U, B[v/t] \rangle \vdash a \Rightarrow U'}{\langle S, U, B \rangle \vdash ((t = e) \text{ in } a) \Rightarrow U'}$$

$$\text{a-meth-call} \quad \frac{\langle S, U, B \rangle \vdash e \Rightarrow v, \quad (v \neq \text{NR}), \quad \lambda x. a = \text{lookup}(m.g), \quad \langle S, U, B[v/x] \rangle \vdash a \Rightarrow U'}{\langle S, U, B \rangle \vdash m.g(e) \Rightarrow U'}$$

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Guard Semantics

e-when-true

$$\frac{\langle S, U, B \rangle \vdash e1 \Rightarrow v, \quad \langle S, U, B \rangle \vdash e2 \Rightarrow \text{true}}{\langle S, U, B \rangle \vdash (e1 \text{ when } e2) \Rightarrow v}$$

e-when-false

$$\frac{\langle S, U, B \rangle \vdash e1 \Rightarrow v, \quad \langle S, U, B \rangle \vdash e2 \Rightarrow \text{false}}{\langle S, U, B \rangle \vdash (e1 \text{ when } e2) \Rightarrow \text{NR}}$$

a-when-ready

$$\frac{\langle S, U, B \rangle \vdash e \Rightarrow \text{true}, \quad \langle S, U, B \rangle \vdash a \Rightarrow U}{\langle S, U, B \rangle \vdash (a \text{ when } e) \Rightarrow U}$$

- ◆ If no rule applies, e.g., if e in (a when e) returns false or NR, then the system is stuck and the effect of the whole atomic action of which (a when e) is a part is “no action”.

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BTRS Expression Rules: register, constant, variables, ops

register read $\langle S, U, B \rangle \vdash r \Rightarrow (U++S) (r)$

Constant $\langle S, U, B \rangle \vdash c \Rightarrow \underline{c}$

Variable $\langle S, U, B \rangle \vdash t \Rightarrow B(t)$

op $\frac{\langle S, U, B \rangle \vdash e1 \Rightarrow v1, \quad v1 \neq \text{NR}}{\langle S, U, B \rangle \vdash e2 \Rightarrow v2, \quad v2 \neq \text{NR}}$
 $\langle S, U, B \rangle \vdash (e1 \text{ op } e2) \Rightarrow v1 \text{ op } v2$

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BTRS Expression Rules: Conditional expression

$$\text{tri-true} \quad \frac{\langle S, U, B \rangle \vdash e1 \Rightarrow \text{true}, \quad \langle S, U, B \rangle \vdash e2 \Rightarrow v}{\langle S, U, B \rangle \vdash (e1 ? e2 : e3) \Rightarrow v}$$

$$\text{tri-false} \quad \frac{\langle S, U, B \rangle \vdash e1 \Rightarrow \text{false}, \quad \langle S, U, B \rangle \vdash e3 \Rightarrow v}{\langle S, U, B \rangle \vdash (e1 ? e2 : e3) \Rightarrow v}$$

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BTRS Expression Rules: Let and Method calls

$$\text{e-let-sub} \quad \frac{\langle S, U, B \rangle \vdash e \Rightarrow v, \quad \langle S, U, B[v/t] \rangle \vdash e2 \Rightarrow v2}{\langle S, U, B \rangle \vdash ((t = e1) \text{ in } e2) \Rightarrow v2}$$

$$\text{e-meth-call} \quad \frac{\langle S, U, B \rangle \vdash e \Rightarrow v, \quad (v \neq \text{NR}), \quad \lambda x. eb = \text{lookup}(m.f), \quad \langle S, U, B[v/x] \rangle \vdash eb \Rightarrow v'}{\langle S, U, B \rangle \vdash m.f(e) \Rightarrow v'}$$

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BTRS Specification Semantics

1. Pick a rule
2. Compute initial $\langle S, U, B \rangle$ (read all registers)
3. Apply the rule yielding U
4. If no failure then update all registers according to U
5. Go to step 1

Scheduling

Schedules specify which rules should be executed concurrently without violating one-rule-at-a-time semantics

A schedule can be specified by parallel and conditional composition or rules

Lifting When's to the top

- A1. $(a1 \text{ when } p) \mid a2 \equiv (a1 \mid a2) \text{ when } p$
- A2. $a1 \mid (a2 \text{ when } p) \equiv (a1 \mid a2) \text{ when } p$

- A3. $(a1 \text{ when } p) ; a2 \equiv (a1 ; a2) \text{ when } p$
- A4. $a1 ; (a2 \text{ when } p) \equiv (a1 ; a2) \text{ when } p'$
where p' is p after the effect of $a1$

- A5. $\text{if } (p \text{ when } q) \text{ then } a \equiv (\text{if } p \text{ then } a) \text{ when } q$
- A6. $\text{if } p \text{ then } (a \text{ when } q) \equiv (\text{if } p \text{ then } a) \text{ when } (q \parallel !p)$

- A7. $(a \text{ when } p1) \text{ when } p2 \equiv a \text{ when } (p1 \ \&\& \ p2)$
- A8. $r := (e \text{ when } p) \equiv (r := e) \text{ when } p$
- A9. $m.g(e \text{ when } p) \equiv m.g(e) \text{ when } p$
similarly for expressions ...

Final Takeaway

- ◆ Have fun designing systems with Bluespec

Thanks

Pizza time now!