8. Networks

#Why networked embedded systems #General network architecture ✓ISO seven network layers **%**Networks \square I²C, CAN, Ethernet **#**Internet-enabled embedded systems **#**Sensor networks

Distributed embedded systems

Bistributed embedded systems

Processing elements (PEs) are connected through a network

H The application can be distributed over the PEs

- ○Clients and servers
- **∺**Network
 - Internet

Network-based ES

 Physically distributed activities---time constants may not allow transmission to central site.
 Engine control: short time delays required for the task

Bata reduction is another important reason for distributed processing.

Modularity is another motivation for networkbased design.

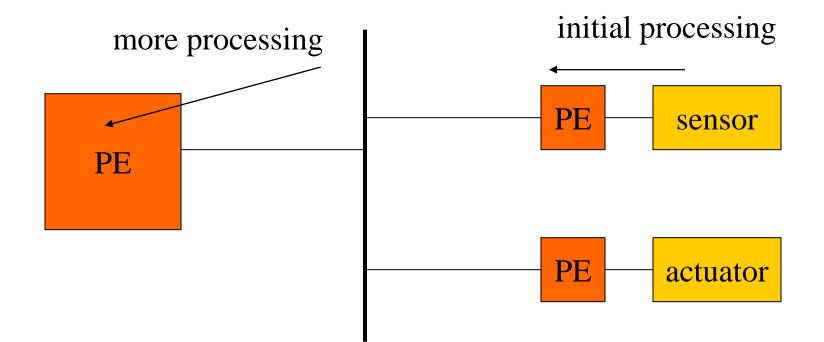
△A large system is assembled out of existing components.

Network-based ES

∺Improved debugging---use one CPU in network to debug others.

- In some cases, networks are used to build fault tolerance into systems.
- Cistributed embedded system design is a good example of hardware/software codesign since we must design the network topology as well as the software running on the network node.

Sensor-actuator networks

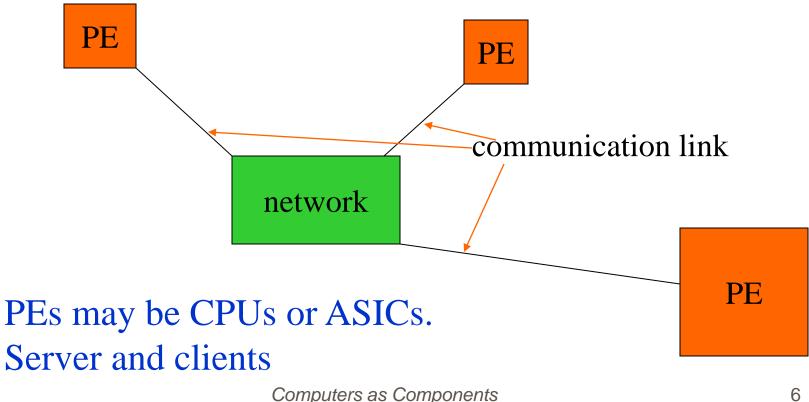


It is necessary to put some PEs near where the events occur.

Computers as Components

Network elements

distributed computing platform:



Distributed ES

Unlike the system bus, the distributed embedded system does not have memory on the network (or bus)

PEs do not fetch instruction over the network as they do on the microprocessor bus.

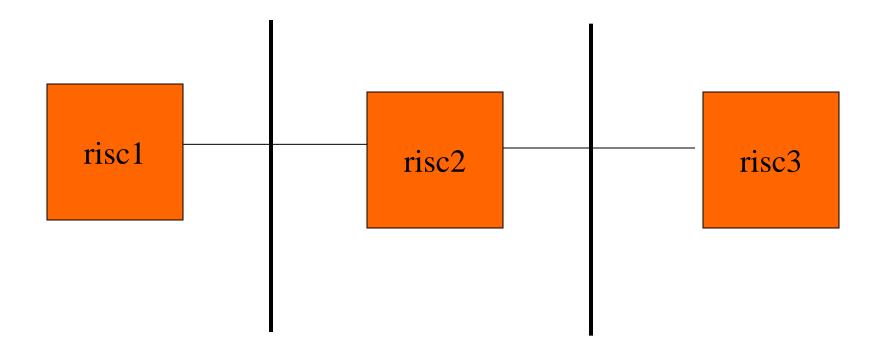
△ speed of PE and latency of network

Why distributed?

Ketwork-based embedded system \bigtriangleup More complicated, then why? \Re Physically separated & deadline is short Rather than building high-speed network to carry the data to a distant, fast PE. PE near to the location data colloected \Re Advantage of a distributed system with several CPU

△A part of the system can be used to debug another part.

Debugging a multi-core system



To diagnose risc 2, we can use risc1 to generate inputs and risc3 to watch output.

Computers as Components

Network abstractions

KNetworks are complex

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- # International Standards Organization (ISO) developed the Open Systems Interconnection (OSI) model to help us to understand networks: △7-layer model.
- Provides a standard way to classify network components and operations.

OSI reference model

7. application	end-use interface
6. presentation	data format
5. session	application dialog control
4. transport	connections
3. network	end-to-end service
2. data link	reliable data transport
1. physical	mechanical, electrical

OSI layers

Hysical: connectors, bit formats, etc.

Bata link: error detection and control across a single link (single hop).

Ketwork: end-to-end multi-hop data communication.

- *** Transport:** provides connections; may optimize network resources.
- **Session:** services for end-user applications: data grouping, checkpointing, etc.

Presentation: data formats, transformation services.

Application: interface between network and end-user programs.

LAN - Local Area Network

Ethernet 10 Mbps, 100Mbps, 1Gbps

- △Token Ring 16 Mbps
- ➢FDDI 100 Mbps

WAN - Wide Area Network

Connects computers that are physically far apart. "long-haul network".

#Technologies:

- △telephone lines
- △Satellite communications

MAN - Metropolitan Area Network

₭ Larger than a LAN and smaller than a WAN

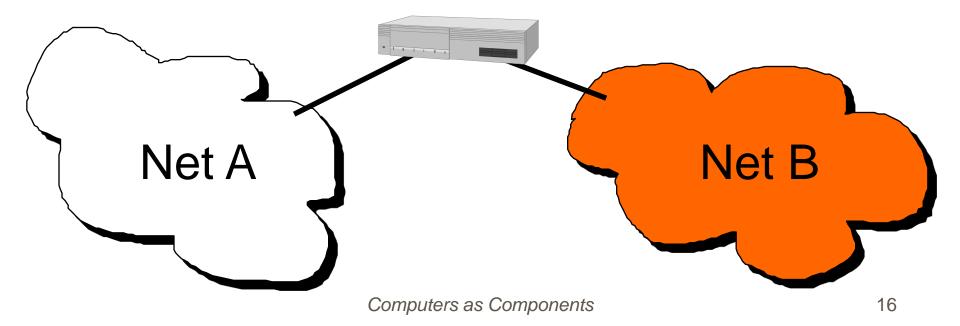
- example: campus-wide network
- multi-access network
- **#**Technologies:

 - Microwave

Internetwork

Connection of 2 or more distinct (possibly dissimilar) networks.

Requires some kind of network device to facilitate the connection.



1. Physical Layer

#deals with physical characteristics of the transmission medium.

defines electrical, mechanical, procedural, and functional specifications for activating, maintaining, and deactivating the physical link

Herein Connectors, and other similar attributes
Herein Connectors, and other similar attributes

⊯Examples : EIA/TIA-232, RJ45, NRZ.

1. Physical Layer

Responsibility:

Itransmission of raw bits over a communication channel.

#Issues:

- Mechanical and electrical interfaces
- ⊡distances



2. Data Link Layer

provides access to the media and physical transmission across the media, which enables the data to locate its intended destination

provides reliable transit of data across a physical link by using the MAC addresses, which define a hardware or data link address in order for multiple stations to share the same medium and still uniquely identify each other.

Concerned with network topology, network access, error notification, ordered delivery of frames, and flow control.

₭ Examples: Ethernet, Frame Relay, FDDI.

2. Data Link Layer

% Two sublayers

Data link control, which provide an error-free communication link

Framing (dividing data into chunks)

header & trailer bits

⊠addressing

MAC, which is needed by multiaccess networks.

⊠MAC provides DLC with "virtual wires" on multiaccess networks.

3. Network Layer

<mark>∺</mark>defines

△end-to-end delivery of packets.

○ logical addressing so that any endpoint can be identified.

A how routing works and how routes are learned so that the packets can be delivered.

A how to fragment a packet into smaller packets to accommodate different media.

#Routers operate at Layer 3.

3. Network Layer

Responsibilities:

△path selection between end-systems (routing).

Subnet flow control.

△fragmentation & reassembly

In the second second

∺Issues:

△ packet headers

virtual circuits

4. Transport Layer

Regulates information flow to ensure endto-end connectivity between host applications reliably and accurately.

Segments data from the sender and reassembles the data into a data stream on the receiver.

Here are concerned with data transport issues.

4. Transport Layer

Hayer 4 protocols include

TCP (Transmission Control Protocol) and

△UDP (User Datagram Protocol).

Responsibilities:

provides virtual end-to-end links between peer processes.

end-to-end flow control

<mark>∺</mark>Issues:

A headers

Mathematical Action

△reliable communication

Computers as Components

5. Session Layer

defines how to start, control and end sessions
between applications.

- includes the control and management of multiple bi-directional messages using dialogue control.
- Synchronizes dialogue between two hosts' presentation layers and manages their data exchange.
- Here a three layers (the application, presentation, and session layers) are concerned with application issues.

5. Session Layer

Here the session layer offers provisions for efficient data transfer.

Examples: SQL, ASP(AppleTalk Session Protocol).

Responsibilities:

Sestablishes, manages, and terminates sessions between applications.

Service location lookup

Hany protocol suites do not include a session layer.

6. Presentation Layer

∺The information that the application layer of one system sends out is ensured to be read by the application layer of another system.

∺If necessary, translates between multiple data formats by using a common format.

#provides encryption and compression of
data.

6. Presentation Layer

#Examples: JPEG, MPEG, ASCII, EBCDIC, HTML.

Responsibilities:

△data encryption

△data compression

data conversion

Many protocol suites do not include a Presentation Layer.

7. Application Layer

#provides network services to the user's
applications.

Holes not provide services to any other OSI layer, but rather, only to applications outside the OSI model.

Examples of such applications: spreadsheet programs, word processing programs, and bank terminal programs.

7. Application Layer

Setablishes the availability of intended communication partners, synchronizes and establishes agreement on procedures for error recovery and control of data integrity.

Responsibilities:

Anything not provided by any of the other layers

∺Issues:

Application level protocols

appropriate selection of "type of service"

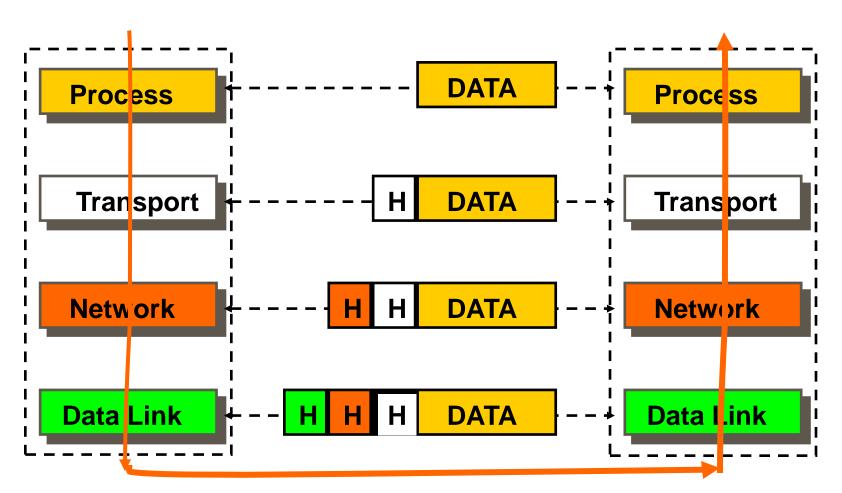
TCP/IP protocols

 \Re Application layer (telnet, ssh, http, ftp, etc) Main functionality: run application protocols HTransport layer (TCP, UDP) △ Main functionality: reliability ∺Network layer (IPv4, IPv6) \square Main functionality: routing/fragmentation/internetworking Host to data link layer (Ethernet) \square Main functionality: medium access, encoding

Layering & Headers

Each layer needs to add some control information to the data in order to do its job.
This information is typically appended to the data before being given to the lower layer.
Once the lower layers deliver the the data and control information - the peer layer uses the control information.

Headers



Computers as Components

Important Summary

Data-Link: communication between machines on the same network.

Network: communication between machines on possibly different networks.

Transport: communication between processes (running on machines on possibly different networks).

Connecting Networks

Repeater: physical layer

Bridge: data link layer

Router: network layer

#Gateway: network layer and above.

Repeater in the physical layer

%Copies bits from one network to another %Does not look at any bits

∺Allows the extension of a network beyond physical length limitations



Bridge in the data link layer

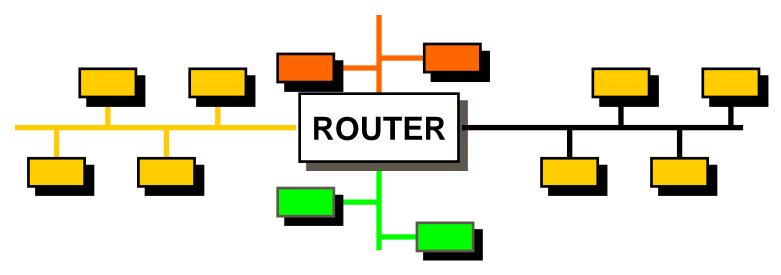
Copies frames from one network to another

- ∺Can operate selectively does not copy all frames (must look at data-link headers).
- Extends the network beyond physical length limitations.



Router in the network layer

Copies packets from one network to another.
Makes decisions about what *route* a packet should take (looks at network headers).





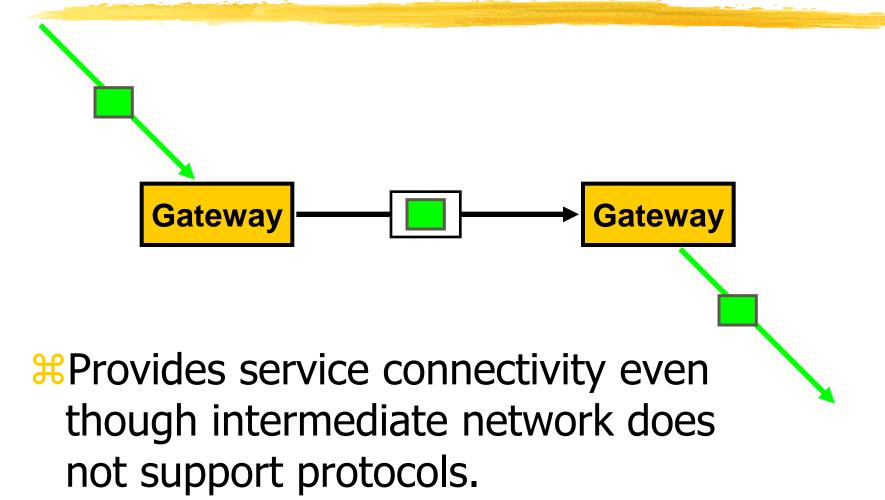
80 Operates as a router

Conversions:
Conversions:

encapsulation - use an intermediate network

translation - connect different application protocols encryption - could be done by a gateway

Encapsulation

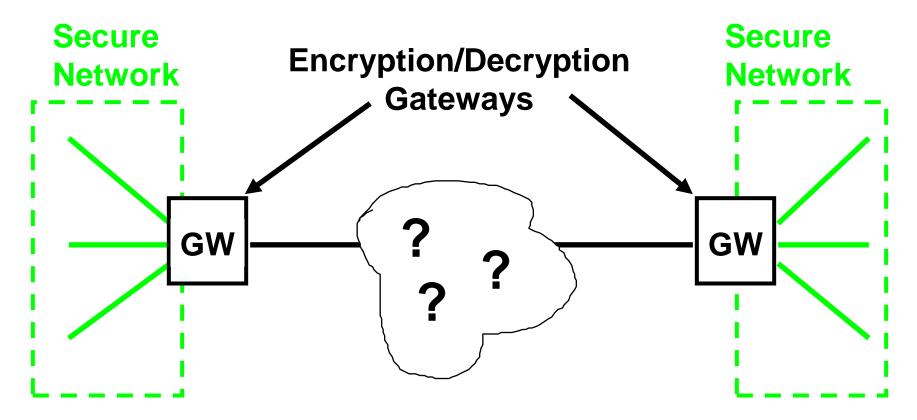






Translate from green protocol to brown protocol

Encryption gateway



Insecure Network

Computers as Components

Hardware vs. Software

∺Repeaters are typically hardware devices.

∺Bridges can be implemented in hardware or software.

Routers & Gateways are typically implemented in software so that they can be extended to handle new protocols.

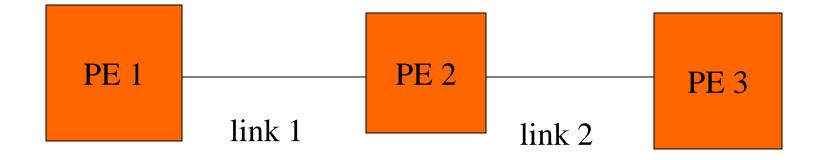
Many workstations can operate as routers or gateways.

Hardware architectures

Many different types of networks: topology; scheduling of communication; routing.

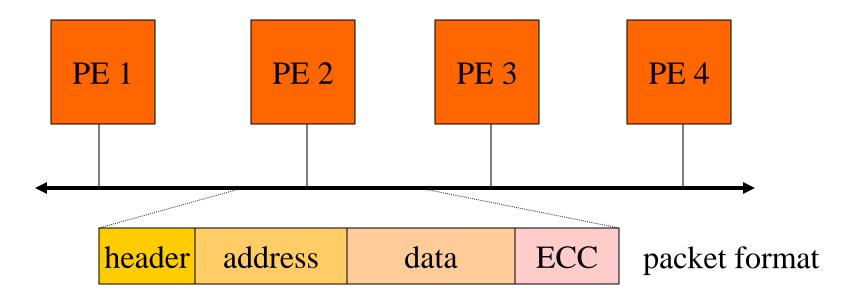
Point-to-point networks

One source, one or more destinations, no data switching (serial port):





#Common physical connection:

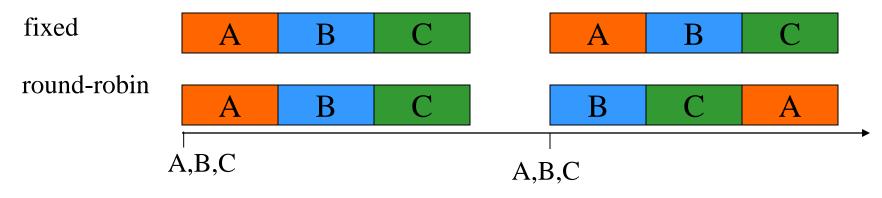


Bus arbitration

Fixed: Same order of resolution every time.

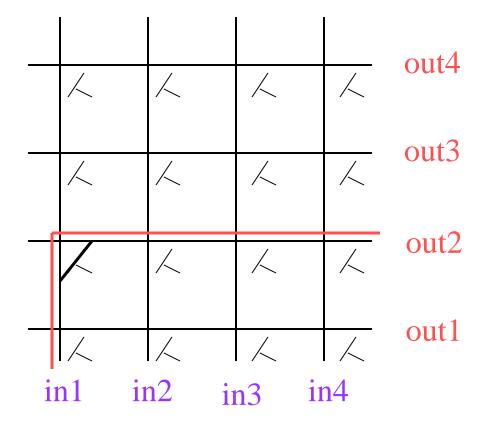
%Fair: every PE has same access over long periods.

round-robin: rotate top priority among PEs.



Computers as Components





Computers as Components

Crossbar characteristics

%Non-blocking (for 1-to-1 mapping) **%Can handle multiple multi-cast**combinations.

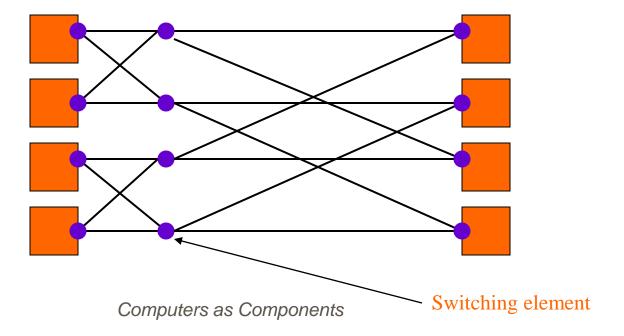
Size proportional to n^2 .

Blocking: if some combinations of sources and destinations For which messages cannot be delivered sinumtaneously.

Multi-stage networks

#Use several stages of switching elements.
#Often blocking.

₿ Often smaller than crossbar.



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Message-based programming

% Transport layer provides message-based programming interface:

send_msg(adrs,data1);

Blocking vs non-blocking

#Message passing: blocking

- Simplest implementation
- The routine is not returning until its data has received or transmitted
- Nonblocking network interface requires a queue of data to be sent

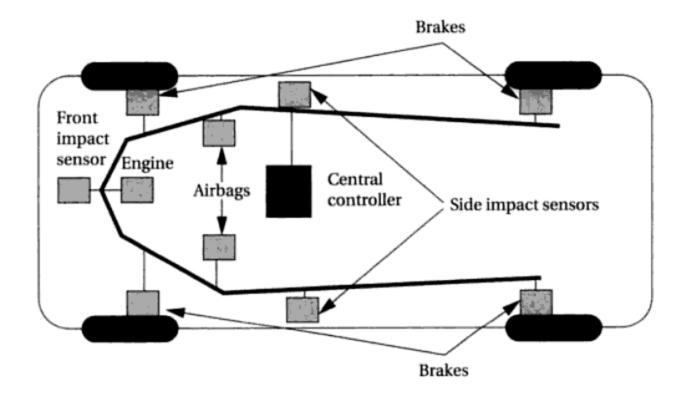
Data-push programming

#Data-push programming: make things happen in network based on data transfers.

Nodes send data out without any request from their intended users.

Data-push programming makes sense for periodic data, which reduces data traffic on the network by automatically sending it when it is needed.

Data-push network



The sensors generally need to be sampled periodically. In such a system, it makes sense for sensors to transmit their data automatically rather than waiting for the controller to request it.

System buses

#Multibus [Intel] &VME [Motorola]: multicard computer system #ISA bus: I/O cards for PC-based systems #PCI bus: high-speed interfaces for PC-based applications; replace ISA

Interconnection networks

#For embedded systems
#I²C bus: microcontroller-based systems
#CAN bus: for automotive electronics
#Echelon LON network: for home and industrial automation

I²C (inter-integrated circuit) bus

Besigned for low-cost, medium data rate applications.

#Command interface in a MPEG2 video chip

Characteristics:

- Monormal Market Mar
- ☐ fixed-priority arbitration.

Several microcontrollers come with built-in I²C controllers.

I²C bus

∺ ~ up to 100 kbps (standard) ~up to 400 bps (extended)

∺Two lines

SDL: serial data line
SCL: serial clock line

Terminology

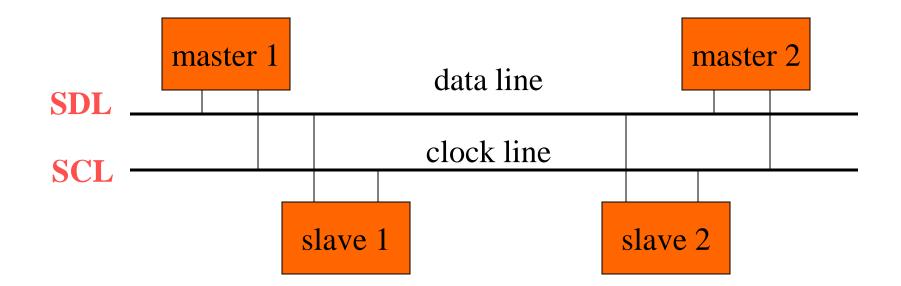
- **#** Transmitter The device sending data to the bus
- **Keceiver** Device receiving data from the bus
- Haster device initiating a transfer, generates to clock and terminates a transfer
- **Slave** Device addressed by the master
- Hulti-master more than one master can attempt to control the bus
- Arbitration procedure to insure that only one master has control of ther bus at any instant
- Synchronization procedure to sync then clocks of two or more devices

Inter-Integrated Circuit

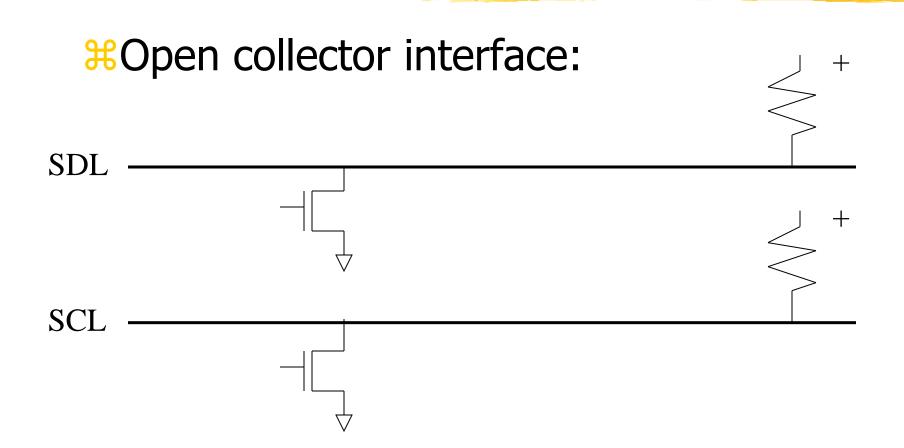
#Multi-master, two wire bus , up to 100
kbits/sec

- ○One data line (SDA)
- ○One clock line (SCL)
- △ Master controls clock for slaves
- Each connected slave has a unique 7-bit address

I²C physical layer







I²C signaling

Sender pulls down bus for 0.
Sender listens to bus---if it tried to send a 1 and heard a 0, someone else is simultaneously transmitting.

#Transmissions occur in 8-bit bytes.

Protocol

%Transfers are byte oriented, msb first
%Start: SDA goes low while SCL is high
%Master sends address of slave (7-bits) on next 7 clocks

%Master sends read/write request bit

O-write to slave

△1-read from slave

Slave ACKs by pulling SDA low on next clock Data transfers now commence

Complete I2C Transfer

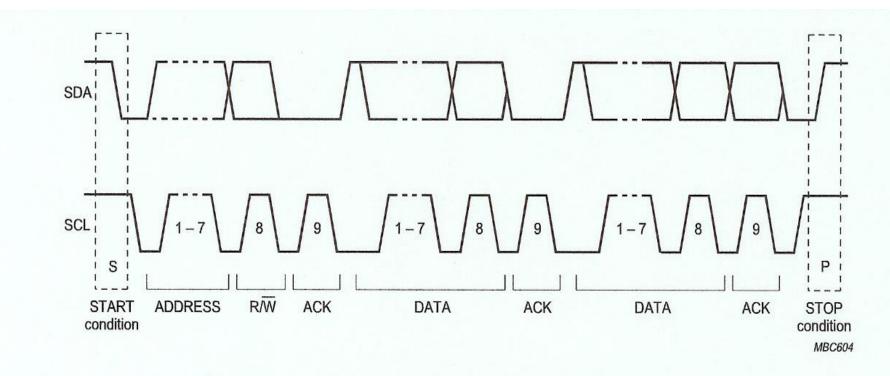


Fig.10 A complete data transfer.

Computers as Components

Master-to-Slave Data Transfer

#Clock is controlled by master

- Bata is written to slave on next 8 clock pulses
- Boundary Construction Hold Science (Science of Science of
- When slave releases SDA, master can send next byte
- Master will eventually set a Stop condition by making a low to high transition on SDA with SCL is high

Master Writes to Slave

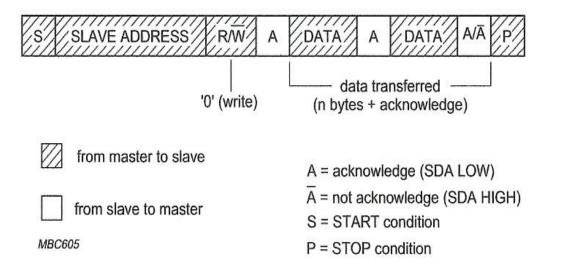


Fig.11 A master-transmitter addressing a slave receiver with a 7-bit address. The transfer direction is not changed.

Master Reads from Slave

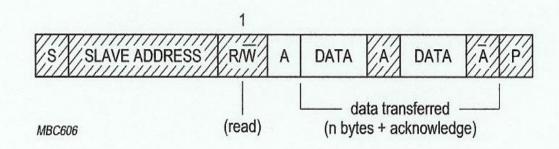


Fig.12 A master reads a slave immediately after the first byte.

I²C transmissions

multi-byte write

S adrs	0	data	data	Р
--------	---	------	------	---

read from slave

S ad	lrs 1	data	Р
------	-------	------	---

write, then read

S	adrs	0	data	S	adrs	1	data	Р	
---	------	---	------	---	------	---	------	---	--

I2C Extensions

%10 bit addressing (up to 1024 addresses)
%Fast mode – up to 400 kbits/sec
%High-Speed – up to 3.4 Mbits/sec

I²C bus arbitration

Sender listens while sending address.
When sender hears a conflict, if its address is higher, it stops signaling.
Low-priority senders relinquish control early enough in clock cycle to allow bit to be transmitted reliably.