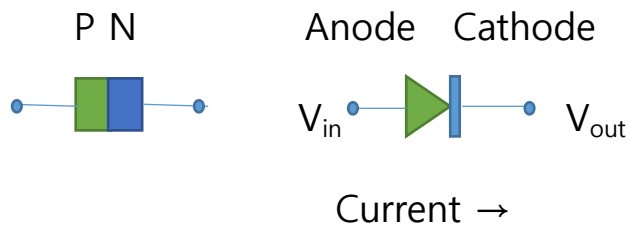


## Lecture 9- Diode

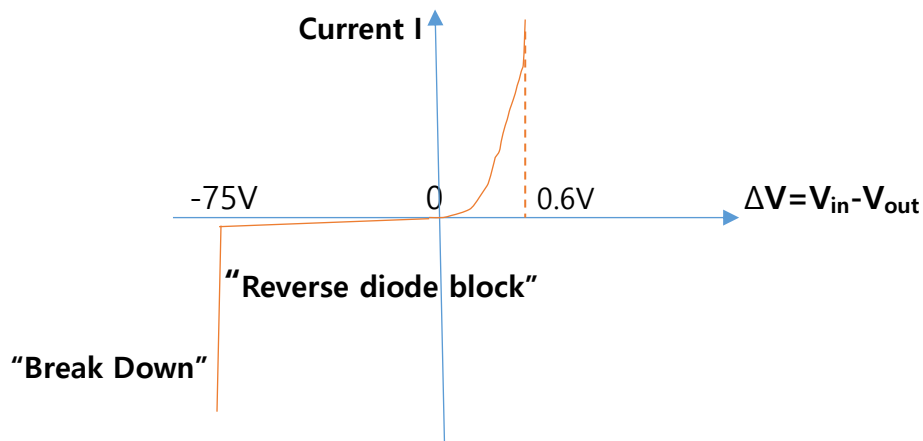
: Contacting of semiconductor materials to regulate current

*Nonlinear* device that gives nonlinear relationship between voltage(V) and current(I).

\*Resistor,Capacitor,Inductor are all *linear* devices such that  $V=IZ(\omega)$



### V-I Curve for Diode



### Application:

Current Valve, Block for Reverse flow, Rectifier, Clamper,

Voltage source

## Laws:

(1)  $Z_{\rightarrow} = 0$  and  $Z_{\leftarrow} = \infty$  :

Current flows for the forward direction, but

Current is blocked for the reverse direction

$\therefore$  Current valve and Blocking for reverse flow, or reverse block

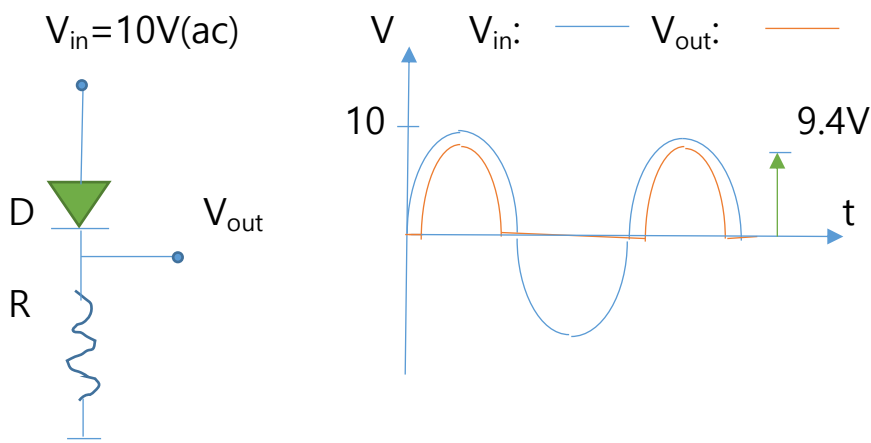
(2)  $V_{\text{out}} = V_{\text{in}} - 0.6$

when the diode conducts current, and 0.6V is the diode drop.

$\Delta V_{\text{out}} = \Delta V_{\text{in}}$  and  $\Delta V_{\text{out}} / \Delta V_{\text{in}} = \text{Gain} = 1$

$\therefore$  Any input voltage change is transferred to the output voltage change.

## Examples

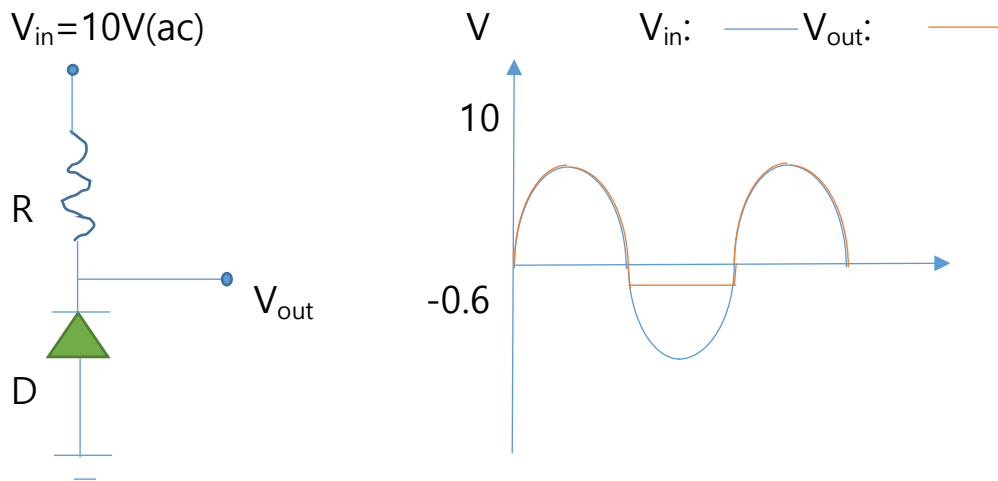


if  $V_{\text{in}} \geq 0.6$ , then diode conducts, thus

$V_{\text{out}} = V_{\text{in}} - 0.6$  and  $\Delta V_{\text{out}} / \Delta V_{\text{in}} = R / (Z_D + R) \approx 1$  ( $\because Z_D \approx 0$ )

If  $V_{\text{in}} < 0.6$ , no current flows, thus  $V_{\text{out}} = 0$  ( $\because$  Ohm's law for  $R$ )

$\therefore$  Halfwave Rectifier

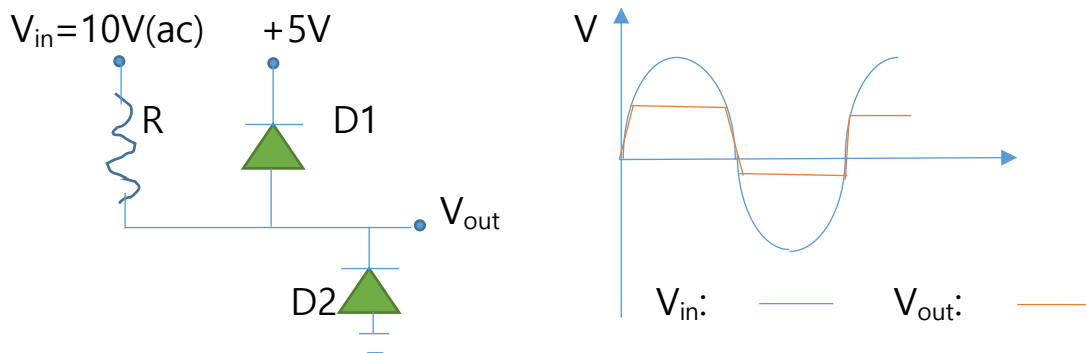


If  $V_{in} > -0.6V$ , then Diode blocks current, thus  $Z_D = \infty$  and

$$V_{out}/V_{in} = Z_D/(R+Z_D) \cong 1 \quad (\because Z_D = \infty) \quad \therefore V_{out} = V_{in}$$

If  $V_{in} \leq -0.6V$ , then Diode conducts and  $V_{out} = -0.6V$  (Diode drop)

$\therefore$  Clamper between  $-0.6V$  to  $10V$ , or  $[-0.6, 10]$  from  $10V(ac)$



If  $V_{in} \geq 5.6V$  then  $D1$  conducts and  $V_{out} = 5.6V$

If  $V_{in} \leq -0.6V$  then  $D2$  conducts and  $V_{out} = -0.6V$

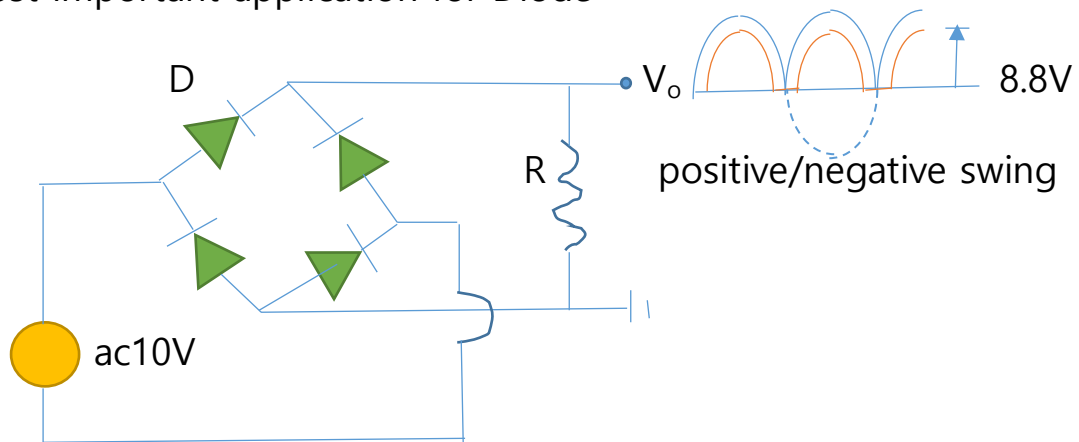
If  $-0.6V < V_{in} < 5.6V$  then both diodes block current;  $Z_{D1} = Z_{D2} = \infty$

$$V_{out}/V_{in} = Z_{D2}/(R+Z_{D2}) \cong 1 \quad (\because Z_{D2} = \infty) \quad \therefore V_{out} = V_{in}$$

$\therefore$  Clamper between  $[-0.6, 5.6]$  from  $10V(ac)$

## Full-wave Rectifier

:Most important application for Diode



$V_o$ : Ripple of max 8.8V (=10 – 2xDiode drop)

⇒Never good quality, thus should be regulated further.

## Zenor diode as Voltage source

:Zenor diode( $D_Z$ ) breaks at low reverse voltage,  $-V_{ZENOR}$  (e.g.-10V)



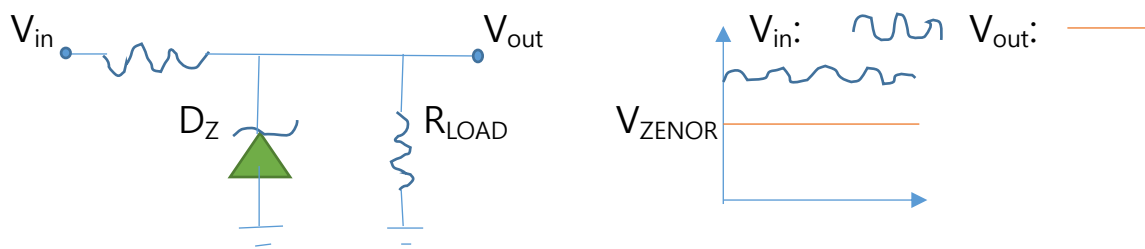
For Zenor Diode:

If  $V_2 - V_1 \geq V_{ZENOR}$

Then the Diode breaks and  $V_2 - V_1 = V_{ZENOR} = const$

Thus  $V_2 - V_1$  is kept const (=  $V_{ZENOR}$ )

and it can be used as the voltage source.



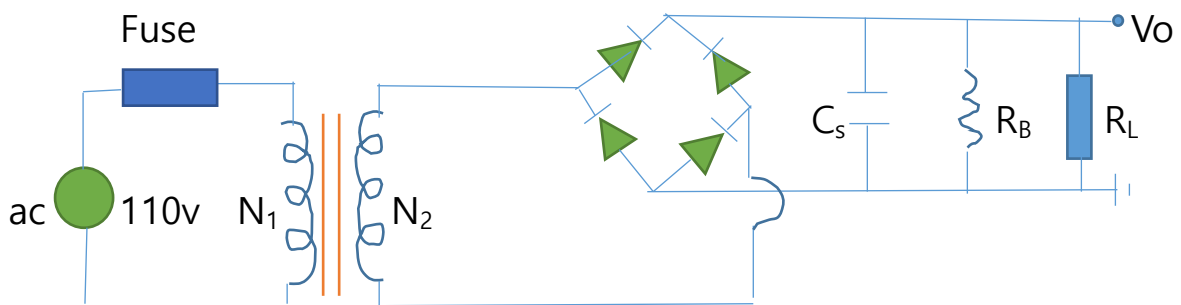
$\therefore$  For unregulated  $V_{in} (\geq V_{ZENOR})$ ,  $V_{out}$  gives constant voltage,  $V_{ZENOR}$

$\therefore$  Zener diode can be a good voltage source (like battery!)

with at least 10mA (thresh-hold) flowing in Zener diode

Power Supply : Most important Diode application

:To convert AC to DC that gives constant voltage supply



Design Specification

AC=110V@60Hz,  $V_o=20V$ , Voltage Ripple=2V,  $i_{max}=1A$

(Source: P.Horowitz et al, The art of electronics student manual, p71,1989)

### 1. Transformer Coil Ratio $N_1:N_2$

$$N_1:N_2 = V_1|_{\text{rms}} : V_2|_{\text{rms}}, \text{ and } V_1|_{\text{rms}} = 110\text{V}$$

and  $V_2|_{\text{rms}} = \sqrt{\int V^2 dt / T} = V_2|_{\text{peak}} / \sqrt{2}$  for sinusoidal wave

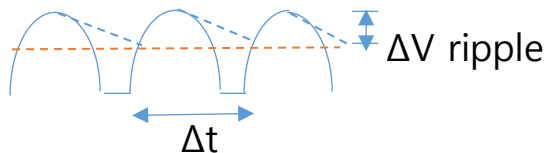
$$V_2|_{\text{peak}} = V_o + \text{Ripple} + 2 \times \text{Diode-Drop} = 20 + 2 + 1.2 = 23.2\text{V}$$

$$\therefore V_2|_{\text{rms}} = 23.2 / \sqrt{2} = 16.2\text{V}$$

$$\text{Thus } N_1:N_2 = V_1|_{\text{rms}} : V_2|_{\text{rms}} = 110:16.2 \approx 55:8$$

### 2. Smoothing Capacitor, $C_s$

:To make smoothing the voltage by charging/discharging

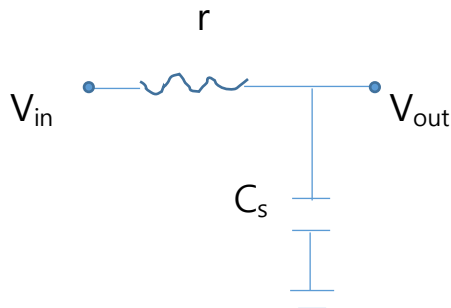


When the current flows into/from the capacitor, voltage rise/drop is followed accordingly. The concept of smoothing capacitor is to make complimentary voltage rise/drop by current flowing into/from the capacitor. The voltage rise/drop  $\Delta V = \Delta Q / C = i \Delta t / C$ , where  $i$  is the current flow into/from the capacitor, and  $\Delta t = 1/2f = 1/120[\text{sec}]$  during the full wave rectifying.

$$\text{Thus for the maximum current of } 1\text{A}, C_s = i / (\Delta V / \Delta t) = 1 / \{2 / (1/120)\}$$

$$= 4 \times 10^{-3}\text{F} = 4000\mu\text{F} = 4\text{mF}$$

It is interesting to note the  $C_s$  can be understood as a LPF as following:  
 When  $r$  is a very small resistance of wiring connected to the  $C_s$ ;



$$V_{out}/V_{in} = 1/j\omega C_s / (r + 1/j\omega C_s) = 1/(1 + j\omega r C_s) \quad \therefore \text{It is a LPF.}$$

$$\text{Magnitude} = |H| = 1/\sqrt{1 + (\omega r C_s)^2} \quad \text{Phase} = \angle H = -\tan^{-1}(\omega r C_s)$$

If  $\omega = 0$  (or DC) then  $|H| = 1$

If  $\omega \gg 1$  then  $|H| \ll 1$ , and Ripple is more reduced with bigger  $C_s$ , and this is the same conclusion as above.

### 3. Bleeding Resistor, $R_B$

When power is off, the electric charge is still left in the  $C_s$  capacitor, therefore it desirable to leak the electric charge through the bleeding resistor  $R_B$  for safety issue.

Let's consider the close loop consisting of  $C_s$  and  $R_B$ . From the Kirchhoff's voltage law,

$$0 = V_C + V_R = \int i dt / C_s + i R_B = 0 \quad \text{Thus } R_B C_s di/dt + i = 0$$

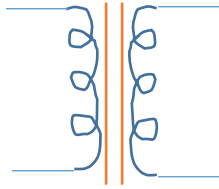
$$\therefore i(t) = i_0 \exp(-t/RC_s), \text{ and it is decaying 64\% when } t = RC_s$$

It is the designer's choice to set the time for 64% decay such as 4s.

$$\text{thus } R_B = 4/C_s = 1K\Omega$$

#### 4. Fuse rating current

$$I_1 V_1 \cong I_2 V_2$$



primary      secondary

$$I_1 = I_2 V_2 / V_1 = (1)(16.2) / 110 \cong 0.14 \text{ (A)}$$

$$\text{The fuse rating current} = (0.14 \text{ A}) C_{\text{surge}} C_{\text{safety}} = 1.1 \text{ A}$$

$C_{\text{surge}}$  = Safety factor for heating effect and surge  $\cong 4$  (from practice)

$C_{\text{safety}}$  = Safety factor for the full load not to blow  $\cong 2$  (from practice)

Q: What happen if one diode is burnt?

When one diode is burnt, either positive or negative swing is considered, thus  $\Delta t$  is changed to  $1/60\text{s}$ .

$$\text{Then voltage ripple, } \Delta V = i \Delta t / C_s = (1)(1/60) / 0.004 = 4.16 \text{ V}$$

$$\text{Thus } V_o = V_2 |_{\text{peak}} - \Delta V - 2(0.6) = 23.2 - 4.16 - 1.2 = 17.8 \text{ V}$$

Q: In Europe (AC 220V@50Hz),

what about the performance of the above power supply?

$$\text{Voltage ripple, } \Delta V = i \Delta t / C_s = (1)(1/100) / 0.004 = 2.5 \text{ V (25\% increase)}$$

$$V_2 |_{\text{rms}} = (N_2 / N_1) V_1 |_{\text{rms}} = (8/55) 220 = 32 \text{ V}$$

$$\text{Thus } V_o = V_2 |_{\text{peak}} - \Delta V - 2(0.6) = 32\sqrt{2} - 2.5 - 1.2 = 41.5 \text{ V}$$



HW6) Design a power supply as follows;

AC 110V@60Hz,  $V_o=5V$ (desired),  $i_{max}=1A$ , Voltage ripple=0.5V