Companion slides for
The Art of Multiprocessor
Programming
by Maurice Herlihy & Nir Shavit

#### Last Lecture

- Defined concurrent objects using linearizability and sequential consistency
- Fact: implemented linearizable objects (Two thread FIFO Queue) in read-write memory without mutual exclusion
- Fact: hardware does not provide linearizable read-write memory



#### Fundamentals

- What is the weakest form of communication that supports mutual exclusion?
- What is the weakest shared object that allows shared-memory computation?



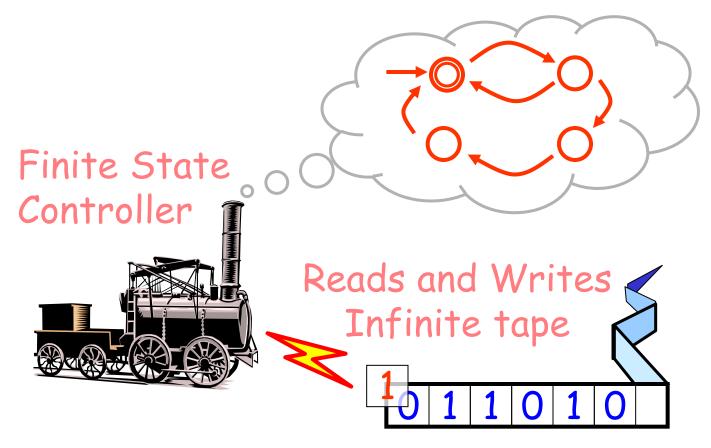
#### Alan Turing



- Helped us understand what is and is not computable on a sequential machine.
- · Still best model available

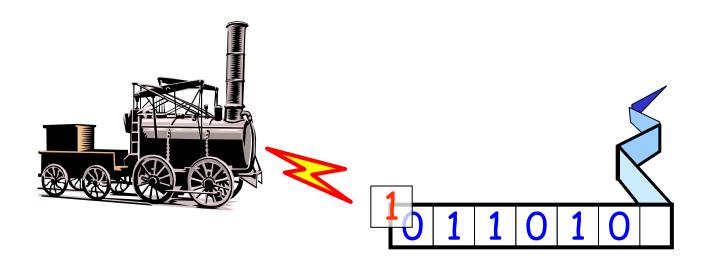


#### Turing Machine





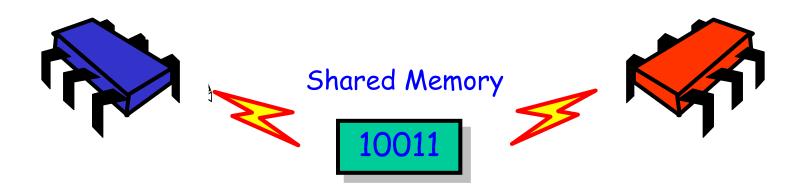
## Turing Computability



- Mathematical model of computation
- What is (and is not) computable
- Efficiency (mostly) irrelevant



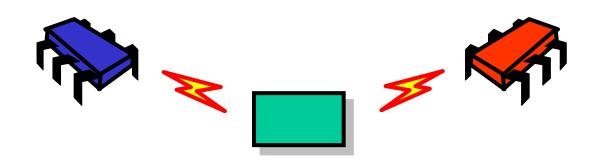
# Shared-Memory Computability?



- Mathematical model of concurrent computation
- What is (and is not) concurrently computable
- Efficiency (mostly) irrelevant

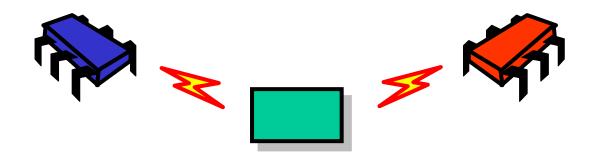


To understand modern multiprocessors we need to ask some basic questions ...

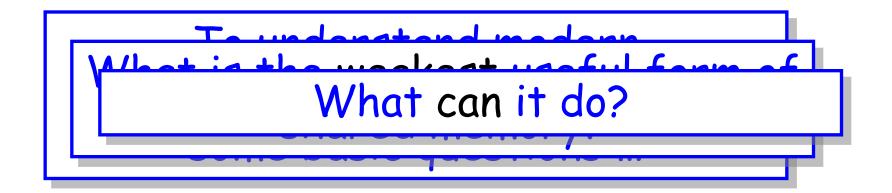


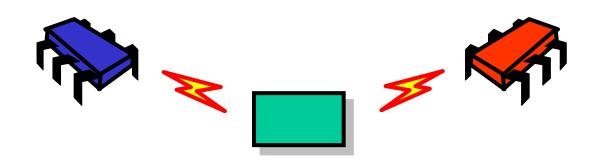


What is the weakest useful form of shared memory?

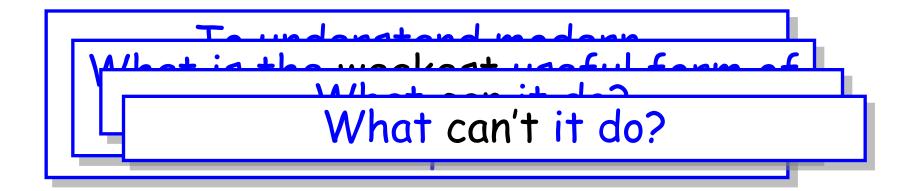


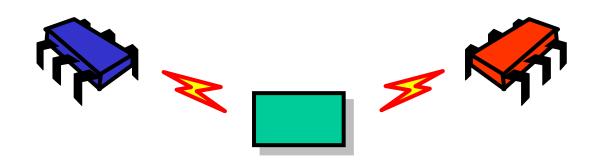






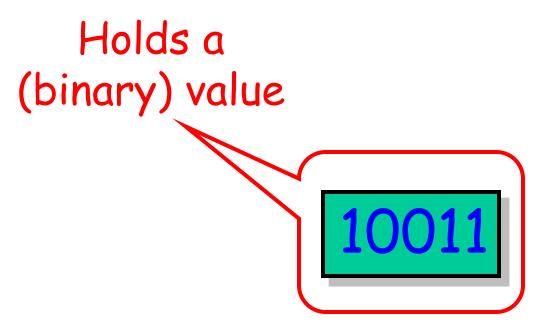






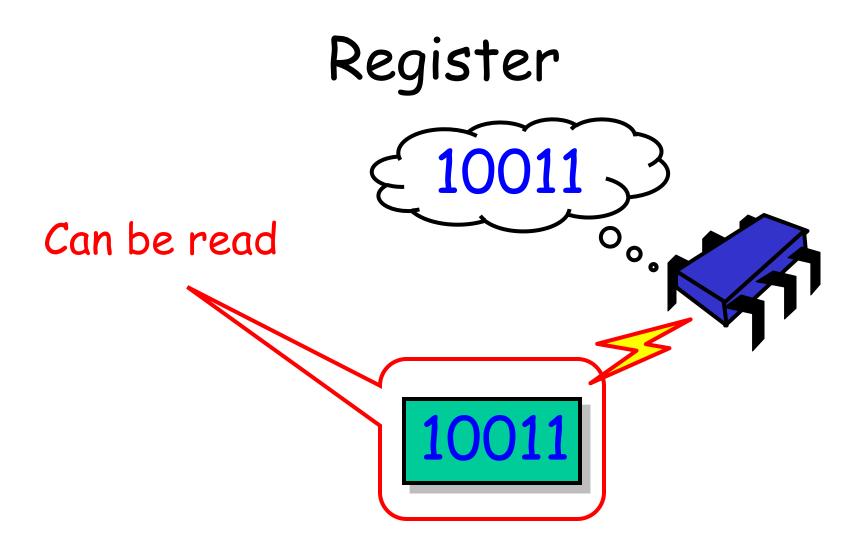


# Register \*



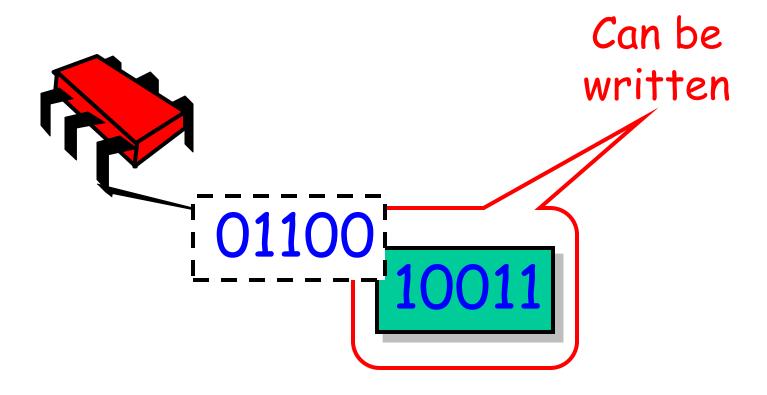
\* A memory location: name is historical







#### Register





#### Registers

```
public interface Register<T> {
   public T read();
   public void write(T v);
}
```

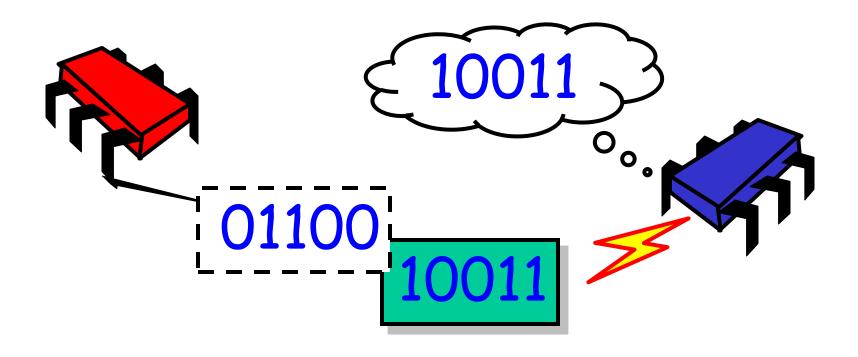


## Registers

```
public interface Register<T> {
  public T read();
public void write(T v)
                   Type of register
               (usually Boolean or m-bit
                        Integer)
```

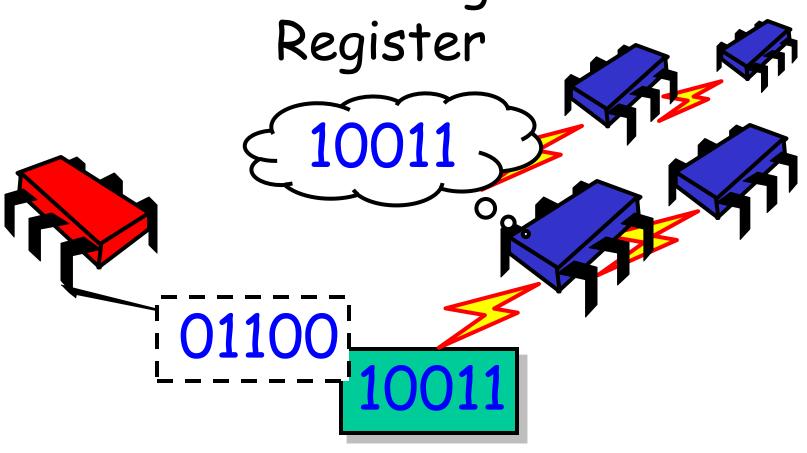


#### Single-Reader/Single-Writer Register

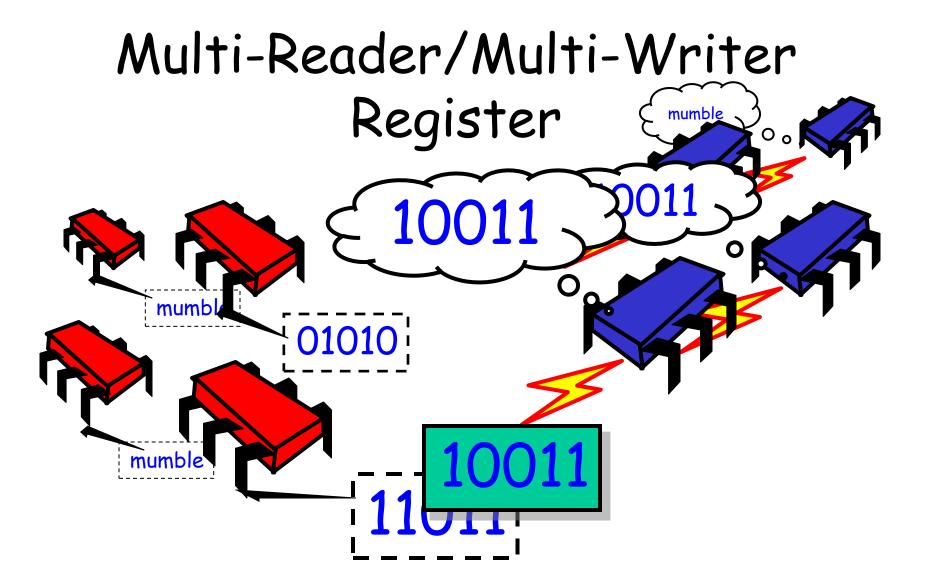




#### Multi-Reader/Single-Writer







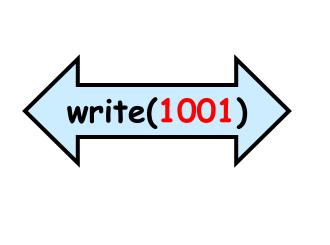


#### Jargon Watch

- SRSW
  - Single-reader single-writer
- MRSW
  - Multi-reader single-writer
- MRMW
  - Multi-reader multi-writer



#### Safe Register



OK if reads and writes don't overlap



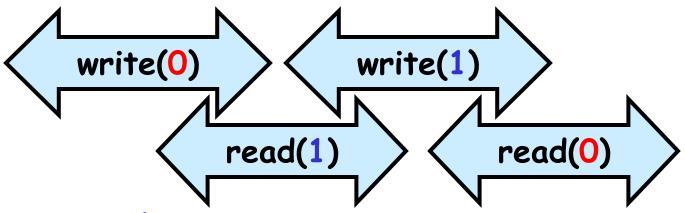


#### Safe Register

Some valid value if reads and writes do overlap write(1001) read(????)

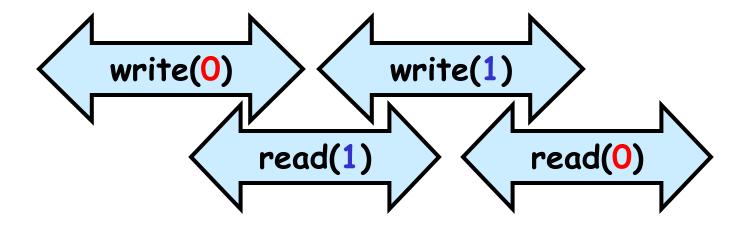


#### Regular Register

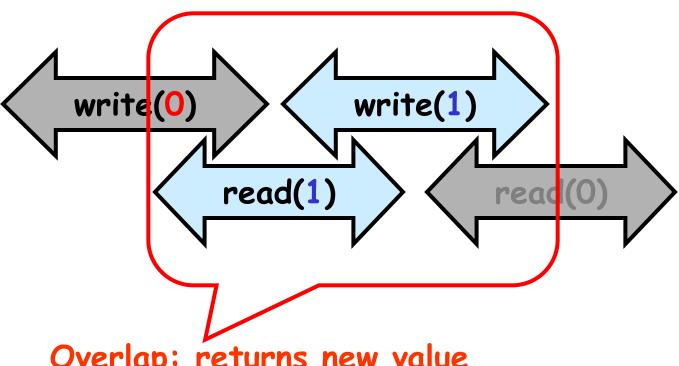


- Single Writer
- · Readers return:
  - Old value if no overlap (safe)
  - Old or one of new values if overlap



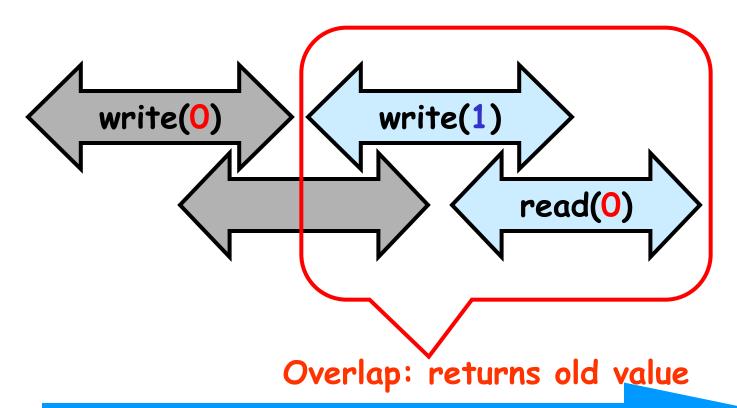




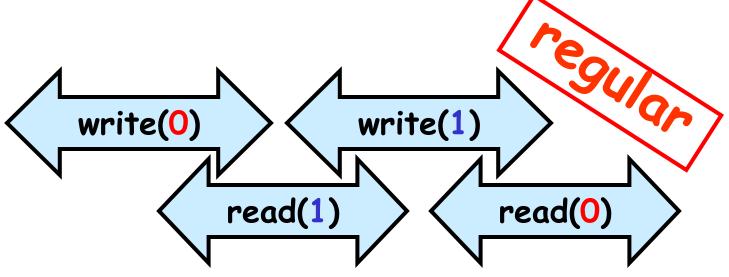


Overlap: returns new value



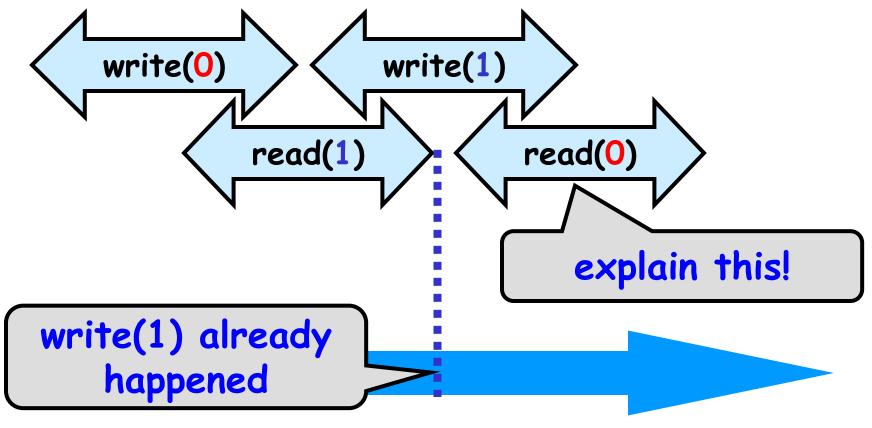






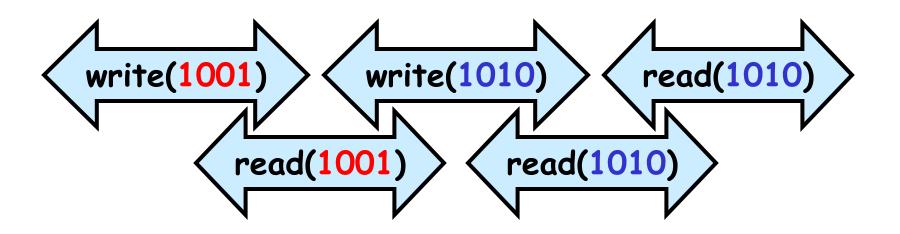


#### Regular ≠ Linearizable





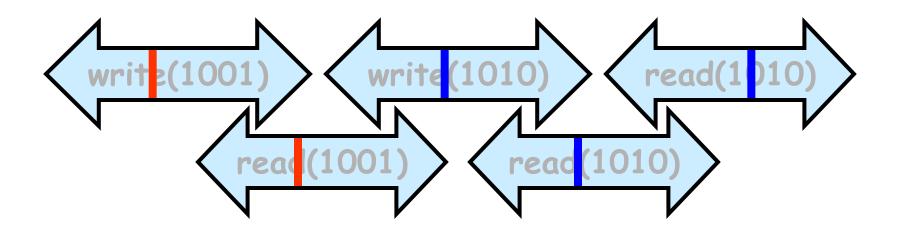
#### Atomic Register



# Linearizable to sequential safe register

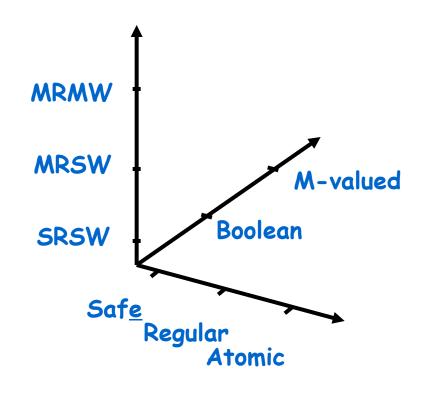


#### Atomic Register



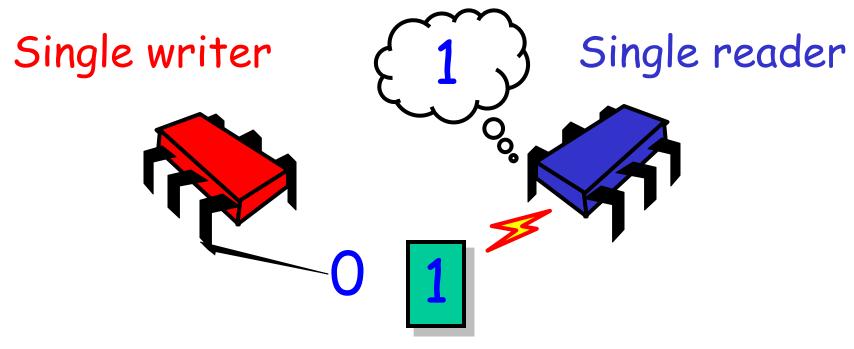


## Register Space





#### Weakest Register



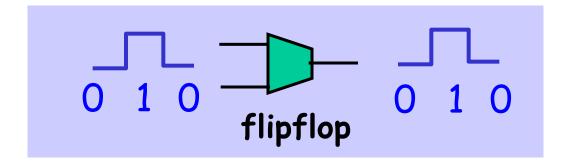




#### Weakest Register

#### Single writer

Single reader



# Get correct reading if not during state transition



#### Results

- From SRSW safe Boolean register
  - All the other registers
  - Mutual exclusion

Foundations of the field

- But not everything!
  - Consensus hierarchy

The really cool stuff ...



## Locking within Registers

- Not interesting to rely on mutual exclusion in register constructions
- We want registers to implement mutual exclusion!
- No fun to use mutual exclusion to implement itself!



#### Wait-Free Implementations

Definition: An object implementation is wait-free if every thread completes a method in a finite number of steps

#### No mutual exclusion

- Thread could halt in critical section
- Build mutual exclusion from registers



- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
- MRSW regular
- MRSW atomic
- MRMW atomic
- Atomic snapshot



- SRSW safe Boolean
- Next
- · MRSW safe Boolean
- MRSW regular Boolean
- MRSW regular
- MRSW atomic
- MRMW atomic
- Atomic snapshot



```
public class SafeBoolMRSWRegister
implements Register<Boolean> {
  public boolean read() { ... }
  public void write(boolean x) { ... }
}
```



```
public class SafeBoolMRSWRegister
implements Register<Boolean> {
  public boolean read() { ... }
  public void write(boolean x) { ... }
}
```



```
public class Safe Bool MRSWRegister
implements register < Boolean > {
  public bo lean read() { ... }
  public //oid write(boolean x) { ... }
property
```





```
public class Safe Bool MRSW Register
implements Register<Boolean> {
             lean read()
  public box
  public //oid wr//te(boolean x) { ... }
                     How many readers
property
                         & writers?
             type
```



```
public class SafeBoolMRSWRegister
 implements Register<Boolean> {
 private SafeBoolSRSWRegister[] r =
   new SafeBoolSRSWRegister[N];
  public void write(boolean x) {
   for (int j = 0; j < N; j++)
    r[j].write(x);
  public boolean read() {
   int i = ThreadID.get();
   return r[i].read();
  }}
```



```
public class SafeBoolMRSWRegister
 implements BooleanRegister {
private SafeBoolSRSWRegister[] r =
  new SafeBoolSRSWRegister[N];
 public void write(boolean x) {
  for (int j = 0; j < N; j++)
    r[j].write(x);
  public boolean read() {
  int i = ThreadID.get();
   return r[i].read(); Each thread has own
 }}
                       safe SRSW register
```



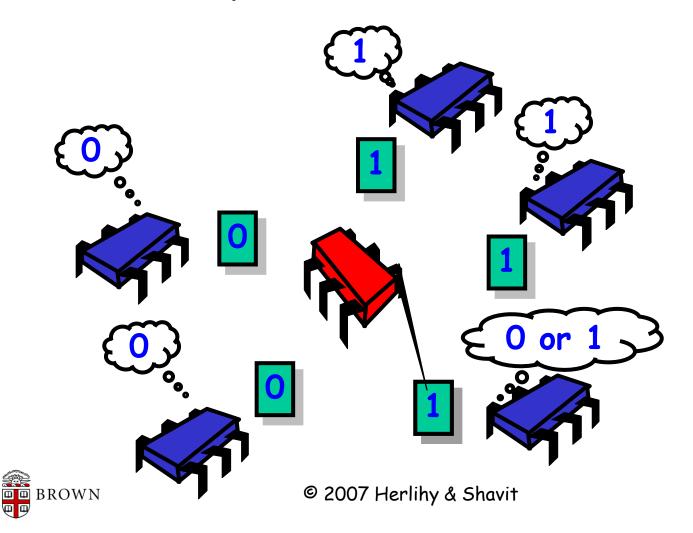
```
public class SafeBoolMRSWRegister
implements BooleanRegister {
 private SafeBoolSRSWRegister[] r =
   new SafeBoolSRSWRegister[N];
 public void write(boolean x) {
   for (int j = 0; j < N; j++)
    r[j].write(x);
 public boolean read()
   int i = ThreadID.get();
                            write method
   return r[i].read();
 }}
```

```
public class SafeBoolMRSWRegister
 implements BooleanRegister {
 private SafeBoolSRSWRegister[] r =
   new SafeBoolSRSWRegister[N];
  public void write(boolean x)
  for (int j = 0; j < N; j++)
   r[j].write(x);
                                 Write each
 public boolean read() {
  int i = ThreadID.get();
                               thread's register
   return r[i].read();
                                 one at a time
 }}
```

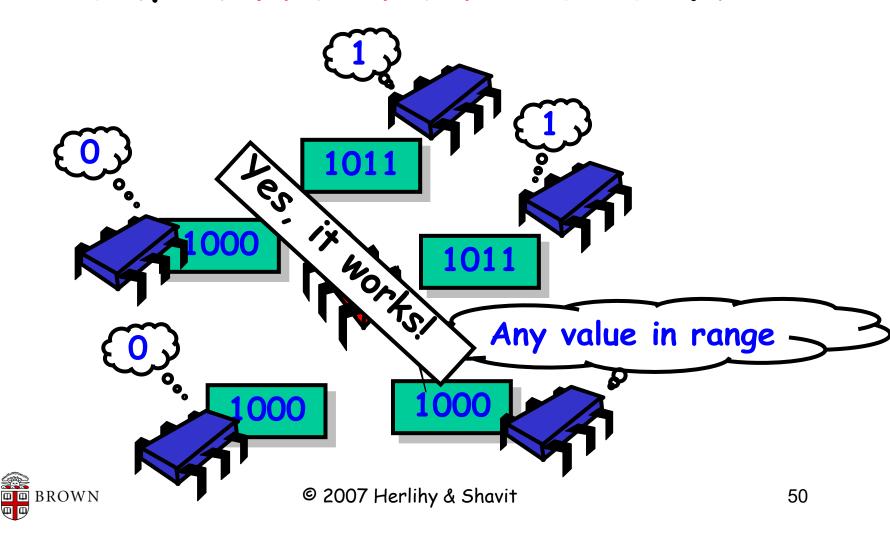
```
public class SafeBoolMRSWRegister
implements BooleanRegister {
private SafeBoolSRSWRegister[] r =
   new SafeBoolSRSWRegister[N];
  public void write(boolean x) {
   for (int j = 0; j < N; j++)
                                 read method
    r[j].write(x);
 public boolean read() {
   int i = ThreadID.get();
   return r[i].read();
```

```
public class SafeBoolMRSWRegister
 implements BooleanRegister {
private SafeBoolSRSWRegister[] r =
   new SafeBoolSRSWRegister[N];
  public void write(boolean x) {
   for (int j = 0; j < N; j++)
    r[j].write(x);
  public boolean read() {
   int i = ThreadID.get();
                                    Read my own
   return r[i].read();
                                       register
```





### Q: Safe Multi-Valued MRSW Safe Multi-Valued SRSW?



- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
- MRSW regular
- MRSW atomic

Questions?

- · MRMW atomic
- Atomic snapshot



- SRSW safe Boolean
- MRSW safe Boolean

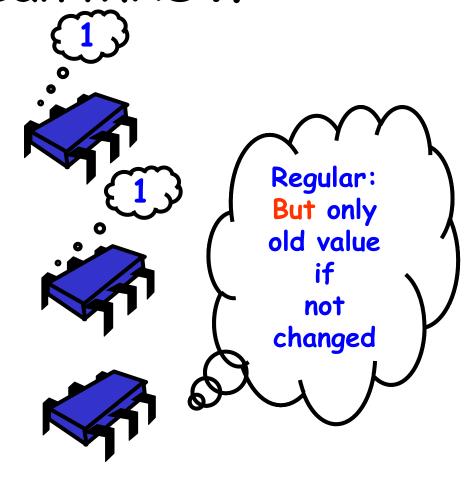


- · MRSW regular Boolean
- MRSW regular
- MRSW atomic
- MRMW atomic
- Atomic snapshot



Safe register can return 0 or 1 even if the same value is written







```
public class RegBoolMRSWRegister
 implements Register<Boolean> {
  private boolean old;
  private SafeBoolMRSWRegister value;
  public void write(boolean x) {
   if (old != x) {
   value.write(x);
   old = x;
   }}
  public boolean read() {
   return value.read();
  }}
```



```
public class RegBoolMRSWRegister
 implements Register<Boolean> {
 threadLocal boolean old;
  private SafeBoolMRSWRegister value;
  public void write(boolean x) {
   if (old != x) {
   value.write(x);
    old = x; Last bit this thread wrote
   }}
                    (OK, we're cheating here on Java syntax)
  public boolean read() {
   return value.read();
  }}
```

```
public class RegBoolMRSWRegister
implements Register<Boolean> {
 threadLocal boolean old;
 private SafeBoolMRSWRegister value;
 public void write(boolean x)
  if (old != x) {
   value.write(x);
   old = x;
  }}
 public boolean read() {
   return value.read();
                          Actual value
 }}
```



```
public class RegBoolMRSWRegister
implements Register<Boolean> {
  threadLocal boolean old;
  private SafeBoolMRSWRegister value;
 public void write(boolean x) {
  if (old != x) {
   value.write
                     Is new value different
   old = x;
                     from last value I wrote?
  }}
  public boolean read() {
   return value.read();
 }}
```

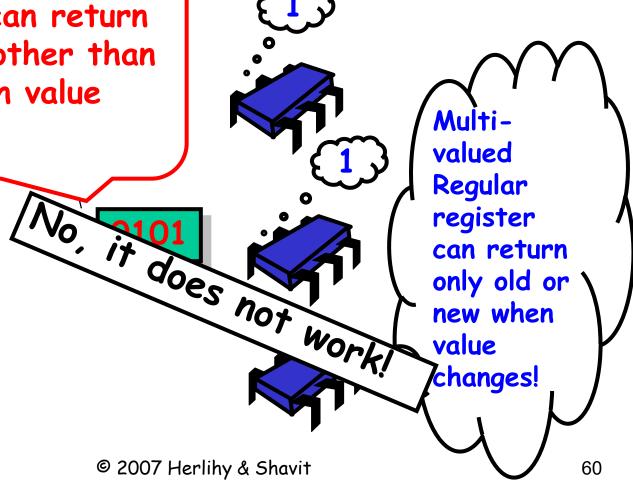
```
public class RegBoolMRSWRegister
implements Register<Boolean> {
  threadLocal boolean old;
  private SafeBoolMRSWRegister value;
  public void write(boolean x) {
  if (old != x) {
   value.write(x);
   old = x;
  public boolean read
                            If so, change it
   return value.read();
                           (otherwise don't!)
 }}
```



```
public class RegBoolMRSWRegister
implements Register<Boolean>{
  threadLocal boolean old;
  private SafeBoolMRSWRegister value;
  public void write(boolean x) {
  if (old != x) { ·Overlap? No Overlap?
   value.write(x); ✓ ·No problem
                       ·either Boolean value works
   old = x;
 public boolean read() {
   return value.read();
```

## Regular Multi-Valued MRSW to Safe Multi-Valued MRSW?

Safe register can return value in range other than old or new when value changes





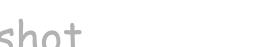
- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
- MRSW regular
- MRSW atomic
- · MRMW atomic
- Atomic snapshot





- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
- MRSW regular
- MRSW atomic
- · MRMW atomic
- Atomic snapshot





```
public class RegMRSWRegister implements Register{
  RegBoolMRSWRegister[M] bit;
  public void write(int x) {
    this.bit[x].write(true);
    for (int i=x-1; i>=0; i--)
      this.bit[i].write(false);
  }
  public int read() {
    for (int i=0; i < M; i++)
      if (this.bit[i].read())
        return i;
   }}
```



```
public class RegMRSWRegister implements Register{
 RegBoolMRSWRegister[M] bit;
 public void write(int
   this.bit[x].write(true)
   for (int i=x-1; i>=0; i--)
     this.bit[i].write(false);
                      Unary representation:
 public int read() { bit[i] means value i
   for (int i=0; i < M; i++)
     if (this.bit[i].read())
       return i;
  }}
```



```
public class RegMRSWRegisterimplements Register {
 RegBoolMRSWRegister[m] bit;
  public void write(int x) {
   this.bit[x].write(true);
    for (int 1=x-1; i>=0; 1--
      this.bit[i].write(false);
                                   Set bit x
 public int read() {
    for (int i=0; i < M; i++)
      if (this.bit[i].read())
        return i;
  }}
```



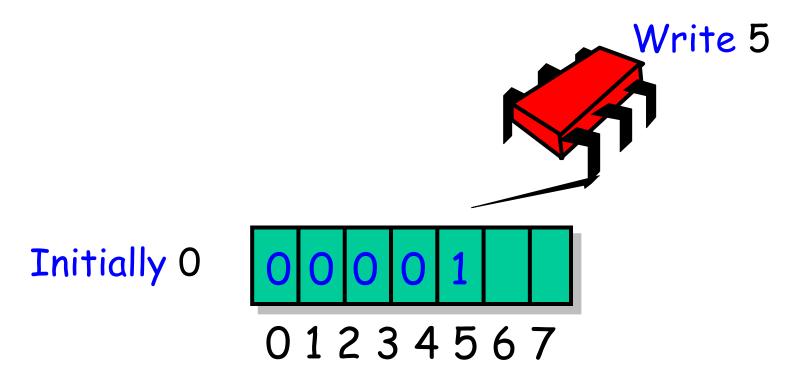
```
public class RegMRSWRegisterimplements Register {
 RegBoolMRSWRegister[m] bit;
 public void write(int x) {
    this.bit[x].write(true);
    for (int i=x-1; i>=0; i--)
     this.bit[i].write(false);
                                 Clear bits
  public int read() {
   for (int i=0; i < M; i++)
                                from higher
     if (this.bit[i].read())
                                  to lower
        return i;
  }}
```



```
public class RegMRSWRegisterimplements Register {
 RegBoolMRSWRegister[m] bit;
                              Scan from lower
 public void write(int x) {
   this.bit[x].write(true); to higher & return
   for (int i=x-1; i>=0; i--)
                                first bit set
     this.bit[i].write(false);
   for (int i=0; i < M; i++)
     if (this.bit[i].read())
       return i;
```

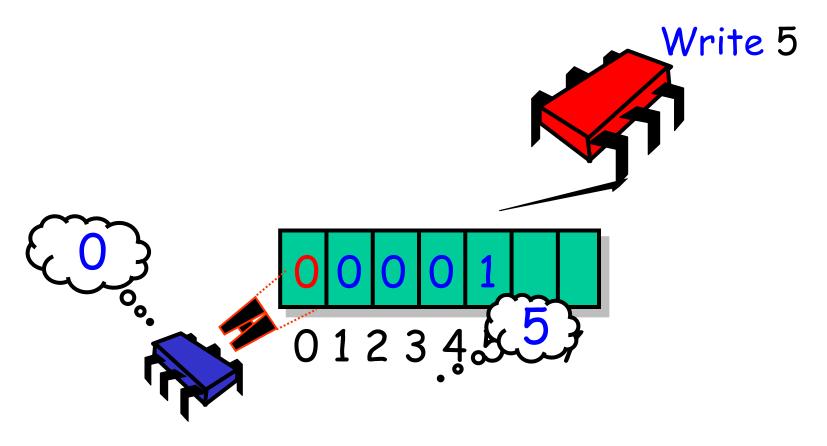


### Writing M-Valued





### Writing M-Valued





- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
- MRSW regular
- · MRSW atomic
- · MRMW atomic
- Atomic snapshot





- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
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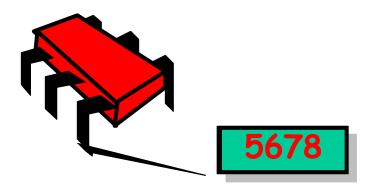
### Road Map (Slight Detour)

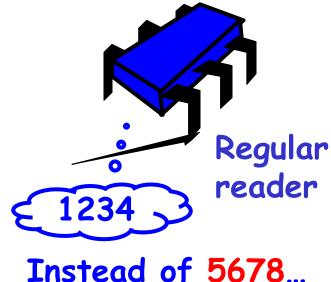
- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
- · MRSW regular SRSW Atomic
- MRSW atomic
- MRMW atomic
- Atomic snapshot



# SRSW Atomic From SRSW Regular

Regular writer



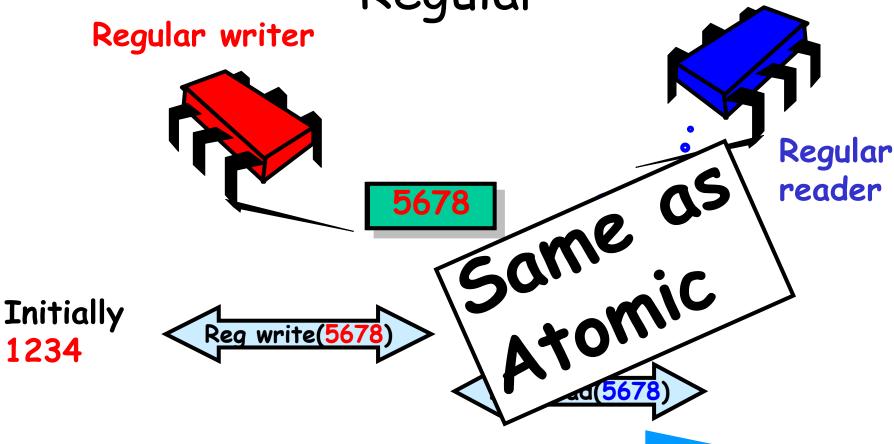


Instead of 56/8.

When is this a problem?



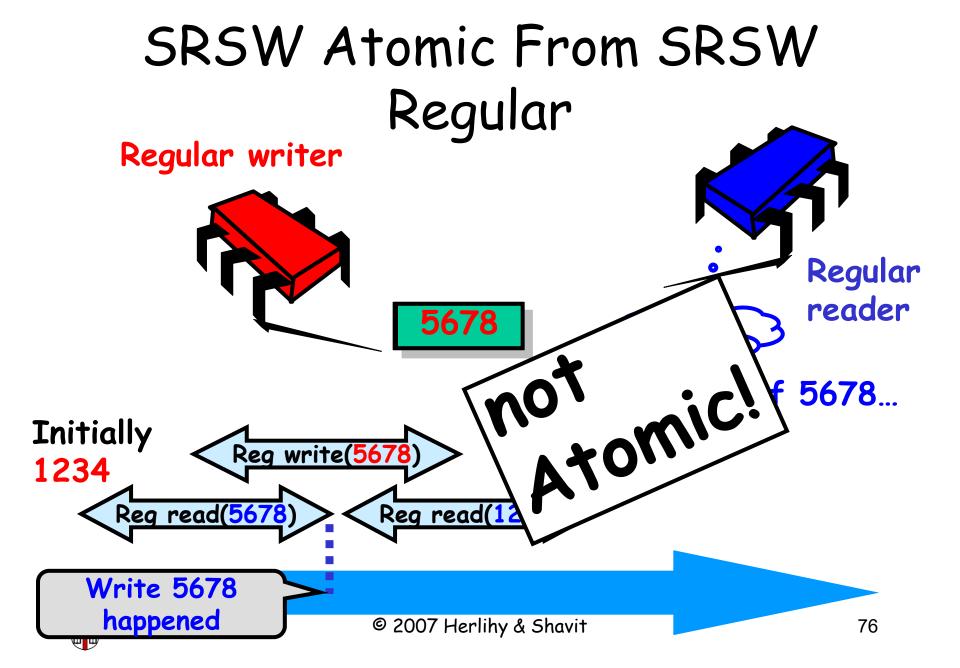
SRSW Atomic From SRSW Regular



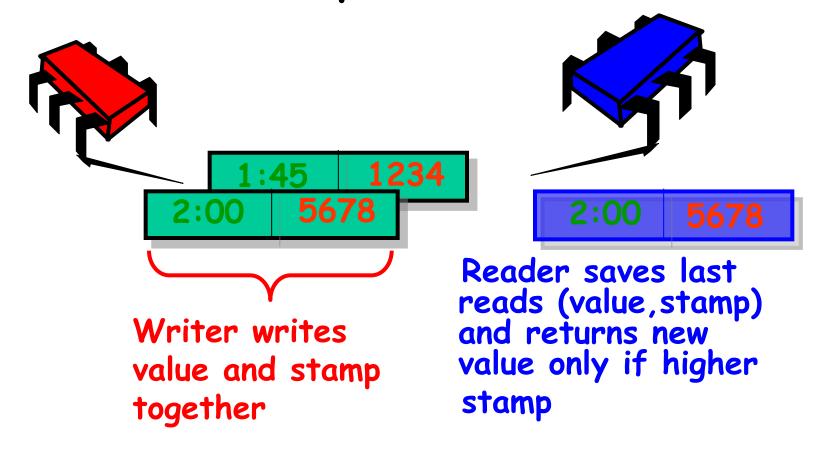




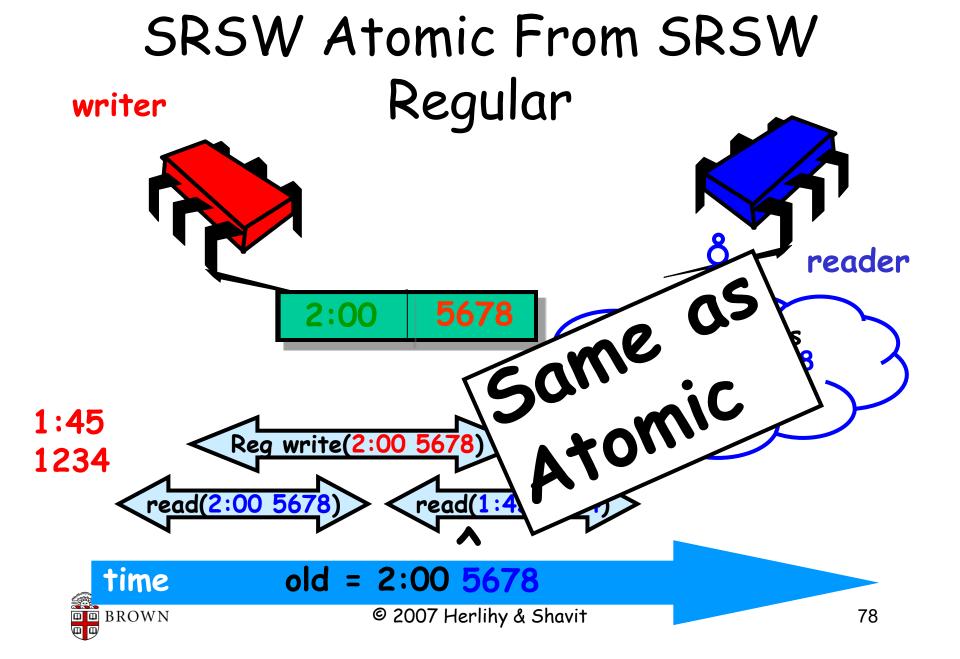
SRSW Atomic From SRSW Regular Regular writer Regular Same as Atomic reader 5678 678... Initially Reg write(5678) 1234 Reg read(123 time © 2007 Herlihy & Shavit 75 BROWN



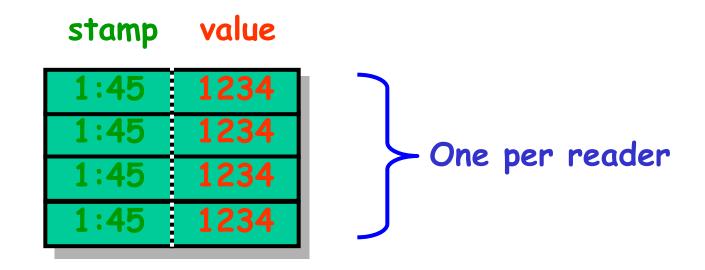
## Timestamped Values





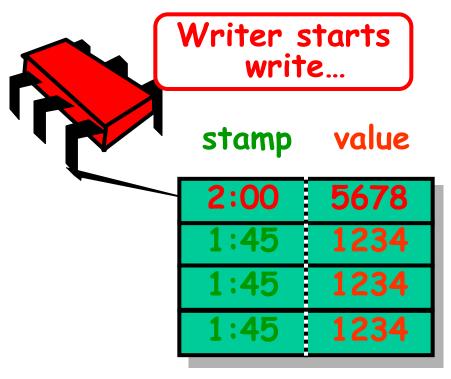


# Atomic Single Reader to Atomic Multi-Reader

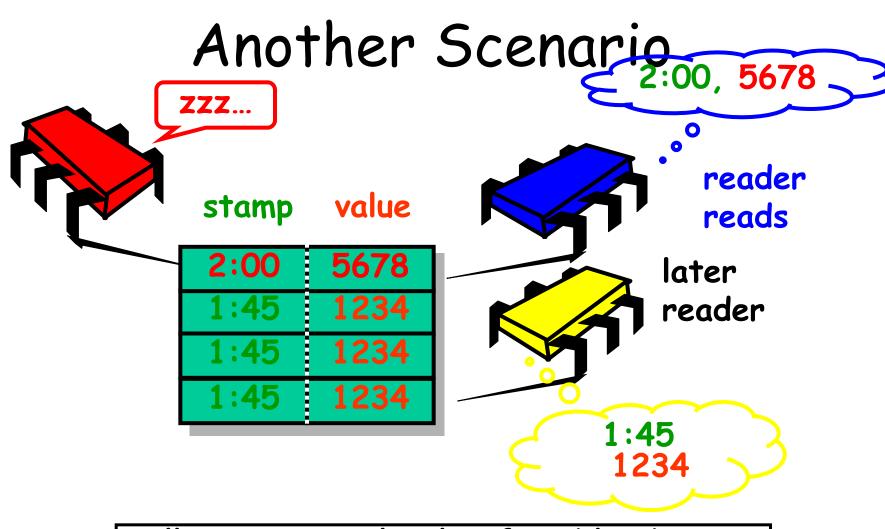




#### Another Scenario







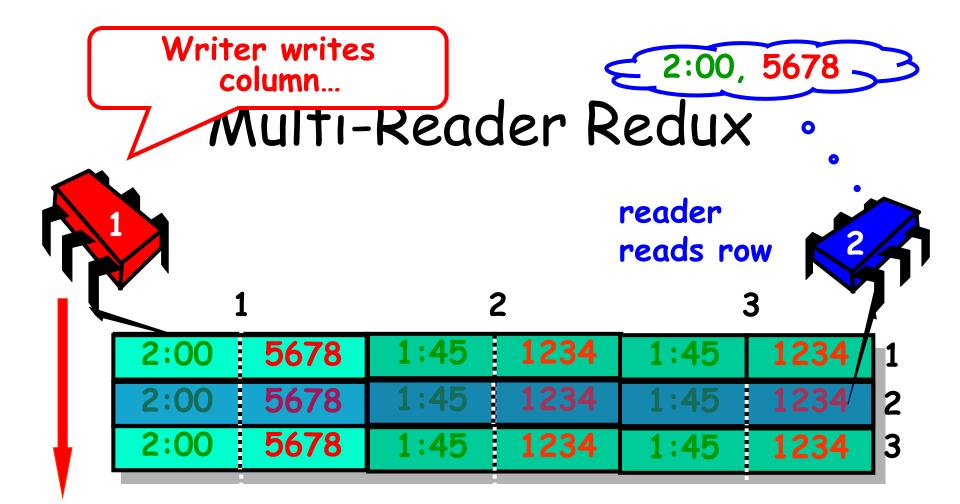


Yellow was completely after blue but read earlier value...not linearizable!

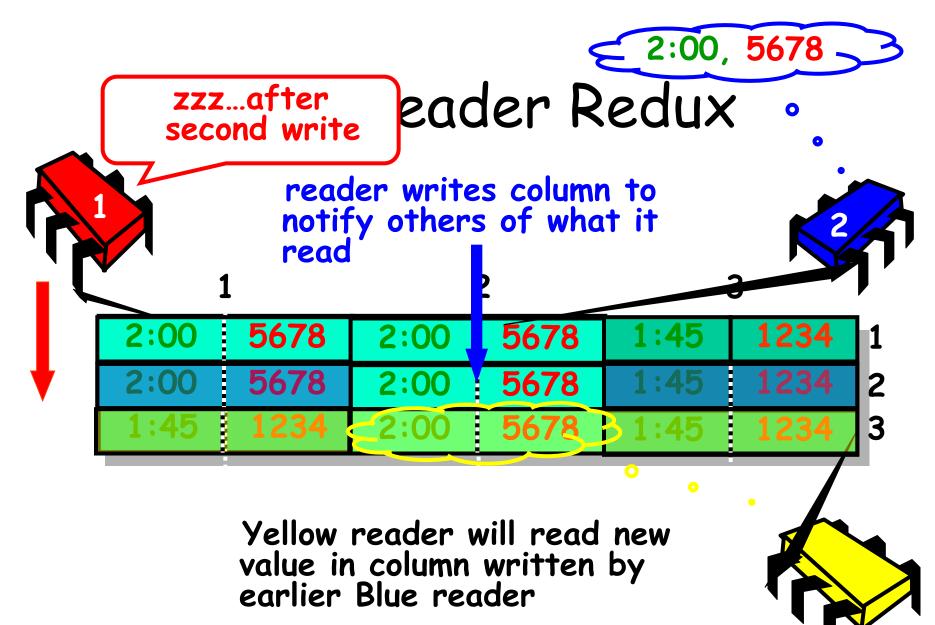
#### Multi-Reader Redux

#### One per thread 1234 1234 1:45 1234 1:45 1234 1:45 1234 1234 1:45 1234 1:45 1:45 1234 1234 1:45



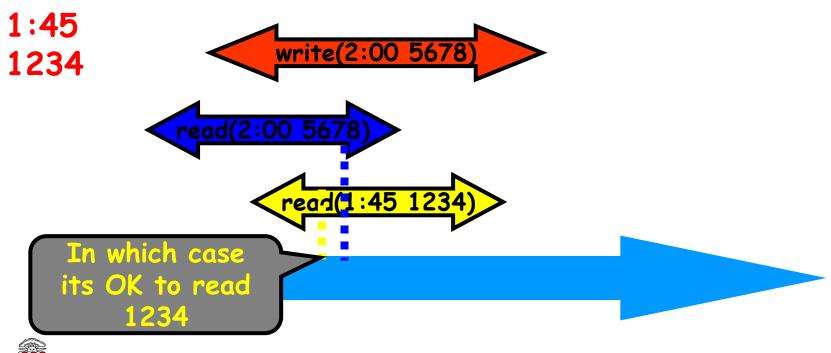






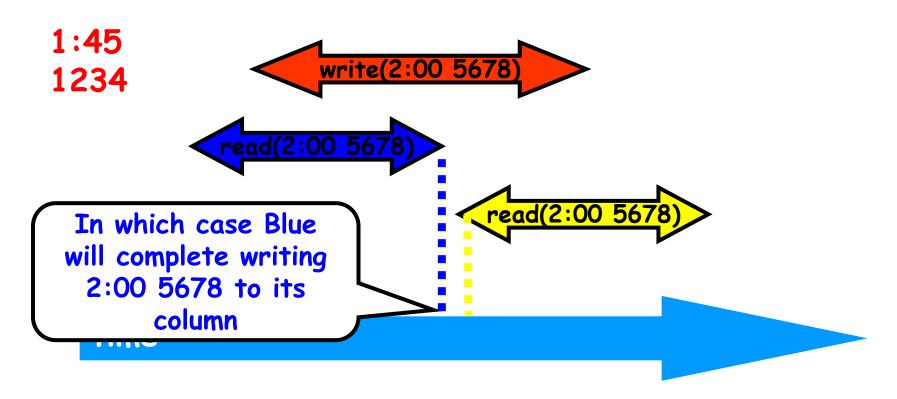


# Can't Yellow Miss Blue's Update? ... Only if Readers Overlap...



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# Bad Case Only When Readers Don't Overlap





# Road Map

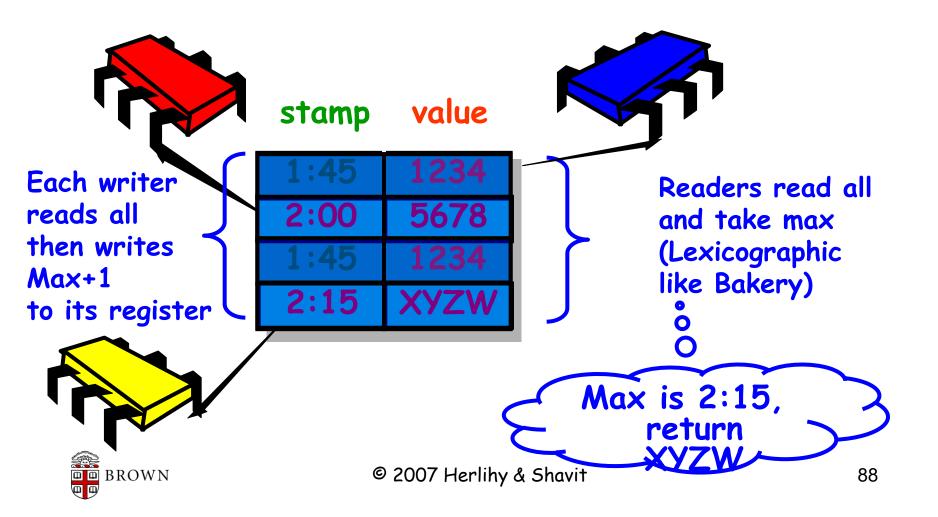
- SRSW safe Boolean
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- MRSW regular
- MRSW atomic
- MRMW atomic



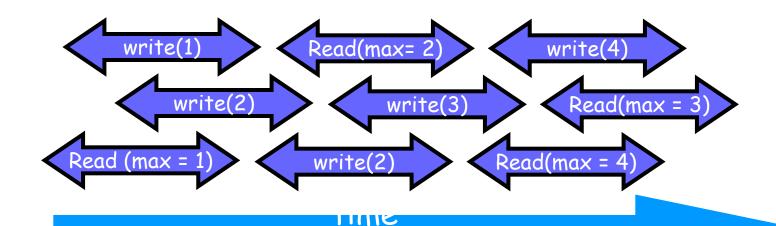
Atomic snapshot



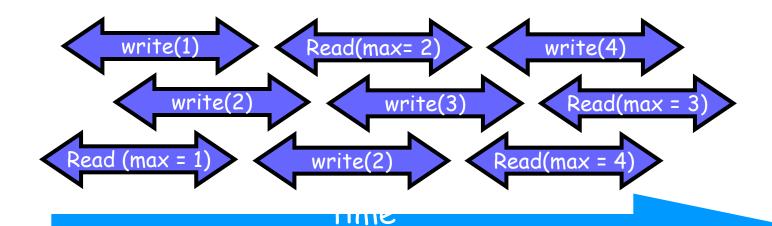
# Multi-Writer Atomic From Multi-Reader Atomic



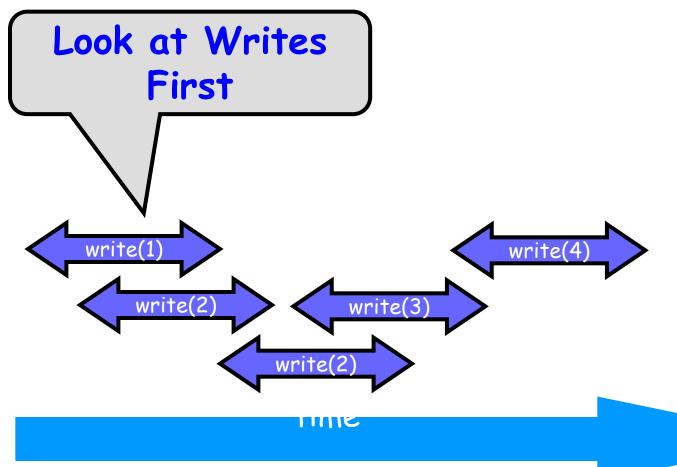
### Atomic Execution Means its Linearizable



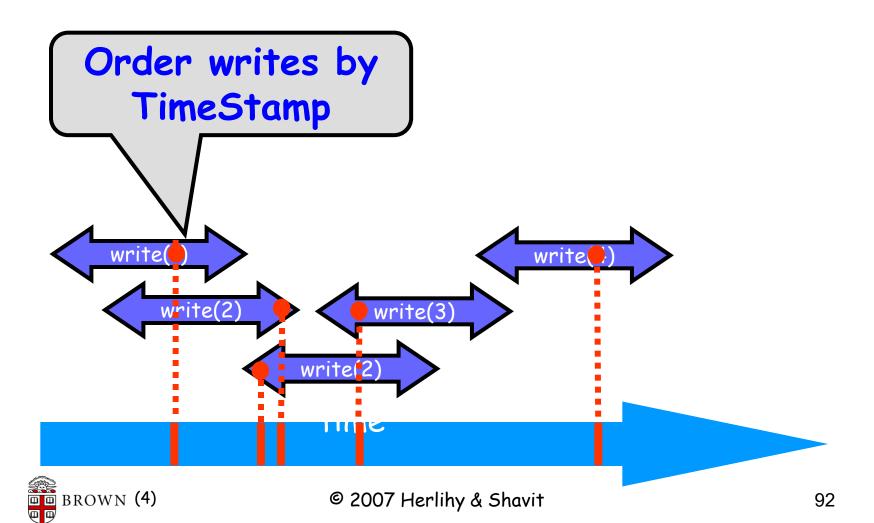


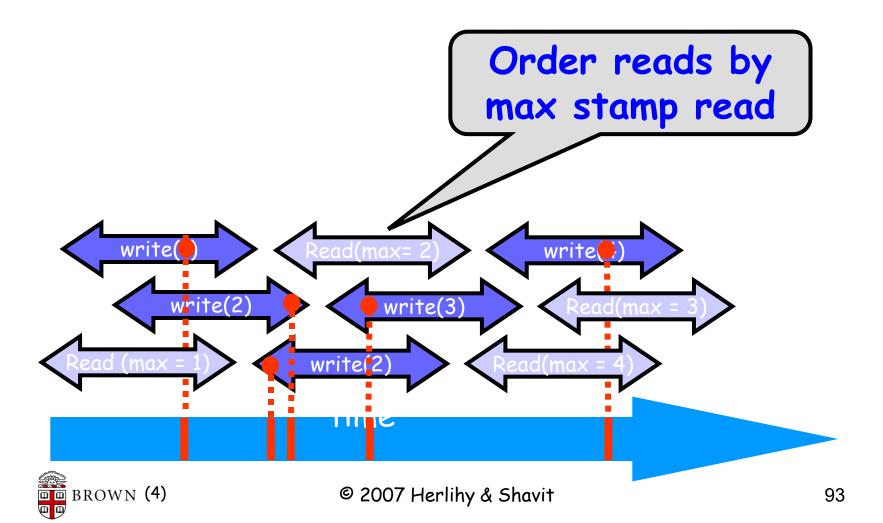


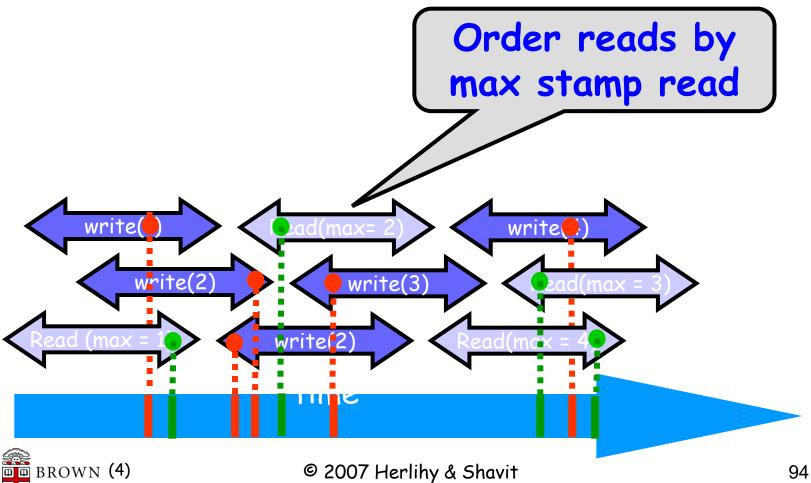




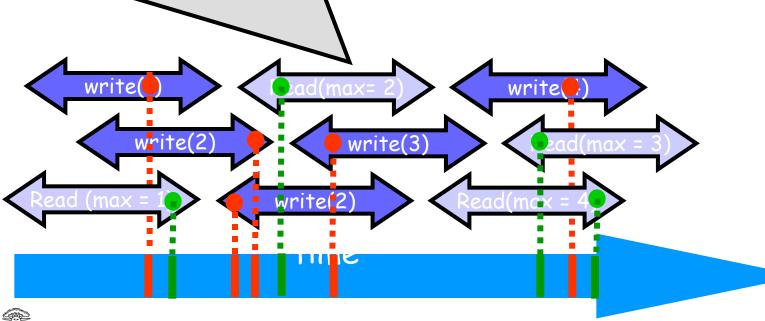








The linearization point depends on the execution (not a line in the code)!



🗓 BROWN **(4)** 

# Road Map

- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
- MRSW regular
- MRSW atomic
- MRMW atomic



Questions?

Atomic snapshot



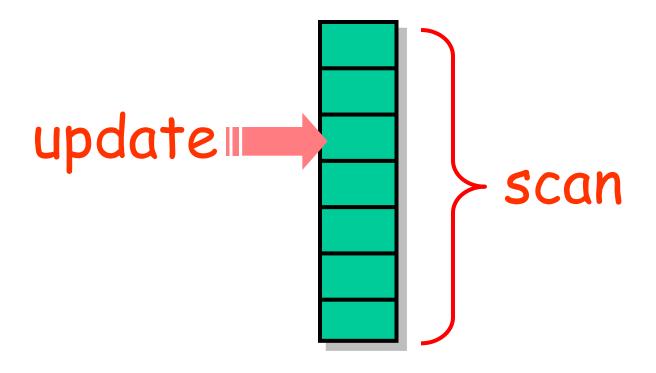
# Road Map

- SRSW safe Boolean
- MRSW safe Boolean
- MRSW regular Boolean
- MRSW regular
- MRSW atomic
- MRMW atomic
- Atomic snapshot





# Atomic Snapshot





# Atomic Snapshot

- Array of SWMR atomic registers
- · Take instantaneous snapshot of all
- · Generalizes to MRMW registers ...



# Snapshot Interface

```
public interface Snapshot {
  public int update(int v);
  public int[] scan();
}
```



# Snapshot Interface

Thread i writes v to its register

```
public interface Snapshot {
    public int update(int v);
    public int[] scan();
}
```



# Snapshot Interface

Instantaneous snapshot of all theads' registers

```
public interface Snapshot {
  public int update(int v);
  public int[] scan();
}
```



# Atomic Snapshot

- Collect
  - Read values one at a time
- Problem
  - Incompatible concurrent collects
  - Result not linearizable



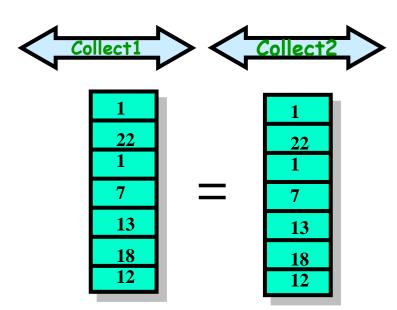
#### Clean Collects

- Clean Collect
  - Collect during which nothing changed
  - Can we make it happen?
  - Can we detect it?



# Simple Snapshot

- Put increasing labels on each entry
- Collect twice
- If both agree,
  - We're done
- Otherwise,
  - Try again





# Simple Snapshot: Update

```
public class SimpleSnapshot implements Snapshot {
  private AtomicMRSWRegister[] register;
  public void update(int value) {
    int i = Thread.myIndex();
      LabeledValue oldValue = register[i].read();
    LabeledValue newValue =
     new LabeledValue(oldValue.label+1, value);
    register[i].write(newValue);
```



# Simple Snapshot: Update

```
public class SimpleSnapshot implements Snapshot {
 private AtomicMRSWRegister[] register;
 public woid update(int value) {
   int i = Thread.myIndex();
    LabeledValue oldValue = register[i].read();
    LabeledValue newValue =
    new Labeled (alue(oldValue.label+1, value);
    register[i].write(newValue);
```

One single-writer register per thread



# Simple Snapshot: Update

```
public class SimpleSnapshot implements Snapshot {
  private AtomicMRSWRegister[] register;
  public void update(int value) {
    int i = Thread.myIndex();
    LabeledValue oldValue = register[i].read();
    LabeledValue newValue =
    new LabeledValue(oldValue.label+1, value);
    register[i].write(newValue);
```

Write each time with higher label



# Simple Snapshot: Collect

```
private LabeledValue[] collect() {
  LabeledValue[] copy =
   new LabeledValue[n];
  for (int j = 0; j < n; j++)
   copy[j] = this.register[j].read();
  return copy;
}</pre>
```



# Simple Snapshot

```
private LabeledValue[] collect() {
  LabeledValue[] copy =
    new LabeledValue[n];

for (int j = 0; j < n; j++)
  copy[j] = this.register[j].read();
  return copy;
}</pre>
```

Just read each register into array



```
public int[] scan() {
 LabeledValue[] oldCopy, newCopy;
oldCopy = collect();
 collect: while (true) {
  newCopy = collect();
  if (!equals(oldCopy, newCopy)) {
    oldCopy = newCopy;
    continue collect;
 }}
  return getValues(newCopy);
}}}
```

```
public int[] scan() {
 LabeledValue[] oldcopy, newcopy; Collect once
oldCopy = collect();
 collect: while (true) {
  newCopy = collect();
  if (!equals(oldCopy, newCopy)) {
    oldCopy = newCopy;
    continue collect;
 }}
  return getValues(newCopy);
}}}
```

```
public int[] scan() {
 LabeledValue[] oldCopy, newCopy: Collect once
oldCopy = collect();

    Collect twice

 newCopy = collect();
 if (!equals(oldCopy, newCopy)) {
    oldCopy = newCopy;
    continue collect;
 }}
  return getValues(newCopy);
}}}
```

```
public int[] scan() {
                                Collect once
LabeledValue[] oldCopy, newCopy;
oldCopy = collect();
                               Collect twice
 newCopy = collect();
 if (!equals(oldCopy, newCopy))
    oldCopy = newCopy;
    continue collect;
                               On mismatch,
  return getValues(newCopy);
                                  try again
}}}
```



```
public int[] scan() {
                                Collect once
LabeledValue[] oldCopy, newCopy;
oldCopy = collect();
                              Collect twice
 newCopy = collect();
 if (!equals(oldCopy, newCopy)) {
  oldCopy = newCopy;
  continue collect;
                         On match, return
                                values
 return getValues(newCopy);
```



# Simple Snapshot

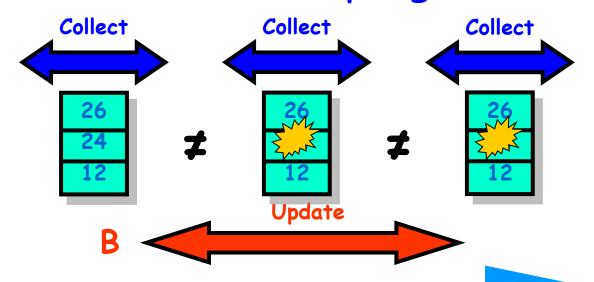
- Linearizable
- Update is wait-free
  - No unbounded loops
- But Scan can starve
  - If interrupted by concurrent update



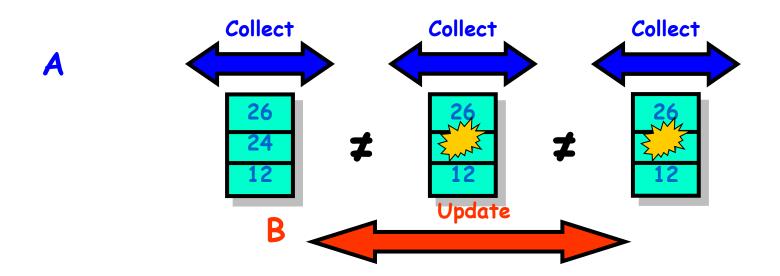
- Add a scan before every update
- Write resulting snapshot together with update value
- If scan is continuously interrupted by updates, scan can take the update's snapshot



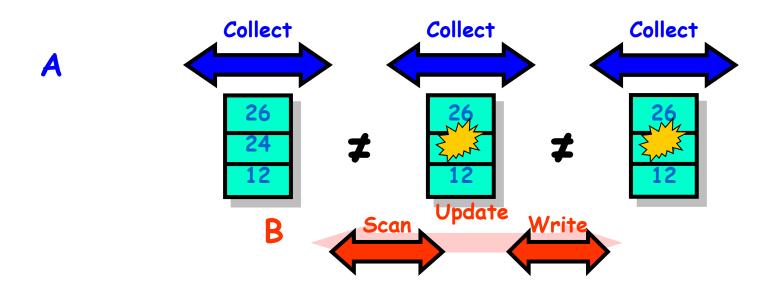
If A's scan observes that B moved twice, then B completed an update while A's scan was in progress



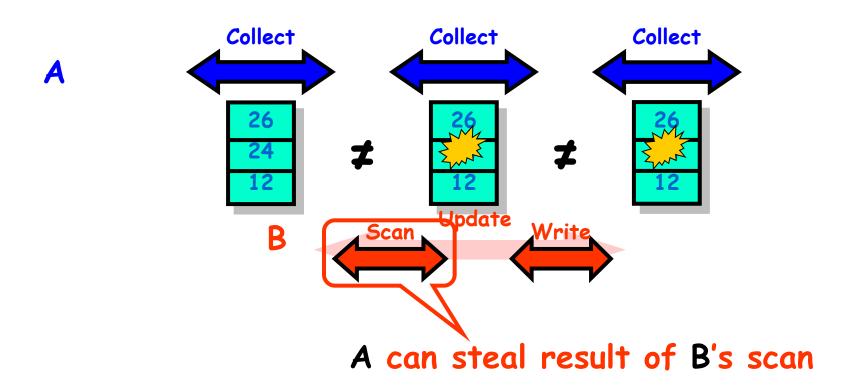






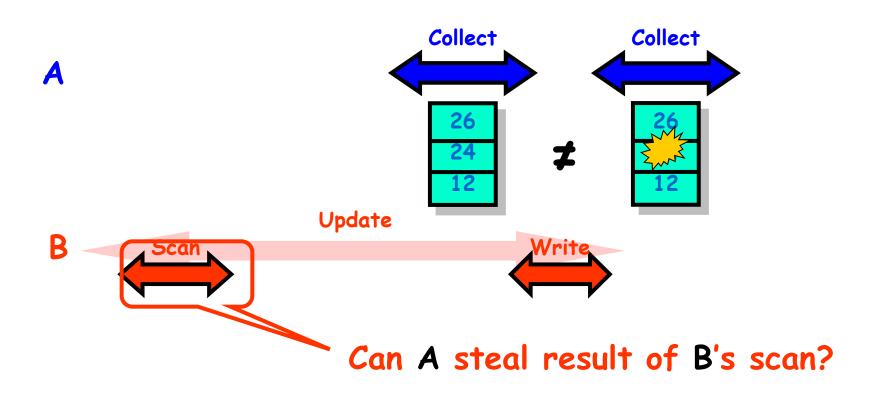






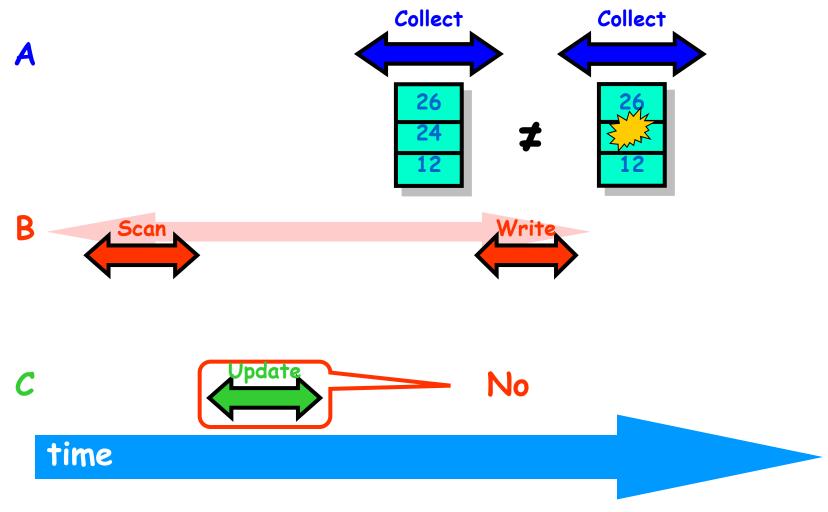


# Once is not Enough



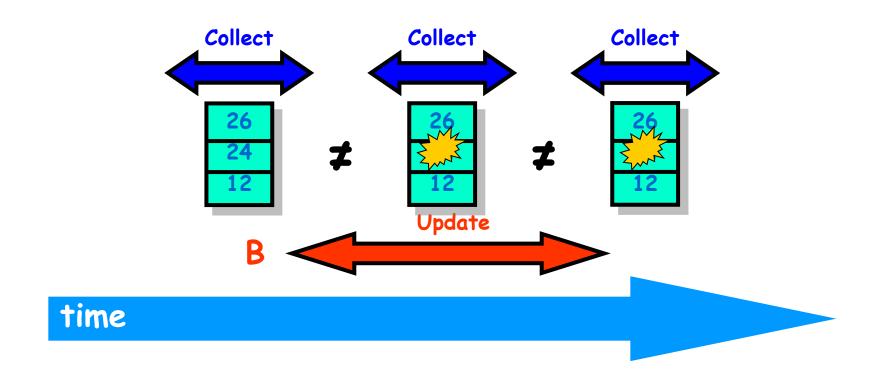


# Once is not Enough





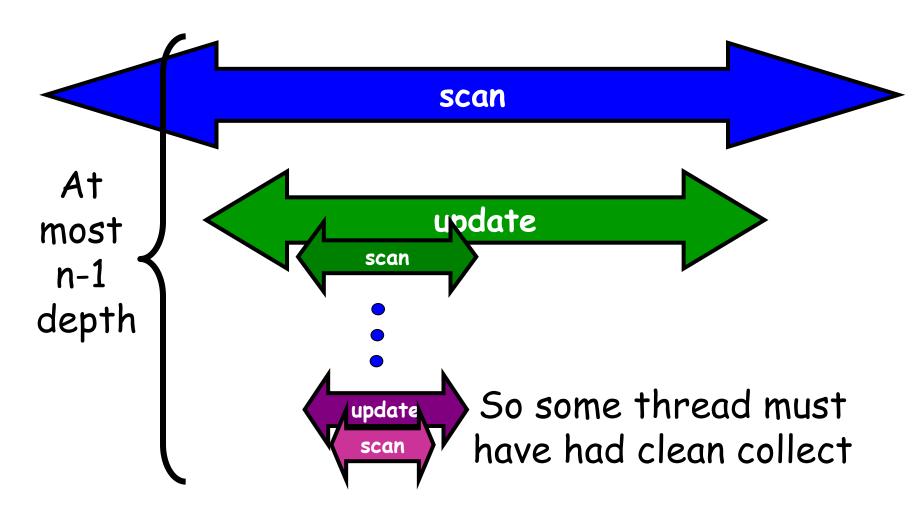
## Someone Must Move Twice



#### Why are scans wait-free?



## Wait-free





```
public class SnapValue {
  public int label;
  public int value;
  public int[] snap;
}
```



```
public class SnapValue {
  public int label;
  public int value;
  public int[] snap;
}
Counter incremented
with each snapshot
```



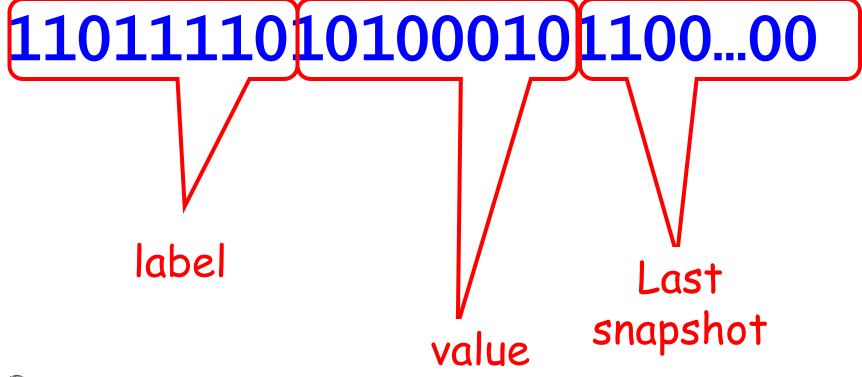
```
public class SnapValue {
  public int label;
  public int value;
  public int[] snap;
}
Actual value
```



```
public class SnapValue {
  public int label;
  public int value;

public int[] snap;
}
most recent snapshot
```







# Wait-free Update

```
public void update(int value) {
 int i = Thread.myIndex();
 int[] snap = this.scan();
 SnapValue oldValue = r[i].read();
 SnapValue newValue =
  new SnapValue(oldValue.label+1,
                value, snap);
 r[i].write(newValue);
```



```
public void update(int value) {
int i = Thread.myIndex(); Take scan
int[] snap = this.scan();
SnapValue oldValue = r[i].read();
SnapValue newValue =
 new SnapValue(oldValue.label+1,
                value, snap);
 r[i].write(newValue);
```



```
public void update(int value) {
int i = Thread.myIndex(); Take Scan
int[] snap = this.scan();
SnapValue oldValue = r[i].read();
SnapValue newValue =
  new SnapValue(oldValue.label+1,
                value, snap);
 r[i].write(newValue):
                 Label value with scan
```



```
public int[] scan() {
 SnapValue[] oldCopy, newCopy;
 boolean[] moved = new boolean[n];
 oldCopy = collect();
 collect: while (true) {
 newCopy = collect();
 for (int j = 0; j < n; j++) {
   if (oldCopy[j].label != newCopy[j].label) {
 }}
  return getValues(newCopy);
}}}
```



```
public int[] scan() {
 SnapValue[] oldCopy, newCopy;
 boolean[] moved = new boolean[n];
  oldCopy = collect();
 collect: while (true)
 newCopy = collect();
 for (int j = 0; j < n; j+
   if (oldCopy[j].label != newCopy[j].label) {
                  Keep track of who moved
 }}
  return getValues(newCopy);
}}}
```



```
public int[] scan() {
 SnapValue[] oldCopy, newCopy;
  boolean[] moved = new boolean[n];
 oldCopy = collect();
 collect: while (true) {
 newCopy = collect();
 for (int j = 0, j < N; j++) {
   if (oldCopy[j].label
                        != newCopy[j].label) {
 }}
  return getValues(newCopy);
}}}
                Repeated double collect
```



```
public int[] scan() {
 SnapValue[] oldCopy, newCopy;
 boolean[] moved = new boolean[n];
 oldCopy = collect();
 collect: while (true) {
 newCopy = collect();
  for (int j = 0; j < n; j++) {
   if (oldCopy[j].label != newCopy[j].label) {
}}}
              If mismatch detected...lets
```



### Mismatch Detected

```
if (oldCopy[j].label != newCopy[j].label) {
  if (moved[j]) { // second move
    return oldCopy[j].snap;
   } else {
   moved[j] = true;
    oldCopy = newCopy;
    continue collect;
  }}}
  return getValues(newCopy);
}}}
```



## Mismatch Detected

```
if (oldCopy[j].label != newCopy[j].label) {
   if (moved[j]) {
    return oldCopy[j].snap;
   } else {
    moved[j] = true;
    oldCopy = newCopy;
    continue collect; If thread moved twice,
                       just steal its snapshot
  }}}
  return getValues(newCopy);
}}}
```



## Mismatch Detected

```
if (oldCopy[j].label != newCopy[j].label) {
  if (moved[j]) { // second move
    return oldCopy[j].snap;
  } else {
   moved[j] = true;
                              Remember that
   oldCopy = newCopy;
                              thread moved
    continue collect;
  return getValues(newCopy);
}}}
```



## Observations

- Uses unbounded counters
  - can be replaced with 2 bits
- Assumes SWMR registers
  - for labels
  - can be extended to MRMW



# Summary

- We saw we could implement MRMW multi valued snapshot objects
- From SRSW binary safe registers (simple flipflops)
- But what is the next step to attempt with read-write registers?

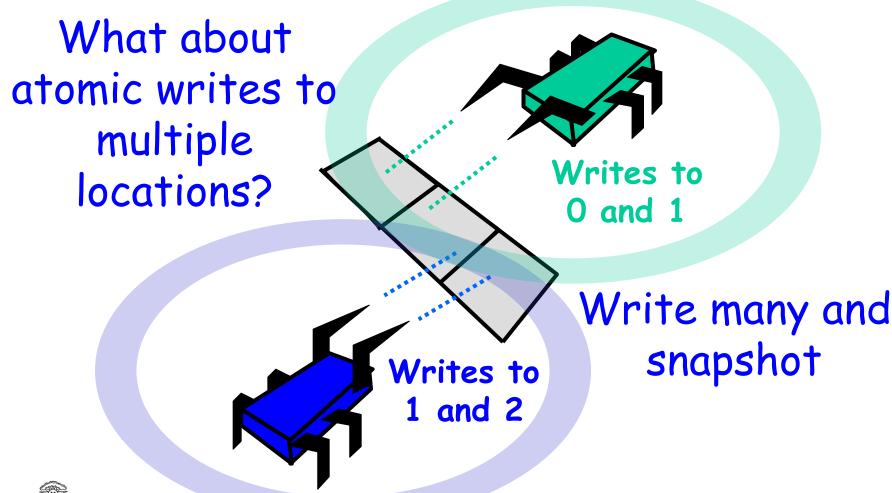


# Grand Challenge

- Snapshot means
  - Write any one array element
  - Read multiple array elements

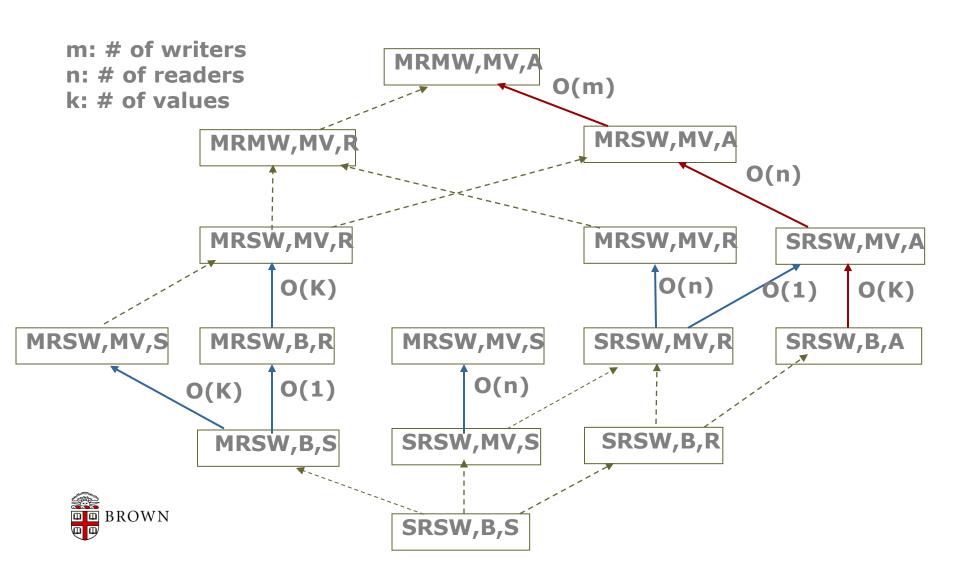


# Grand Challenge



**BROWN** 

### Registers: Hasse Diagram





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