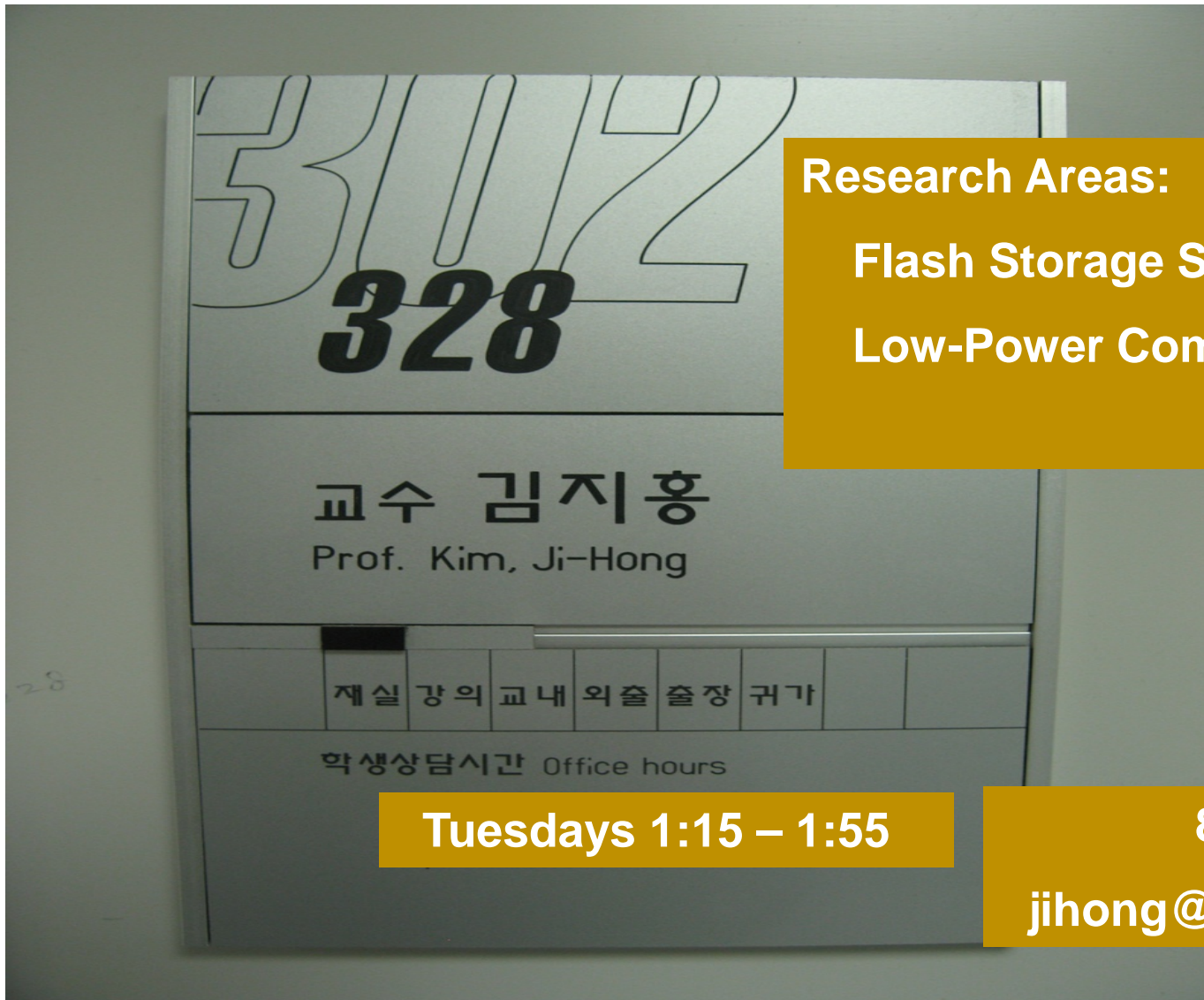

4190.763(001)

내장형시스템특강

Flash-Aware Computing

Course Information

Instructor



Research Areas:

Flash Storage Systems

Low-Power Computing

Tuesdays 1:15 – 1:55

880-8792

jihong@davinci.snu.ac.kr

Today's Lecture

- **Course Overview & Information**

Our Target: Flash Storage

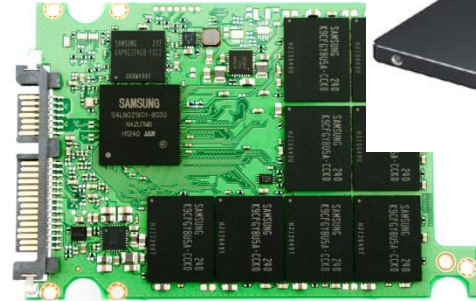
- Rapid Improvements in capacity/performance/price of NAND flash memory



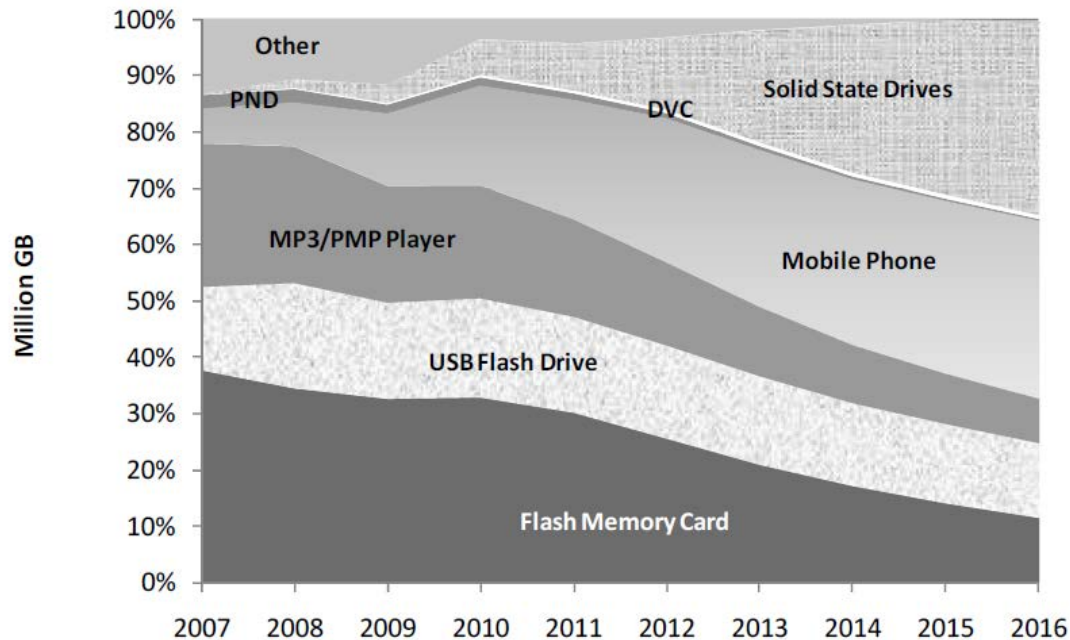
Performance

Price

Capacity

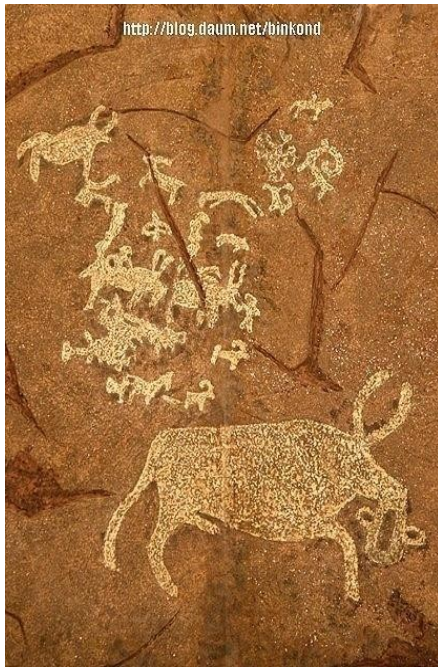


NAND Flash Applications Demand



NAND Flash Memory

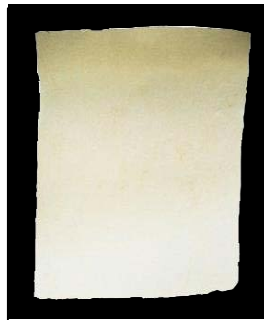
- a **non-volatile computer storage** chip that can be electrically erased and reprogrammed



암각화 선사시대



木簡, 竹簡 (중국, 秦시대)



종이 (105년, 채운, 後漢)



Magnetic (1980~)

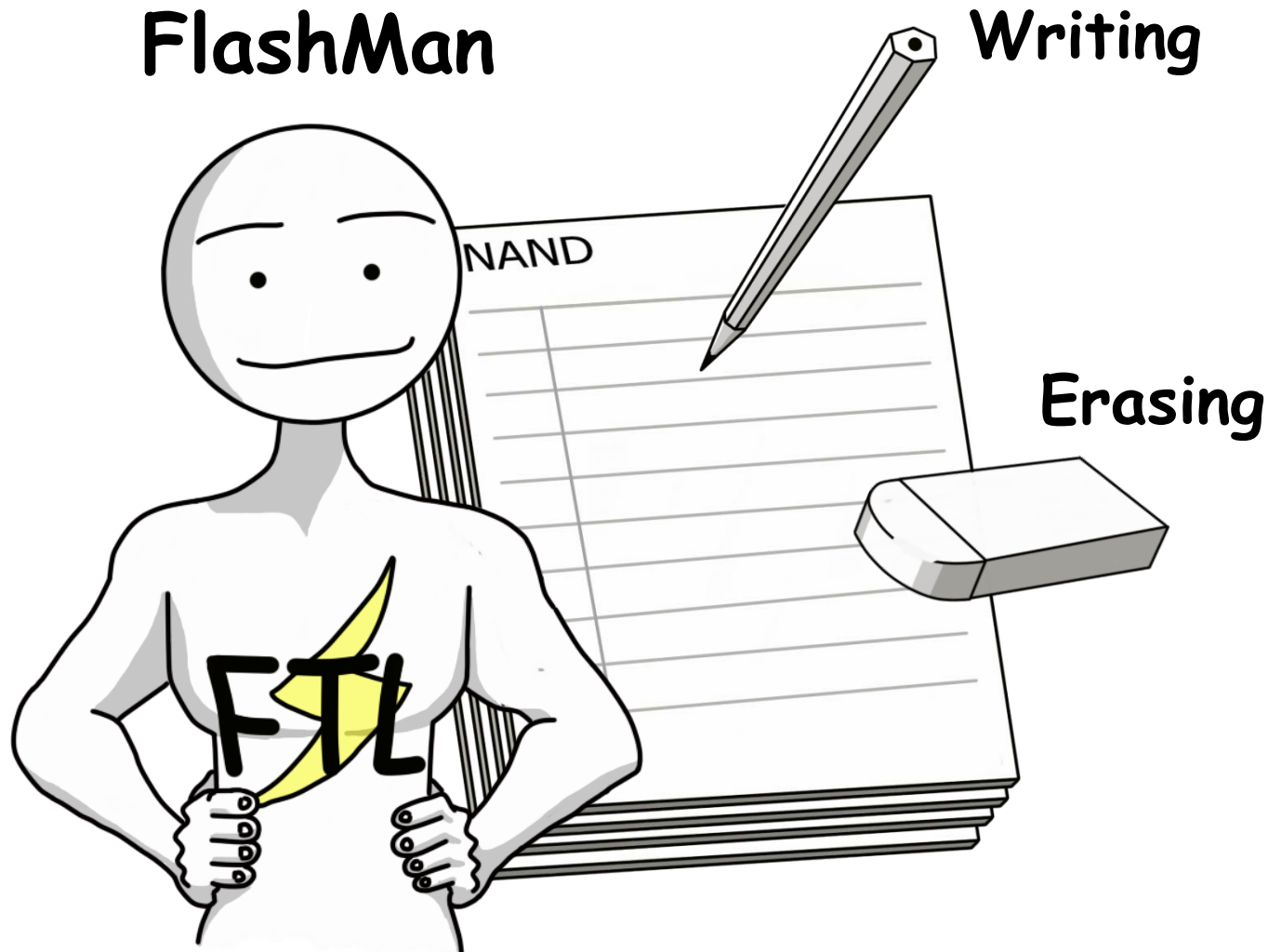


Optical Disk (1990~)



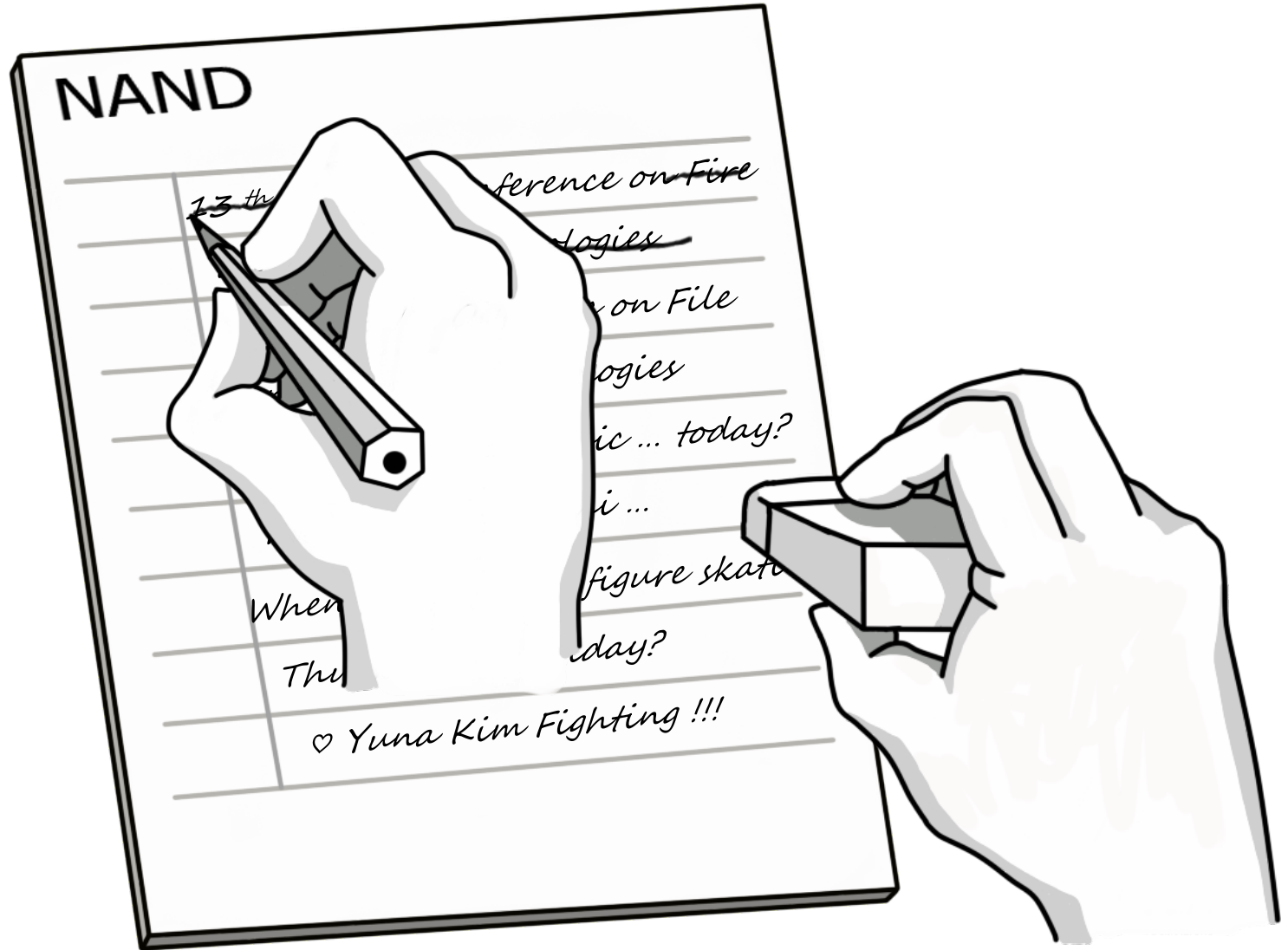
Silicon (2002 ~)

NAND Flash Memory is like Sheets of Paper



Illustrated by Jisung Park, Seoul National University

Writing Letters and Erasing Paper



Flash-Aware Computing (FAC)

- **Cross-layer optimizations**
- **Two directions**
 - **Device-Aware** System Design
 - **System-Aware** Device Design

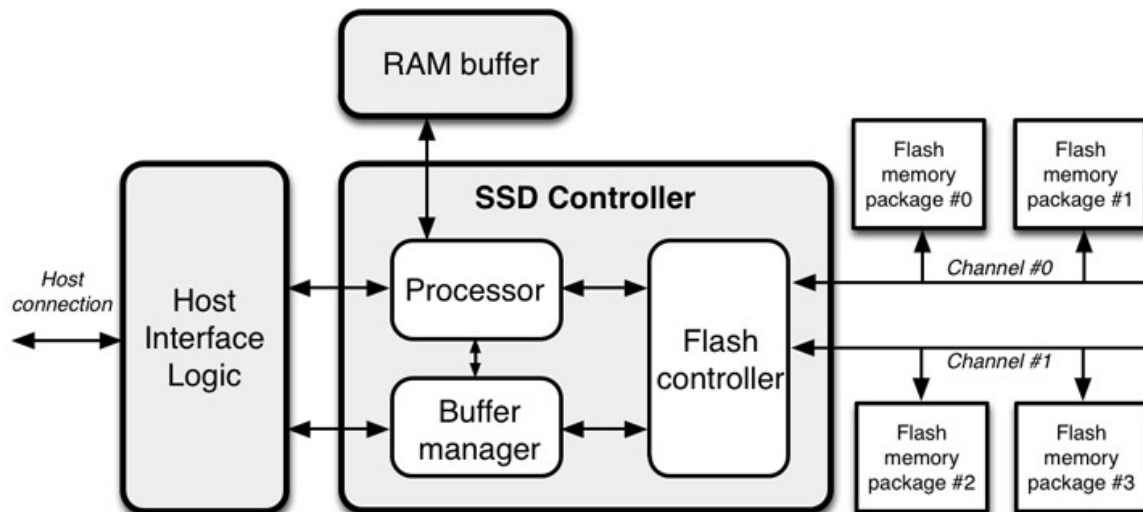
Course Overview

- **Part 1: Lectures on key components of flash storage**
- **Part 2: Paper study on recent topics in flash storage systems**
- **No prior knowledge/exposure to Flash required.**

Part 1: Key Components of SSD

- NAND Flash Basics/(ECC?)
- Flash Translation Layers
- SSD Architecture
- Flash-Aware OS/File System Issues
- Host/App-Managed Flash Issues

Architecture of a solid-state drive



Course Prerequisite

At Least:

Undergraduate-level Familiarity with

Computer Architecture &

Operating Systems

Evaluation (Tentative)

Assignments

30%

**Final Exam
(Closed)**

40%

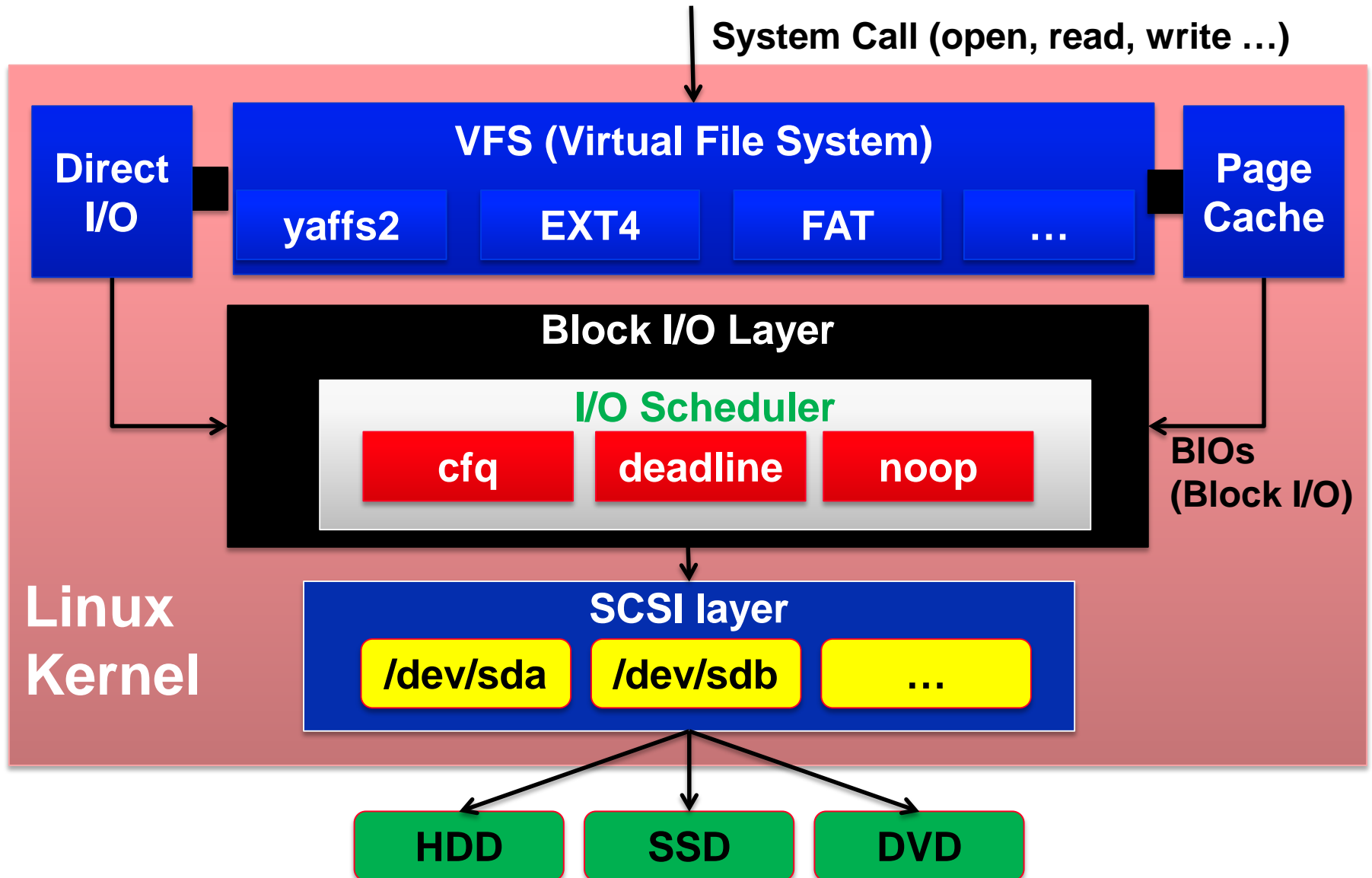
Group Activity

30%

Attendance

-5%

Linux I/O Stack Diagram



Any Questions??